Formalization of Conflict Analysis of Programs with Procedures, Thread Creation, and Monitors in Isabelle/HOL

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Abstract

In this work we formally verify the soundness and precision of a static program analysis that detects conflicts (e.g. data races) in programs with procedures, thread creation and monitors with the Isabelle theorem prover. As common in static program analysis, our program model abstracts guarded branching by nondeterministic branching, but completely interprets the call-/return behavior of procedures, synchronization by monitors, and thread creation. The analysis is based on the observation that all conflicts already occur in a class of particularly restricted schedules. These restricted schedules are suited to constraint-system-based program analysis.

The formalization is based upon a flowgraph-based program model with an operational semantics as reference point.

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1 Introduction

Conflicts are a common programming error in parallel programs. A conflict occurs if the same resource is accessed simultaneously by more than one process. Given a program π and two sets of control points U and V, the analysis problem is to decide whether there is an execution of π that simultaneously reaches one control point from U and one from V.

In this work, we use a flowgraph-based program model that extends a previously studied model [6] by reentrant monitors. In our model, programs can call recursive procedures, dynamically create new threads and synchronize via reentrant monitors. As usual in static program analysis, our program model abstracts away guarded branching by nondeterministic choice. We use an operational semantics as reference point for the correctness proofs. It models parallel execution by interleaving, i.e. just one thread is executed at any time and context switches may occur after every step. The next step is nondeterministically selected from all threads ready for execution. The analysis is based on a constraint system generated from the flowgraph. From its least solution, one can decide whether control points from U and V are simultaneously reachable or not.

It is notoriously hard to analyze concurrent programs with constraint systems because of the arbitrary fine-grained interleaving. The key idea behind our analysis is to use a restricted scheduling: While the interleaving semantics can switch the context after each step, the restricted scheduling just allows context switches at certain points of a thread's execution. We can show that each conflict is also reachable under this restricted scheduling. The restricted schedules can be easily analyzed with constraint systems as most of the complexity generated by arbitrary interleaving does no longer occur due to the restrictions. The remaining concurrency effects can be smoothly handled by using the concept of acquisition histories [5].

Related Work In [6] we present a constraint-system-based analysis for programs with thread creation and procedures but without monitors. The abstraction from synchronization is common in this line of research: There are automata-based techniques [1, 2, 3] as well as constraint-system-based techniques [7, 6] to analyze programs with procedures and either parallel calls or thread creation, but without any synchronization. In [5, 4] analysis techniques for interprocedural parallel programs with a fixed number of initial threads and nested locks are presented. These nested locks are not syntactically bound to the program structure, but assumed to be well-nested, that is any unlock statement is required to release the lock that was acquired last by the thread. Moreover, there is no support for reentrant

locks¹. We use monitors instead of locks. Monitors are syntactically bound to the program structure and thus well-nestedness is guaranteed statically. Additionally we directly support reentrant monitors. Our model cannot simulate well-nested locks where a lock statement and its corresponding unlock statement may be in different procedures (as in [5, 4]). As common programming languages like Java also use reentrant monitors rather than locks, we believe our model to be useful as well.

Document structure This document contains a commented formalization of these ideas as a collection of Isabelle/HOL theories. A more abstract description is in preparation. This document starts with formalization monitor consistent interleaving (Section 2) and acquisition histories (Section 3). Labeled transition systems are formalized in Section 4, and Section 5 defines the notion of interleaving semantics. Flowgraphs are defined in Section 6, and Section 7 describes their operational semantics. Section 8 contains the formalization of the restricted interleaving and Section 9 contains the constraint systems. Finally, the main result of this development – the correctness of the constraint systems w.r.t. to the operational semantics – is briefly stated in Section 10.

2 Monitor Consistent Interleaving

theory ConsInterleave imports Interleave Misc begin

The monitor consistent interleaving operator is defined on two lists of arbitrary elements, provided an abstraction function α that maps list elements to pairs of sets of monitors is available. α e = (M, M') intuitively means that step e enters the monitors in M and passes (enters and leaves) the monitors in M'. The consistent interleaving describes all interleavings of the two lists that are consistent w.r.t. the monitor usage.

2.1 Monitors of lists of monitor pairs

The following defines the set of all monitors that occur in a list of pairs of monitors. This definition is used in the following context: mon-pl ($map \ \alpha$ w) is the set of monitors used by a word w w.r.t. the abstraction α

definition

```
mon-pl \ w == foldl \ (\cup) \ \{\} \ (map \ (\lambda e. \ fst \ e \cup snd \ e) \ w)
```

lemma $mon\text{-}pl\text{-}empty[simp]: mon\text{-}pl [] = \{\}$

¹Reentrant locks can always be simulated by non-reentrant ones, at the cost of a worst-case exponential blowup of the program size

```
\langle proof \rangle
lemma mon-pl-cons[simp]: mon-pl (e \# w) = fst \ e \cup snd \ e \cup mon-pl \ w
  \langle proof \rangle
lemma mon-pl-unconc: !!b. mon-pl (a@b) = mon-pl a \cup mon-pl b
  \langle proof \rangle
lemma mon-pl-ileq: w \preceq w' \Longrightarrow mon-pl w \subseteq mon-pl w'
  \langle proof \rangle
lemma mon-pl-set: mon-pl w = \bigcup \{ \text{ fst } e \cup \text{ snd } e \mid e. \ e \in \text{set } w \}
  \langle proof \rangle
fun
  cil :: 'a \ list \Rightarrow ('a \Rightarrow ('m \ set \times 'm \ set)) \Rightarrow 'a \ list \Rightarrow 'a \ list \ set
    (\langle - \otimes_{-} \rightarrow [64,64,64] 64) where
    - Interleaving with the empty word results in the empty word
  [] \otimes_{\alpha} w = \{w\}
  |w \otimes_{\alpha}[] = \{w\}
    If both words are not empty, we can take the first step of one word, interleave
the rest with the other word and then append the first step to all result set elements,
provided it does not allocate a monitor that is used by the other word
  \mid e1\#w1 \otimes_{\alpha} e2\#w2 = (
    if fst (\alpha \ e1) \cap mon-pl \ (map \ \alpha \ (e2\#w2)) = \{\} then
      e1 \cdot (w1 \otimes_{\alpha} e2 \# w2)
    else \{\}
    if fst (\alpha \ e2) \cap mon-pl \ (map \ \alpha \ (e1\#w1)) = \{\} then
      e2 \cdot (e1 \# w1 \otimes_{\alpha} w2)
    else \{\}
```

Note that this definition allows reentrant monitors, because it only checks that a monitor that is going to be entered by one word is not used in the *other* word. Thus the same word may enter the same monitor multiple times.

The next lemmas are some auxiliary lemmas to simplify the handling of the consistent interleaving operator.

```
 \begin{array}{l} \textbf{lemma} \ \ cil\text{-}last\text{-}case\text{-}split[cases\ set,\ case\text{-}names\ left\ right]:} \\ \|\ w \in e1 \# w1 \otimes_{\alpha} \ e2 \# w2; \\ \|\ w'.\ \|\ w = e1 \# w';\ w' \in (w1 \otimes_{\alpha} \ e2 \# w2); \\ fst\ (\alpha\ e1) \cap mon\text{-}pl\ (map\ \alpha\ (e2 \# w2)) = \{\}\ \|\ \Longrightarrow P; \\ \|\ w'.\ \|\ w = e2 \# w';\ w' \in (e1 \# w1 \otimes_{\alpha} \ w2); \\ fst\ (\alpha\ e2) \cap mon\text{-}pl\ (map\ \alpha\ (e1 \# w1)) = \{\}\ \|\ \Longrightarrow P \\ \|\ proof\ \rangle \end{aligned}
```

 $\label{lemma:cil-cases} \textbf{lemma: cil-cases} [cases\ set,\ case-names\ both-empty\ left-empty\ right-empty\ app-left\ app-right]:$

```
\llbracket w \in wa \otimes_{\alpha} wb;
      \llbracket w=[]; wa=[]; wb=[] \rrbracket \Longrightarrow P;
      \llbracket wa = \llbracket ; w = wb \rrbracket \implies P;
      \llbracket w=wa; wb=[] \rrbracket \implies P;
     !!ea\ wa'\ w'.\ \llbracket w=ea\#w';\ wa=ea\#wa';\ w'\in wa'\otimes_{\alpha}wb;
                         fst (\alpha \ ea) \cap mon-pl (map \ \alpha \ wb) = \{\} \ \rrbracket \Longrightarrow P;
     !!eb\ wb'\ w'.\ [\![w\!=\!eb\#w';\ wb\!=\!eb\#wb';\ w'\!\in\!wa\otimes_{\alpha}\!wb';
                         fst (\alpha \ eb) \cap mon-pl (map \ \alpha \ wa) = \{\} \ ] \Longrightarrow P
  ]\!] \Longrightarrow P
\langle proof \rangle
lemma cil-induct'[case-names both-empty left-empty right-empty append]: [
   \bigwedge \alpha. P \alpha [] [];
   \bigwedge \alpha \ ad \ ae. \ P \ \alpha \ [] \ (ad \# \ ae);
   \bigwedge \alpha \ z \ aa. \ P \ \alpha \ (z \# aa) \ [];
   \bigwedge \alpha e1 w1 e2 w2.
     \llbracket fst \ (\alpha \ e1) \cap mon\text{-}pl \ (map \ \alpha \ (e2 \ \# \ w2)) = \{\} \rrbracket \Longrightarrow P \ \alpha \ w1 \ (e2 \ \# \ w2);
     \llbracket fst \ (\alpha \ e2) \cap mon\text{-}pl \ (map \ \alpha \ (e1 \ \# \ w1)) = \{\} \rrbracket \Longrightarrow P \ \alpha \ (e1 \ \# \ w1) \ w2 \rrbracket
   \implies P \alpha (e1 \# w1) (e2 \# w2)
  \rrbracket \Longrightarrow P \ \alpha \ wa \ wb
   \langle proof \rangle
lemma cil-induct-fix\alpha:
   P \alpha [] [];
   \bigwedge ad\ ae.\ P\ \alpha\ []\ (ad\ \#\ ae);
   \bigwedge z \ aa. \ P \ \alpha \ (z \# aa) \ [];
   \bigwedge e1 \ w1 \ e2 \ w2.
     \llbracket \textit{fst} \ (\alpha \ e2) \cap \textit{mon-pl} \ (\textit{map} \ \alpha \ (e1 \ \# \ w1)) = \{\} \longrightarrow P \ \alpha \ (e1 \ \# \ w1) \ w2;
      fst \ (\alpha \ e1) \cap mon-pl \ (map \ \alpha \ (e2 \ \# \ w2)) = \{\} \longrightarrow P \ \alpha \ w1 \ (e2 \ \# \ w2)]
      \implies P \alpha (e1 \# w1) (e2 \# w2)
   \implies P \alpha v w
   \langle proof \rangle
lemma cil-induct-fix\alpha'[case-names both-empty left-empty right-empty append]: \llbracket
   P \alpha [] [];
   \bigwedge ad\ ae.\ P\ \alpha\ []\ (ad\ \#\ ae);
   \bigwedge z \ aa. \ P \ \alpha \ (z \# aa) \ [];
   \bigwedge e1 \ w1 \ e2 \ w2.
     fst \ (\alpha \ e1) \cap mon-pl \ (map \ \alpha \ (e2 \ \# \ w2)) = \{\} \Longrightarrow P \ \alpha \ w1 \ (e2 \ \# \ w2);
     fst \ (\alpha \ e2) \cap mon-pl \ (map \ \alpha \ (e1 \ \# \ w1)) = \{\} \Longrightarrow P \ \alpha \ (e1 \ \# \ w1) \ w2
     \implies P \alpha (e1 \# w1) (e2 \# w2)
  ]\!] \Longrightarrow P \alpha wa wb
   \langle proof \rangle
lemma [simp]: w \otimes_{\alpha}[] = \{w\}
   \langle proof \rangle
lemma cil-contains-empty[rule-format, simp]: ([] \in wa \otimes_{\alpha} wb) = (wa = [] \land wb = [])
   \langle proof \rangle
```

```
lemma cil-cons-cases[cases set, case-names left right]: \llbracket e\#w \in w1 \otimes_{\alpha} w2;
  !!w1'. \llbracket w1 = e \# w1'; \ w \in w1' \otimes_{\alpha} w2; \ fst \ (\alpha \ e) \cap mon-pl \ (map \ \alpha \ w2) = \{\} \ \rrbracket \Longrightarrow P;
  !!w2'. \llbracket w2 = e \# w2'; w \in w1 \otimes_{\Omega} w2'; fst (\alpha \ e) \cap mon\text{-}pl \ (map \ \alpha \ w1) = \{\} \ \rrbracket \Longrightarrow P
\mathbb{I} \Longrightarrow P
   \langle proof \rangle
lemma cil-set-induct[induct set, case-names empty left right]: !!\alpha w1 w2.
     w \in w1 \otimes_{\alpha} w2;
     !!\alpha. P [] \alpha [] [];
     !!\alpha \ e \ w' \ w1' \ w2. \ [w' \in w1' \otimes_{\alpha} w2; \ fst \ (\alpha \ e) \cap \ mon-pl \ (map \ \alpha \ w2) = \{\};
                             P w' \alpha w1' w2 \implies P (e\#w') \alpha (e\#w1') w2;
     !!\alpha \ e \ w' \ w2' \ w1. \ \llbracket w' \in w1 \otimes_{\alpha} w2'; \ fst \ (\alpha \ e) \cap mon-pl \ (map \ \alpha \ w1) = \{\};
                             P \stackrel{\cdot}{w'} \alpha w1 w2' \implies P (e\#w') \alpha w1 (e\#w2')
  \rrbracket \Longrightarrow P \ w \ \alpha \ w1 \ w2
   \langle proof \rangle
lemma cil-set-induct-fix\alpha[induct set, case-names empty left right]: !!w1 w2.
     w \in w1 \otimes_{\alpha} w2;
     P [ ] \alpha [ ] [ ];
     !!e\ w'\ w1'\ w2.\ \llbracket w'\in w1'\otimes_{\alpha}w2;\ fst\ (\alpha\ e)\cap mon-pl\ (map\ \alpha\ w2)=\{\};
                          P w' \alpha w1' w2 \implies P (e\#w') \alpha (e\#w1') w2;
     !!e\ w'\ w2'\ w1.\ \llbracket w' \in w1 \otimes_{\alpha} w2';\ fst\ (\alpha\ e) \cap mon-pl\ (map\ \alpha\ w1) = \{\};
                           P w' \alpha w1 w2' \implies P (e\#w') \alpha w1 (e\#w2')
  \rrbracket \Longrightarrow P \ w \ \alpha \ w1 \ w2
   \langle proof \rangle
lemma cil-cons1: \llbracket w \in wa \otimes_{\alpha} wb; fst (\alpha \ e) \cap mon\text{-pl} (map \ \alpha \ wb) = \{\} \rrbracket
                        \implies e \# w \in e \# wa \otimes_{\alpha} wb
   \langle proof \rangle
lemma cil-cons2: \llbracket w \in wa \otimes_{\alpha} wb; fst (\alpha \ e) \cap mon\text{-pl} (map \ \alpha \ wa) = \{\} \rrbracket
                        \implies e \# w \in wa \otimes_{\alpha} e \# wb
  \langle proof \rangle
           Properties of consistent interleaving
lemma cil-subset-il: w \otimes_{\alpha} w' \subseteq w \otimes w'
   \langle proof \rangle
lemma cil-subset-il': w \in w1 \otimes_{\alpha} w2 \implies w \in w1 \otimes w2
lemma cil-set: w \in w1 \otimes_{\alpha} w2 \implies set \ w = set \ w1 \cup set \ w2
   \langle proof \rangle
corollary cil-mon-pl: w \in w1 \otimes_{\alpha} w2
   \implies mon\text{-}pl\ (map\ \alpha\ w) = mon\text{-}pl\ (map\ \alpha\ w1) \cup mon\text{-}pl\ (map\ \alpha\ w2)
  \langle proof \rangle
lemma cil-length[rule-format]: \forall w \in wa \otimes_{\alpha} wb. length w = length wa + length wb
   \langle proof \rangle
lemma cil-ileq: w \in w1 \otimes_{\Omega} w2 \implies w1 \prec w \land w2 \prec w
```

```
\langle proof \rangle
\mathbf{lemma} \ cil\text{-}commute: \ w \otimes_{\alpha} w' = \ w' \otimes_{\alpha} w
\langle proof \rangle
\mathbf{lemma} \ cil\text{-}assoc1: \text{!!}wl \ w1 \ w2 \ w3. \ \llbracket \ w \in wl \otimes_{\alpha} w3; \ wl \in w1 \otimes_{\alpha} w2 \ \rrbracket
\Rightarrow \exists \ wr. \ w \in w1 \otimes_{\alpha} wr \ \land \ wr \in w2 \otimes_{\alpha} w3
\langle proof \rangle
\mathbf{lemma} \ cil\text{-}assoc2: 
\mathbf{assumes} \ A: \ w \in w1 \otimes_{\alpha} wr \ \mathbf{and} \ B: \ wr \in w2 \otimes_{\alpha} w3
\mathbf{shows} \ \exists \ wl. \ w \in wl \otimes_{\alpha} w3 \ \land \ wl \in w1 \otimes_{\alpha} w2
\langle proof \rangle
\mathbf{lemma} \ cil\text{-}map: \ w \in w1 \ \otimes_{(\alpha \circ f)} \ w2 \ \Longrightarrow \ map \ f \ w \in \ map \ f \ w1 \ \otimes_{\alpha} \ map \ f \ w2
\langle proof \rangle
```

end

3 Acquisition Histories

theory Acquisition History imports ConsInterleave begin

The concept of acquisition histories was introduced by Kahlon, Ivancic, and Gupta [5] as a bounded size abstraction of executions that acquire and release locks that contains enough information to decide consistent interleavability. In this work, we use this concept for reentrant monitors. As in Section 2, we encode monitor usage information in pairs of sets of monitors, and regard lists of such pairs as (abstract) executions. An item (E, U) of such a list describes a sequence of steps of the concrete execution that first enters the monitors in E and then passes through the monitors in U. The monitors in E are never left by the execution. Note that due to the syntactic binding of monitors to the program structure, any execution of a single thread can be abstracted to a sequence of (E, U)-pairs. Restricting the possible schedules (see Section 8) will allow us to also abstract executions reaching a single program point to a sequence of such pairs.

We want to decide whether two executions are interleavable. The key observation of [5] is, that two executions e and e' are not interleavable if and only if there is a conflicting pair (m, m') of monitors, such that e enters (and never leaves) m and then uses m' and e' enters (and never leaves) m' and then uses m.

An acquisition history is a map from monitors to set of monitors. The acquisition history of an execution maps a monitor m that is allocated at

the end of the execution to all monitors that are used after or in the same step that finally enters m. Monitors that are not allocated at the end of an execution are mapped to the empty set. Though originally used for a setting without reentrant monitors, acquisition histories also work for our setting with reentrant monitors.

This theory contains the definition of acquisition histories and acquisition history interleavability, an ordering on acquisition histories that reflects the blocking potential of acquisition histories, and a mapping function from paths to acquisition histories that is shown to be compatible with monitor consistent interleaving.

3.1 Definitions

Acquisition histories are modeled as functions from monitors to sets of monitors. Intuitively $m' \in h$ m models that an execution finally is in m, and monitor m' has been used (i.e. passed or entered) after or at the same time m has been finally entered. By convention, we have $m \in h$ m or h $m = \{\}$.

```
definition ah == \{ (h::'m \Rightarrow 'm \ set) \ . \ \forall \ m. \ h \ m = \{ \} \lor m \in h \ m \}
```

lemma ah-cases[cases set]: $\llbracket h \in ah; h \ m = \{\} \Longrightarrow P ; m \in h \ m \Longrightarrow P \rrbracket \Longrightarrow P \ \langle proof \rangle$

3.2 Interleavability

Two acquisition histories h1 and h2 are considered interleavable, iff there is no conflicting pair of monitors m1 and m2, where a pair of monitors m1 and m2 is called *conflicting* iff m1 is used in h2 after entering m2 and, vice versa, m2 is used in h1 after entering m1.

definition

```
ah-il :: ('m \Rightarrow 'm \ set) \Rightarrow ('m \Rightarrow 'm \ set) \Rightarrow bool \ (infix \langle [*] \rangle \ 65)
where
h1 \ [*] \ h2 == \neg(\exists \ m1 \ m2. \ m1 \in h2 \ m2 \land m2 \in h1 \ m1)
```

From our convention, it follows (as expected) that the sets of entered monitors (lock-sets) of two interleavable acquisition histories are disjoint

```
lemma ah-il-lockset-disjoint: [\![h1 \in ah; h2 \in ah; h1 \ [*] \ h2 \ ]\!] \Longrightarrow h1 \ m = \{\} \lor h2 \ m = \{\} \lor proof \rangle
```

Of course, acquisition history interleavability is commutative

```
lemma ah-il-commute: h1 [*] h2 \Longrightarrow h2 [*] h1 \langle proof \rangle
```

3.3 Used monitors

Let's define the monitors of an acquisition history, as all monitors that occur in the acquisition history

definition $mon\text{-}ah :: ('m \Rightarrow 'm \ set) \Rightarrow 'm \ set$ **where** $mon\text{-}ah \ h == \bigcup \{ \ h(m) \mid m. \ True \}$

3.4 Ordering

The element-wise subset-ordering on acquisition histories intuitively reflects the blocking potential: The bigger the acquisition history, the fewer acquisition histories are interleavable with it.

Note that the Isabelle standard library automatically lifts the subset ordering to functions, so we need no explicit definition here.

```
 \begin{array}{l} \textbf{lemma} \ ah\text{-}leq\text{-}il \colon \llbracket \ h1 \ [*] \ h2; \ h1' \leq h1; \ h2' \leq h2 \ \rrbracket \Longrightarrow h1' \ [*] \ h2' \\ \langle proof \rangle \\ \textbf{lemma} \ ah\text{-}leq\text{-}il\text{-}left \colon \llbracket \ h1 \ [*] \ h2; \ h1' \leq h1 \ \rrbracket \Longrightarrow h1' \ [*] \ h2 \ \textbf{and} \\ ah\text{-}leq\text{-}il\text{-}right \colon \llbracket \ h1 \ [*] \ h2; \ h2' \leq h2 \ \rrbracket \Longrightarrow h1 \ [*] \ h2' \\ \langle proof \rangle \\ \end{array}
```

3.5 Acquisition histories of executions

Next we define a function that abstracts from executions (lists of enter/use pairs) to acquisition histories

```
primrec \alpha ah :: ('m \ set \times 'm \ set) \ list \Rightarrow 'm \Rightarrow 'm \ set \ \mathbf{where}
\alpha ah \ [] \ m = \{\}
| \ \alpha ah \ (e\#w) \ m = (if \ m \in fst \ e \ then \ fst \ e \cup snd \ e \cup mon-pl \ w \ else \ \alpha ah \ w \ m)
- \ \alpha ah \ generates \ valid \ acquisition \ histories
\mathbf{lemma} \ \alpha ah-ah: \ \alpha ah \ w \in ah
\langle proof \rangle
\mathbf{lemma} \ \alpha ah-hd: \ [m \in fst \ e; \ x \in fst \ e \cup snd \ e \cup mon-pl \ w] \implies x \in \alpha ah \ (e\#w) \ m
\langle proof \rangle
\mathbf{lemma} \ \alpha ah-tl: \ [m \notin fst \ e; \ x \in \alpha ah \ w \ m] \implies x \in \alpha ah \ (e\#w) \ m
\langle proof \rangle
\mathbf{lemma} \ \alpha ah-cases[cases \ set, \ case-names \ hd \ tl]: \ [
x \in \alpha ah \ w \ m;
!!e \ w'. \ [w = e\#w'; \ m \in fst \ e; \ x \in fst \ e \cup snd \ e \cup mon-pl \ w'] \implies P;
!!e \ w'. \ [w = e\#w'; \ m \notin fst \ e; \ x \in \alpha ah \ w' \ m] \implies P
\| \implies P 
\langle proof \rangle
```

```
 \begin{array}{l} \textbf{lemma} \ \alpha ah\text{-}cons\text{-}cases[cases\ set,\ case\text{-}names\ hd\ tt]\colon \llbracket \\ x\in \alpha ah\ (e\#w')\ m; \\ \llbracket m\in fst\ e;\ x\in fst\ e\ \cup\ snd\ e\ \cup\ mon\text{-}pl\ w'\rrbracket \Longrightarrow P; \\ \llbracket m\notin fst\ e;\ x\in \alpha ah\ w'\ m\rrbracket \Longrightarrow P \\ \rrbracket \Longrightarrow P \\ \langle proof \rangle \\ \\ \textbf{lemma}\ mon\text{-}ah\text{-}subset:\ mon\text{-}ah\ (\alpha ah\ w)\ \subseteq\ mon\text{-}pl\ w \\ \langle proof \rangle \\ \\ \textbf{lemma}\ \alpha ah\text{-}ileq:\ w1\preceq w2 \Longrightarrow \alpha ah\ w1\ \leq\ \alpha ah\ w2 \\ \langle proof \rangle \\ \end{aligned}
```

We can now prove the relation of monitor consistent interleavability and interleavability of the acquisition histories.

```
\mathbf{lemma}\ ah	ext{-}interleavable 1:
```

```
w \in w1 \otimes_{\alpha} w2 \Longrightarrow \alpha ah \ (map \ \alpha \ w1) \ [*] \ \alpha ah \ (map \ \alpha \ w2)— The lemma is shown by induction on the structure of the monitor consistent interleaving operator
```

 $\langle proof \rangle$

```
lemma ah-interleavable2:
```

```
assumes A: \alpha ah \ (map \ \alpha \ w1) \ [*] \ \alpha ah \ (map \ \alpha \ w2)
shows w1 \otimes_{\alpha} w2 \neq \{\}
— This lemma is shown by induction on the sum of the word lengths \langle proof \rangle
```

Finally, we can state the relationship between monitor consistent interleaving and interleaving of acquisition histories

```
theorem ah-interleavable: (\alpha ah \ (map \ \alpha \ w1) \ [*] \ \alpha ah \ (map \ \alpha \ w2)) \longleftrightarrow (w1 \otimes_{\alpha} w2 \neq \{\}) \ \langle proof \rangle
```

3.6 Acquisition history backward update

We define a function to update an acquisition history backwards. This function is useful for constructing acquisition histories in backward constraint systems.

definition

```
ah-update :: ('m \Rightarrow 'm \ set) \Rightarrow ('m \ set * 'm \ set) \Rightarrow 'm \ set \Rightarrow ('m \Rightarrow 'm \ set) where ah-update \ h \ F \ M \ m == if \ m \in fst \ F \ then \ fst \ F \ \cup \ snd \ F \ \cup \ M \ else \ h \ m
```

Intuitively, ah-update h (E, U) M m means to prepend a step (E, U) to the acquisition history h of a path that uses monitors M. Note that we need the extra parameter M, since an acquisition history does not contain information about the monitors that are used on a path before the first monitor that will not be left has been entered.

```
lemma ah-update-cons: \alphaah (e\#w) = ah-update (\alphaah w) e (mon-pl w) \langle proof \rangle
```

The backward-update function is monotonic in the first and third argument as well as in the used monitors of the second argument. Note that it is, in general, not monotonic in the entered monitors of the second argument.

```
 \begin{array}{l} \textbf{lemma} \ ah\text{-}update\text{-}mono\text{:} \ \llbracket h \leq h'; \ F = F'; \ M \subseteq M' \rrbracket \\ \Longrightarrow ah\text{-}update \ h \ F \ M \leq ah\text{-}update \ h' \ F' \ M' \\ \langle proof \rangle \\ \textbf{lemma} \ ah\text{-}update\text{-}mono2\text{:} \ \llbracket h \leq h'; \ U \subseteq U'; \ M \subseteq M' \rrbracket \\ \Longrightarrow ah\text{-}update \ h \ (E,U) \ M \leq ah\text{-}update \ h' \ (E,U') \ M' \\ \langle proof \rangle \\ \end{array}
```

end

4 Labeled transition systems

```
theory LTS imports Main begin
```

Labeled transition systems (LTS) provide a model of a state transition system with named transitions.

4.1 Definitions

An LTS is modeled as a ternary relation between start configuration, transition label and end configuration

```
type-synonym ({}'c,{}'a) LTS = ({}'c \times {}'a \times {}'c) set
```

Transitive reflexive closure

```
inductive\hbox{-} set
```

```
trcl :: ('c,'a) \ LTS \Rightarrow ('c,'a \ list) \ LTS for t where empty[simp]: (c,[],c) \in trcl \ t | \ cons[simp]: [ \ (c,a,c') \in t; \ (c',w,c'') \in trcl \ t \ ]] \Longrightarrow (c,a\#w,c'') \in trcl \ t
```

4.2 Basic properties of transitive reflexive closure

```
lemma trcl-empty-cons: (c,[],c') \in trcl\ t \Longrightarrow (c=c') \langle proof \rangle lemma trcl-empty-simp[simp]: (c,[],c') \in trcl\ t = (c=c') \langle proof \rangle lemma trcl-single[simp]: ((c,[a],c') \in trcl\ t) = ((c,a,c') \in t) \langle proof \rangle
```

```
lemma trcl-uncons: (c,a\#w,c') \in trcl\ t \Longrightarrow \exists\ ch\ .\ (c,a,ch) \in t\ \land\ (ch,w,c') \in trcl\ t
  \langle proof \rangle
lemma trcl-uncons-cases:
     (c,e\#w,c')\in trcl\ S;
     !!ch. [(c,e,ch) \in S; (ch,w,c') \in trcl S] \implies P
  \langle proof \rangle
lemma trcl-one-elem: (c,e,c') \in t \Longrightarrow (c,[e],c') \in trcl\ t
  \langle proof \rangle
lemma trcl-unconsE[cases set, case-names split]: [
     (c,e\#w,c')\in trcl\ S;
     !!ch. [(c,e,ch) \in S; (ch,w,c') \in trcl S] \Longrightarrow P
  \rrbracket \Longrightarrow P
  \langle proof \rangle
lemma trcl-pair-unconsE[cases set, case-names split]: [
     ((s,c),e\#w,(s',c'))\in trcl\ S;
     !!sh\ ch.\ \llbracket ((s,c),e,(sh,ch)) \in S;\ ((sh,ch),w,(s',c')) \in trcl\ S \rrbracket \Longrightarrow P
  \mathbb{I} \Longrightarrow P
  \langle proof \rangle
lemma trcl\text{-}concat: !! c . [(c,w1,c')\in trcl\ t; (c',w2,c'')\in trcl\ t]
  \implies (c, w1@w2, c'') \in trcl\ t
\langle proof \rangle
lemma trcl-unconcat: !! c . (c,w1@w2,c') \in trcl t
  \implies \exists ch \ . \ (c,w1,ch) \in trcl \ t \land (ch,w2,c') \in trcl \ t
\langle proof \rangle
4.2.1
             Appending of elements to paths
lemma trcl-rev-cons: [(c,w,ch) \in trcl\ T; (ch,e,c') \in T\ ] \implies (c,w@[e],c') \in trcl\ T
  \langle proof \rangle
lemma trcl-rev-uncons: (c, w@[e], c') \in trcl\ T
  \implies \exists ch. (c,w,ch) \in trcl \ T \land (ch,e,c') \in T
  \langle proof \rangle
lemma trcl-rev-induct[induct set, consumes 1, case-names empty snoc]: !! c'.
     (c,w,c') \in trcl S;
    !!c. P c [] c;
     !!c \ w \ c' \ \stackrel{\circ}{e} \ c''. \ \llbracket \ (c,w,c') \in trcl \ S; \ (c',e,c'') \in S; \ P \ c \ w \ c' \ \rrbracket \Longrightarrow P \ c \ (w@[e]) \ c''
  \rrbracket \Longrightarrow P \ c \ w \ c'
  \langle proof \rangle
lemma trcl-rev-cases: !!c c'.
     (c,w,c') \in trcl S;
     \llbracket w = \llbracket \colon c = c' \rrbracket \Longrightarrow P;
     !!ch \ e \ wh. \parallel w=wh@[e]; (c,wh,ch) \in trcl \ S; (ch,e,c') \in S \parallel \Longrightarrow P
  \rrbracket \Longrightarrow P
  \langle \mathit{proof} \, \rangle
```

```
lemma trcl-cons2: [(c,e,ch) \in T; (ch,f,c') \in T] \implies (c,[e,f],c') \in trcl T \land proof \rangle
```

4.2.2 Transitivity reasoning setup

```
declare trcl-cons2[trans] — It's important that this is declared before trcl-concat, because we want trcl-concat to be tried first by the transitivity reasoner declare cons[trans] declare trcl-concat[trans] declare trcl-rev-cons[trans]
```

4.2.3 Monotonicity

```
\begin{array}{l} \textbf{lemma} \ trcl\text{-}mono: !!A \ B. \ A \subseteq B \Longrightarrow trcl \ A \subseteq trcl \ B \\ & \langle proof \rangle \end{array} \begin{array}{l} \textbf{lemma} \ trcl\text{-}inter\text{-}mono: } x \in trcl \ (S \cap R) \Longrightarrow x \in trcl \ S \ x \in trcl \ (S \cap R) \Longrightarrow x \in trcl \ R \end{array}
```

4.2.4 Special lemmas for reasoning about states that are pairs

```
lemmas trcl-pair-induct = trcl.induct[of (xc1,xc2) xb (xa1,xa2), split-format (complete), consumes 1, case-names empty cons]
lemmas trcl-rev-pair-induct = trcl-rev-induct[of (xc1,xc2) xb (xa1,xa2), split-format (complete), consumes 1, case-names empty snoc]
```

4.2.5 Invariants

```
lemma trcl-prop-trans[cases set, consumes 1, case-names empty steps]: [ (c,w,c') \in trcl S; [ c=c'; w=[]] \Longrightarrow P; [ c\in Domain S; c'\in Range (Range S)] \Longrightarrow P \langle proof \rangle
```

end

 $\langle proof \rangle$

5 Thread Tracking

```
theory ThreadTracking imports Main HOL-Library.Multiset LTS Misc begin
```

This theory defines some general notion of an interleaving semantics. It defines how to extend a semantics specified on a single thread and a context to a semantic on multisets of threads. The context is needed in order to keep track of synchronization.

5.1 Semantic on multiset configuration

The interleaving semantics is defined on a multiset of stacks. The thread to make the next step is nondeterministically chosen from all threads ready to make steps.

```
definition
  gtr\ gtrs == \{\ (add\text{-}mset\ s\ c,e,add\text{-}mset\ s'\ c')\mid s\ c\ e\ s'\ c'\ .\ ((s,c),e,(s',c'))\in gtrs\ \}
lemma gtrI-s: ((s,c),e,(s',c')) \in gtrs \implies (add\text{-mset } s \ c,e,add\text{-mset } s' \ c') \in gtr \ gtrs
  \langle proof \rangle
lemma gtrI: ((s,c), w, (s',c')) \in trcl \ gtrs
  \implies (add\text{-}mset\ s\ c,w,add\text{-}mset\ s'\ c') \in trcl\ (gtr\ gtrs)
  \langle proof \rangle
lemma gtrE: [
     (c,e,c') \in gtr T;
     !!s\ ce\ s'\ ce'.\ [\ c=add-mset\ s\ ce;\ c'=add-mset\ s'\ ce';\ ((s,ce),e,(s',ce'))\in T\ ]]\Longrightarrow
  \mathbb{I} \Longrightarrow P
  \langle proof \rangle
lemma gtr-empty-conf-s[simp]:
  (\{\#\}, w, c') \notin gtr S
  (c,w,\{\#\})\notin gtr\ S
  \langle proof \rangle
lemma gtr\text{-}empty\text{-}conf1[simp]: ((\{\#\}, w, c') \in trcl (gtr S)) \longleftrightarrow (w=[] \land c'=\{\#\})
lemma gtr\text{-}empty\text{-}conf2[simp]: ((c,w,\{\#\})\in trcl\ (gtr\ S))\longleftrightarrow (w=[]\land c=\{\#\})
  \langle proof \rangle
lemma gtr-find-thread: [
     (c,e,c') \in gtr\ gtrs;
     !!s\ ce\ s'\ ce'. \llbracket c=add\text{-}mset\ s\ ce;\ c'=add\text{-}mset\ s'\ ce';\ ((s,ce),e,(s',ce'))\in gtrs \rrbracket \Longrightarrow
P
  \rrbracket \Longrightarrow P
  \langle proof \rangle
lemma gtr-step-cases[cases set, case-names loc other]: [
     (add\text{-}mset\ s\ ce,e,c') \in gtr\ gtrs;
     !!s' ce'. \llbracket c'=add\text{-}mset \ s' \ ce'; \ ((s,ce),e,(s',ce')) \in gtrs \ \rrbracket \Longrightarrow P;
     !!cc \ ss \ ss' \ ce'. \llbracket \ ce=add\text{-}mset \ ss \ cc; \ c'=add\text{-}mset \ ss' \ ce';
                             ((ss,add\text{-}mset\ s\ cc),e,(ss',ce'))\in gtrs\ \rrbracket \Longrightarrow P
  \mathbb{I} \Longrightarrow P
  \langle proof \rangle
lemma gtr-rev-cases[cases set, case-names loc other]: [
     (c,e,add\text{-}mset\ s'\ ce') \in gtr\ gtrs;
    !!s\ ce.\ \llbracket\ c=add\text{-}mset\ s\ ce;\ ((s,ce),e,(s',ce'))\in gtrs\ \rrbracket\implies P;
     !!cc \ ss \ ss' \ ce. [ c=add\text{-}mset \ ss \ ce; \ ce'=add\text{-}mset \ ss' \ cc;
```

5.2 Invariants

5.3 Context preservation assumption

We now assume that the original semantics does not modify threads in the context, i.e. it may only add new threads to the context and use the context to obtain monitor information, but not change any existing thread in the context. This assumption is valid for our semantics, where the context is just needed to determine the set of allocated monitors. It allows us to generally derive some further properties of such semantics.

```
locale env\text{-}no\text{-}step =
fixes gtrs :: (('s \times 's \ multiset),'l) \ LTS
assumes env\text{-}no\text{-}step\text{-}s[cases \ set, \ case\text{-}names \ csp]:}
[((s,c),e,(s',c'))\in gtrs; \, !!csp. \ c'=csp+c \implies P \ ]] \implies P

— The property of not changing existing threads in the context transfers to paths lemma (in env\text{-}no\text{-}step) env\text{-}no\text{-}step[cases \ set, \ case\text{-}names \ csp]:} [[((s,c),w,(s',c'))\in trcl \ gtrs;
!! \ csp. \ c'=csp+c \implies P
[] \implies P
\langle proof \rangle
```

The following lemma can be used to make a case distinction how a step operated on a given thread in the end configuration:

loc The thread made the step

spawn The thread was spawned by the step

env The thread was not involved in the step

```
lemma (in env-no-step) rev-cases-p[cases set, case-names loc spawn env]: assumes STEP: (c,e,add\text{-}mset\ s'\ ce')\in gtr\ gtrs\ and
LOC: !!s\ ce. \ \llbracket\ c=\{\#s\#\}+ce;\ ((s,ce),e,(s',ce'))\in gtrs\ \rrbracket\ \Longrightarrow\ P\ and
SPAWN: !!ss\ ss'\ ce\ csp.
\ \llbracket\ c=add\text{-}mset\ ss\ ce;\ ce'=add\text{-}mset\ ss'\ (csp+ce);
\ ((ss,ce),e,(ss',add\text{-}mset\ s'\ (csp+ce)))\in gtrs\ \rrbracket
\ \Longrightarrow\ P\ and
ENV: !!ss\ ss'\ ce\ csp.
\ \llbracket\ c=add\text{-}mset\ ss\ (add\text{-}mset\ s'\ ce);\ ce'=add\text{-}mset\ ss'\ (csp+ce);
\ ((ss,add\text{-}mset\ s'\ ce),e,(ss',csp+(add\text{-}mset\ s'\ ce)))\in gtrs\ \rrbracket
\ \Longrightarrow\ P
\ shows\ P
\langle proof \rangle
```

5.4 Explicit local context

In the multiset semantics, a single thread has no identity. This may become a problem when reasoning about a fixed thread during an execution. For example, in our constraint-system-based approach the operational characterization of the least solution of the constraint system requires to state properties of the steps of the initial thread in some execution. With the multiset semantics, we are unable to identify those steps among all steps.

There are many solutions to this problem, for example, using thread ids either as part of the thread's configuration or as part of the whole configuration by using lists of stacks or maps from ids to stacks as configuration datatype.

In the following we present a special solution that is strong enough to suit our purposes but not meant as a general solution.

Instead of identifying every single thread uniquely, we only distinguish one thread as the *local* thread. The other threads are *environment* threads. We then attach to every step the information whether it was on the local or on some environment thread.

We call this semantics loc/env-semantics in contrast to the multiset-semantics of the last section.

5.4.1 Lifted step datatype

```
datatype 'a el-step = LOC 'a | ENV 'a

definition
  loc w == filter (\lambda e. case e of LOC a \Rightarrow True \mid ENV \ a \Rightarrow False) w

definition
  env w == filter (\lambda e. case e of LOC a \Rightarrow False \mid ENV \ a \Rightarrow True) w

definition
```

```
le\text{-rem-s }e == case \ e \ of \ LOC \ a \Rightarrow a \mid ENV \ a \Rightarrow a
```

Standard simplification lemmas

```
lemma loc-env-simps[simp]:
  loc [] = []
  env[] = []
  \langle proof \rangle
lemma loc-single[simp]: loc [a] = (case a of LOC e \Rightarrow [a] \mid ENV e \Rightarrow [])
  \langle proof \rangle
lemma loc\text{-}uncons[simp]:
  loc (a\#b) = (case \ a \ of \ LOC \ e \Rightarrow [a] \mid ENV \ e \Rightarrow [])@loc \ b
lemma loc\text{-}unconc[simp]: loc (a@b) = loc a @ loc b
  \langle proof \rangle
lemma env-single[simp]: env [a] = (case \ a \ of \ LOC \ e \Rightarrow [] \mid ENV \ e \Rightarrow [a])
  \langle proof \rangle
lemma env-uncons[simp]:
  env (a\#b) = (case \ a \ of \ LOC \ e \Rightarrow [] \mid ENV \ e \Rightarrow [a]) @ \ env \ b
lemma env-unconc[simp]: env (a@b) = env a @ env b
  \langle proof \rangle
```

The following simplification lemmas are for converting between paths of the multiset- and loc/env-semantics

```
lemma le-rem-simps [simp]:
  le\text{-}rem\text{-}s\ (LOC\ a)=a
  le\text{-}rem\text{-}s\ (ENV\ a)=a
  \langle proof \rangle
lemma le-rem-id-simps[simp]:
  \textit{le-rem-s} \circ LOC = \textit{id}
  le\text{-}rem\text{-}s\circ ENV\,=\,id
  \langle proof \rangle
lemma le-rem-id-map[simp]:
  map\ le\text{-}rem\text{-}s\ (map\ LOC\ w) = w
  map\ le\text{-}rem\text{-}s\ (map\ ENV\ w) = w
  \langle proof \rangle
lemma env-map-env [simp]: env (map\ ENV\ w) = map\ ENV\ w
lemma env-map-loc [simp]: env (map\ LOC\ w) = []
  \langle proof \rangle
lemma loc-map-env [simp]: loc (map\ ENV\ w) = []
  \langle proof \rangle
lemma loc-map-loc [simp]: loc (map\ LOC\ w) = map\ LOC\ w
  \langle proof \rangle
```

5.4.2 Definition of the loc/env-semantics

```
type-synonym 's el-conf = ('s \times 's multiset)
inductive-set
  gtrp :: ('s \ el\text{-}conf,'l) \ LTS \Rightarrow ('s \ el\text{-}conf,'l \ el\text{-}step) \ LTS
 for S
  where
  gtrp-loc: ((s,c),e,(s',c')) \in S \Longrightarrow ((s,c),LOC\ e,(s',c')) \in gtrp\ S
 | gtrp-env: ((s,add-mset\ sl\ c),e,(s',add-mset\ sl\ c')) \in S
               \implies ((sl, add\text{-}mset\ s\ c), ENV\ e, (sl, add\text{-}mset\ s'\ c')) \in gtrp\ S
5.4.3
           Relation between multiset- and loc/env-semantics
lemma qtrp2qtr-s:
  ((s,c),e,(s',c')) \in gtrp \ T \Longrightarrow (add\text{-mset } s \ c,le\text{-rem-s} \ e,add\text{-mset } s' \ c') \in gtr \ T
\langle proof \rangle
lemma gtrp2gtr:
  ((s,c),w,(s',c')) \in trcl (gtrp T)
  \implies (add-mset s c,map le-rem-s w,add-mset s' c')\intrcl (gtr T)
  \langle proof \rangle
lemma (in env-no-step) gtr2gtrp-s[cases set, case-names gtrp]:
  assumes A: (add\text{-}mset\ s\ c,e,c') \in gtr\ gtrs
  and CASE: !!s' ce' ee. [c'=add\text{-mset }s' \ ce'; \ e=le\text{-rem-s }ee;
                          ((s,c),ee,(s',ce')) \in gtrp \ gtrs
  shows P
  \langle proof \rangle
lemma (in env-no-step) gtr2gtrp[cases set, case-names gtrp]:
  assumes A: (add\text{-}mset\ s\ c, w, c') \in trcl\ (gtr\ gtrs)
  and CASE: !!s' ce' ww. [c'=add\text{-mset }s' ce'; w=map le-rem-s ww;
                            ((s,c),ww,(s',ce')) \in trcl (gtrp gtrs)
                           \Longrightarrow P
 shows P
\langle proof \rangle
           Invariants
5.4.4
lemma gtrp-preserve-s:
  assumes A: ((s,c),e,(s',c')) \in gtrp\ T
  and INIT: P (add-mset \ s \ c)
 and PRES: !!s c s' c' e. \llbracket P \ (add\text{-mset } s \ c); \ ((s,c),e,(s',c')) \in T \rrbracket
                             \implies P \ (add\text{-}mset \ s' \ c')
  shows P (add-mset s' c')
\langle proof \rangle
```

```
lemma gtrp\text{-}preserve:
assumes A: ((s,c),w,(s',c')) \in trcl\ (gtrp\ T)
and INIT: P\ (add\text{-}mset\ s\ c)
and PRES: !!s c\ s'\ c'\ e. \llbracket P\ (add\text{-}mset\ s\ c);\ ((s,c),e,(s',c')) \in T \rrbracket
\implies P\ (add\text{-}mset\ s'\ c')
shows P\ (add\text{-}mset\ s'\ c')
\langle proof \rangle
```

end

6 Flowgraphs

theory Flowgraph imports Main Misc begin

We use a flowgraph-based program model that extends the one we used previously [6]. A program is represented as an edge annotated graph and a set of procedures. The nodes of the graph are partitioned by the procedures, i.e. every node belongs to exactly one procedure. There are no edges between nodes of different procedures. Every procedure has a distinguished entry and return node and a set of monitors it synchronizes on. Additionally, the program has a distinguished main procedure. The edges are annotated with statements. A statement is either a base statement, a procedure call or a thread creation (spawn). Procedure calls and thread creations refer to the called procedure or to the initial procedure of the spawned thread, respectively.

We require that the main procedure and any initial procedure of a spawned thread does not to synchronize on any monitors. This avoids that spawning of a procedure together with entering a monitor is available in our model as an atomic step, which would be an unrealistic assumption for practical problems. Technically, our model would become strictly more powerful without this assumption.

If we allowed this, our model would become strictly more powerful,

6.1 Definitions

```
datatype ('p,'ba) edgeAnnot = Base 'ba | Call 'p | Spawn 'p type-synonym ('n,'p,'ba) edge = ('n × ('p,'ba) edgeAnnot × 'n)

record ('n,'p,'ba,'m) flowgraph-rec = edges :: ('n,'p,'ba) edge set — Set of annotated edges main :: 'p — Main procedure entry :: 'p \Rightarrow 'n — Maps a procedure to its entry point return :: 'p \Rightarrow 'n — Maps a procedure to its return point
```

```
mon :: 'p \Rightarrow 'm \ set — Maps procedures to the set of monitors they allocate
 \mathit{proc\text{-}of} :: 'n \,\Rightarrow\, 'p — Maps a node to the procedure it is contained in
definition
  initial proc fg \ p == p = main \ fg \lor (\exists \ u \ v. \ (u, Spawn \ p, v) \in edges \ fg)
lemma main-is-initial[simp]: initialproc fg (main fg)
  \langle proof \rangle
locale flowgraph =
  fixes fg :: ('n,'p,'ba,'m,'more) flowgraph-rec-scheme (structure)
  — Edges are inside procedures only
 assumes edges-part: (u,a,v) \in edges \ fg \implies proc\text{-}of \ fg \ u = proc\text{-}of \ fg \ v
  — The entry point of a procedure must be in that procedure
 assumes entry-valid[simp]: proc-of fg (entry fg p) = p
  — The return point of a procedure must be in that procedure
 assumes return-valid[simp]: proc-of fg (return fg p) = p
  — Initial procedures do not synchronize on any monitors
 assumes initial-no-mon[simp]: initialproc fg p \Longrightarrow mon fg p = \{\}
6.2
        Basic properties
lemma (in flowgraph) spawn-no-mon[simp]:
  (u, Spawn \ p, \ v) \in edges \ fg \Longrightarrow mon \ fg \ p = \{\}
  \langle proof \rangle
lemma (in flowgraph) main-no-mon[simp]: mon fg (main fg) = \{\}
  \langle proof \rangle
lemma (in flowgraph) entry-return-same-proc[simp]:
  entry fg p = return fg p' \Longrightarrow p = p'
  \langle proof \rangle
lemma (in flowgraph) entry-entry-same-proc[simp]:
  entry fg p = entry fg p' \Longrightarrow p = p'
  \langle proof \rangle
```

6.3 Extra assumptions for flowgraphs

lemma (in flowgraph) return-return-same-proc[simp]:

return $fg p = return fg p' \Longrightarrow p = p'$

 $\langle proof \rangle$

In order to simplify the definition of our restricted schedules (cf. Section 8), we make some extra constraints on flowgraphs. Note that these are no real restrictions, as we can always rewrite flowgraphs to match these constraints, preserving the set of conflicts. We leave it to future work to consider such a rewriting formally.

The background of this restrictions is that we want to start an execution of a thread with a procedure call that never returns. This will allow easier technical treatment in Section 8. Here we enforce this semantic restrictions by syntactic properties of the flowgraph.

The return node of a procedure is called *isolated*, if it has no incoming edges and is different from the entry node. A procedure with an isolated return node will never return. See Section 8.1 for a proof of this.

definition

```
isolated\text{-}ret\ fg\ p == \ (\forall\ u\ l.\ \neg(u,l,return\ fg\ p)\in edges\ fg)\ \land\ entry\ fg\ p \neq\ return\ fg\ p
```

The following syntactic restrictions guarantee that each thread's execution starts with a non-returning call. See Section 8.1 for a proof of this.

```
locale eflowgraph = flowgraph +

— Initial procedure's entry node isn't equal to its return node

assumes initial-no-ret: initialproc fg\ p \implies entry\ fg\ p \ne return\ fg\ p

— The only outgoing edges of initial procedures' entry nodes are call edges to
procedures with isolated return node

assumes initial-call-no-ret: [initialproc fg\ p; (entry fg\ p,l,v) \in edges\ fg]

\implies \exists\ p'.\ l=Call\ p'\ \land\ isolated-ret\ fg\ p'
```

6.4 Example Flowgraph

This section contains a check that there exists a (non-trivial) flowgraph, i.e. that the assumptions made in the *flowgraph* and *eflowgraph* locales are consistent and have at least one non-trivial model.

definition

```
\begin{array}{l} example-fg == () \\ edges = \{((0::nat, 0::nat), Call \ 1, (0,1)), \ ((1,0), Spawn \ 0, (1,0)), \\ ((1,0), Call \ 0, \ (1,0))\}, \\ main = 0, \\ entry = \lambda p. \ (p,0), \\ return = \lambda p. \ (p,1), \\ mon = \lambda p. \ if \ p=1 \ then \ \{0\} \ else \ \{\}, \\ proc-of = \lambda \ (p,x). \ p \ () \end{array}
```

lemma exists-eflowgraph: eflowgraph example-fg $\langle proof \rangle$

 \mathbf{end}

7 Operational Semantics

```
theory Semantics
imports Main Flowgraph HOL-Library.Multiset LTS Interleave ThreadTracking
```

begin

7.1 Configurations and labels

The state of a single thread is described by a stack of control nodes. The top node is the current control node and the nodes deeper in the stack are stored return addresses. The configuration of a whole program is described by a multiset of stacks.

Note that we model stacks as lists here, the first element being the top element.

```
type-synonym 'n conf = ('n list) multiset
```

A step is labeled according to the executed edge. Additionally, we introduce a label for a procedure return step, that has no corresponding edge.

```
datatype ('p,'ba) \ label = LBase \ 'ba \mid LCall \ 'p \mid LRet \mid LSpawn \ 'p
```

7.2 Monitors

The following defines the monitors of nodes, stacks, configurations, step labels and paths (sequences of step labels)

definition

— The monitors of a node are the monitors the procedure of the node synchronizes on

```
mon-n fg n == mon fg (proc-of fg n)
```

definition

```
— The monitors of a stack are the monitors of all its nodes mon-s fg\ s == \bigcup \{ mon-n\ fg\ n \mid n\ .\ n \in set\ s \}
```

definition

```
— The monitors of a configuration are the monitors of all its stacks mon-c fg c == \bigcup { mon-s fg s | s . s \in# c }
```

— The monitors of a step label are the monitors of procedures that are called by this step

```
definition mon-e :: ('b, 'c, 'd, 'a, 'e) flowgraph-rec-scheme \Rightarrow ('c, 'f) label \Rightarrow 'a set where
```

```
\mathit{mon\text{-}e}\;\mathit{fg}\;\mathit{e} = (\mathit{case}\;\mathit{e}\;\mathit{of}\;(\mathit{LCall}\;\mathit{p}) \Rightarrow \mathit{mon}\;\mathit{fg}\;\mathit{p}\;|\; \text{-} \Rightarrow \{\})
```

```
lemma mon\text{-}e\text{-}simps \ [simp]:
mon\text{-}e \ fg \ (LBase \ a) = \{\}
mon\text{-}e \ fg \ (LCall \ p) = mon \ fg \ p
mon\text{-}e \ fg \ (LRet) = \{\}
mon\text{-}e \ fg \ (LSpawn \ p) = \{\}
\langle proof \rangle
definition
mon\text{-}w \ fg \ w == \bigcup \ \{ \ mon\text{-}e \ fg \ e \ | \ e. \ e \in set \ w \}
```

```
lemma mon-s-alt: mon-s fg \ s == \bigcup \ (mon \ fg \ `proc-of \ fg \ `set \ s)
  \langle proof \rangle
lemma mon\text{-}c\text{-}alt: mon\text{-}c fg c == \bigcup (mon\text{-}s fg 'set\text{-}mset c)
lemma mon-w-alt: mon-w fg \ w == \bigcup \ (mon-e \ fg \ `set \ w)
  \langle proof \rangle
lemma mon-sI: [n \in set \ s; \ m \in mon-n \ fg \ n] \implies m \in mon-s \ fg \ s
lemma mon-sD: m \in mon-sfg s \Longrightarrow \exists n \in set s. m \in mon-nfg n
  \langle proof \rangle
lemma mon-n-same-proc:
  proc\text{-}of fg \ n = proc\text{-}of fg \ n' \Longrightarrow mon\text{-}n fg \ n = mon\text{-}n fg \ n'
  \langle proof \rangle
lemma mon-s-same-proc:
  proc\text{-}of\ fg\ `set\ s=proc\text{-}of\ fg\ `set\ s'\Longrightarrow mon\text{-}s\ fg\ s=mon\text{-}s\ fg\ s'
lemma (in flowgraph) mon-of-entry[simp]: mon-n fg (entry fg p) = mon fg p
lemma (in flowgraph) mon-of-ret[simp]: mon-n fg (return fg p) = mon fg p
  \langle proof \rangle
lemma mon\text{-}c\text{-}single[simp]: mon\text{-}c fg \{\#s\#\} = mon\text{-}s fg s
lemma mon-s-single[simp]: mon-s fg [n] = mon-n fg n
  \langle proof \rangle
lemma mon-s-empty[simp]: mon-s fg [] = {}
lemma mon\text{-}c\text{-}empty[simp]: mon\text{-}cfg \{\#\} = \{\}
  \langle proof \rangle
lemma mon-s-unconc: mon-s fg (a@b) = mon-s fg a \cup mon-s fg b
lemma mon-s-uncons[simp]: mon-s fg (a\#as) = mon-n fg a \cup mon-s fg as
  \langle proof \rangle
lemma mon-c-union-conc: mon-c fg(a+b) = mon-c fg a \cup mon-c fg b
  \langle proof \rangle
lemma mon\text{-}c\text{-}add\text{-}mset\text{-}unconc: mon\text{-}c fg (add\text{-}mset\ x\ b) = mon\text{-}s fg\ x\cup mon\text{-}c
fg b
  \langle proof \rangle
lemmas mon-c-unconc = mon-c-union-conc mon-c-add-mset-unconc
lemma mon-cI: [s \in \# c; m \in mon-s fg s] \implies m \in mon-c fg c
```

```
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}cD\text{:} \ \llbracket m \in mon\text{-}c \ fg \ c \rrbracket \implies \exists \ s. \ s \in \# \ c \ \land \ m \in mon\text{-}s \ fg \ s 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}s\text{-}mono\text{:} \ set \ s' \implies mon\text{-}s \ fg \ s \subseteq mon\text{-}s \ fg \ s' 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}c\text{-}mono\text{:} \ c \subseteq \# \ c' \implies mon\text{-}c \ fg \ c \subseteq mon\text{-}c \ fg \ c' 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}w\text{-}empty[simp]\text{:} \ mon\text{-}w \ fg \ [e] = \# \{ \} 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}w\text{-}single[simp]\text{:} \ mon\text{-}w \ fg \ [e] = mon\text{-}e \ fg \ e 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}w\text{-}uncons\text{:} \ mon\text{-}w \ fg \ (wa@wb) = mon\text{-}w \ fg \ wa \cup mon\text{-}w \ fg \ wb 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}w\text{-}uncons[simp]\text{:} \ mon\text{-}w \ fg \ (e\# w) = mon\text{-}e \ fg \ e \cup mon\text{-}w \ fg \ w 
 \langle proof \rangle 
 | \mathbf{lemma} \ mon\text{-}w\text{-}ileq\text{:} \ w \preceq w' \implies mon\text{-}w \ fg \ w \subseteq mon\text{-}w \ fg \ w' 
 \langle proof \rangle
```

7.3 Valid configurations

We call a configuration valid if each monitor is owned by at most one thread.

${f definition}$

```
valid fg c == \forall s s'. \{\#s, s'\#\} \subseteq \# c \longrightarrow mon\text{--}s fg s \cap mon\text{--}s fg s' = \{\}
```

lemma valid-empty[simp, intro!]: $valid\ fg\ \{\#\}$ $\langle proof \rangle$

lemma valid-single[simp, intro!]: $valid fg \{\#s\#\}$ $\langle proof \rangle$

lemma valid-split1:

$$valid\ fg\ (c+c') \Longrightarrow valid\ fg\ c \land valid\ fg\ c' \land mon-c\ fg\ c \cap mon-c\ fg\ c' = \{\} \ \langle proof \rangle$$

lemma valid-split2:

$$\llbracket valid\ fg\ c;\ valid\ fg\ c';\ mon-c\ fg\ c\cap mon-c\ fg\ c'=\{\} \rrbracket \Longrightarrow valid\ fg\ (c+c')$$
 $\langle proof \rangle$

lemma valid-union-conc:

```
valid fg (c+c') \longleftrightarrow (valid fg c \land valid fg c' \land mon-c fg c \cap mon-c fg c' = \{\})
\langle proof \rangle
```

 $\mathbf{lemma}\ valid\text{-}add\text{-}mset\text{-}conc:$

```
valid fg (add-mset \ x \ c') \longleftrightarrow (valid fg \ c' \land mon-s \ fg \ x \cap mon-c \ fg \ c' = \{\})\langle proof \rangle
```

 $lemmas \ valid$ -unconc = valid-union-conc valid-add-mset-conc

```
lemma valid-no-mon: mon-c fg c = \{\} \Longrightarrow valid fg \ c \ \langle proof \rangle
```

7.4 Configurations at control points

```
primrec atU-s:: 'n \ set \Rightarrow 'n \ list \Rightarrow bool \ \mathbf{where}
  atU-s U = False
\mid atU-s U (u\#r) = (u\in U)
lemma atU-s-decomp[simp]: atU-s U (s@s') = (atU-s U s \lor (s=[] \land atU-s U s'))
  \langle proof \rangle
definition
  atU\ U\ c == \exists s.\ s \in \#\ c \land atU\text{-}s\ U\ s
lemma atU-fmt: \llbracket atU\ U\ c; \, !!ui\ r.\ \llbracket ui\#r\in\#\ c;\ ui\in U\rrbracket \Longrightarrow P\rrbracket \Longrightarrow P
  \langle proof \rangle
lemma at U-union-cases [case-names left right, consumes 1]:
    atU \ U \ (c1+c2);
    atU\ U\ c1 \Longrightarrow P;
    atU\ U\ c2 \Longrightarrow P
  \mathbb{I} \Longrightarrow P
  \langle proof \rangle
lemma atU-add: atU \ U \ c \Longrightarrow atU \ U \ (c+ce) \land atU \ U \ (ce+c)
  \langle proof \rangle
lemma atU-union[simp]: atU \ U \ (c1+c2) = (atU \ U \ c1 \lor atU \ U \ c2)
lemma atU-empty[simp]: \neg atU \ U \ \{\#\}
  \langle proof \rangle
lemma atU-single[simp]: atU \ U \ \{\#s\#\} = atU-s U \ s
  \langle proof \rangle
lemma atU-single-top[simp]: atU\ U\ \{\#u\#r\#\} = (u \in U)
  \langle proof \rangle
lemma atU-add-mset[simp]: atU\ U\ (add-mset\ c\ c2) = (atU-s\ U\ c\ \lor\ atU\ U\ c2)
  \langle proof \rangle
lemma at U-xchange-stack: at U U (add\text{-mset }(u\#r)\ c) \Longrightarrow at U\ U\ (add\text{-mset }(u\#r')
c)
  \langle proof \rangle
definition
  atUV\ U\ V\ c == \exists\ su\ sv.\ \{\#su\#\} + \{\#sv\#\} \subseteq \#\ c \land\ atU\text{-}s\ U\ su\ \land\ atU\text{-}s\ V\ sv
lemma atUV-empty[simp]: \neg atUV U V {\#}
  \langle proof \rangle
```

```
lemma atUV-single[simp]: \neg atUV U V \{\#s\#\}
  \langle proof \rangle
lemma atUV-union[simp]:
  atUV U V (c1+c2) \longleftrightarrow
     (atUV\ U\ V\ c1)\ \lor
     (atUV \ U \ V \ c2) \ \lor
     (atU\ U\ c1\ \wedge\ atU\ V\ c2)\ \lor
    (atU\ V\ c1\ \wedge\ atU\ U\ c2)
  \langle proof \rangle
lemma atUV-add-mset[simp]:
  atUV \ U \ V \ (add\text{-}mset \ c \ c2) \longleftrightarrow
     (atUV \ U \ V \ c2) \ \lor
     (atU\ U\ \{\#c\#\}\ \land\ atU\ V\ c2)\ \lor
    (atU\ V\ \{\#c\#\}\ \land\ atU\ U\ c2)
  \langle proof \rangle
lemma at UV-union-cases [case-names left right lr rl, consumes 1]: [
    atUV U V (c1+c2);
    atUV \ U \ V \ c1 \Longrightarrow P;
    atUV \ U \ V \ c2 \Longrightarrow P;
     \llbracket atU\ U\ c1;\ atU\ V\ c2 \rrbracket \Longrightarrow P;
    \llbracket atU\ V\ c1;\ atU\ U\ c2 \rrbracket \Longrightarrow P
  \rrbracket \Longrightarrow P
  \langle proof \rangle
```

7.5 Operational semantics

7.5.1 Semantic reference point

We now define our semantic reference point. We assess correctness and completeness of analyses relative to this reference point.

inductive-set

```
refpoint :: ('n,'p,'ba,'m,'more) flowgraph-rec-scheme \Rightarrow ('n\ conf\ \times\ ('p,'ba)\ label\ \times\ 'n\ conf)\ set for fg where
```

— A base edge transforms the top node of one stack and leaves the other stacks untouched.

```
refpoint-base: [(u,Base\ a,v) \in edges\ fg;\ valid\ fg\ (\{\#u\#r\#\}+c)\ ]] \Longrightarrow (add\text{-}mset\ (u\#r)\ c,LBase\ a,add\text{-}mset\ (v\#r)\ c) \in refpoint\ fg\ |
```

— A call edge transforms the top node of a stack and then pushes the entry node of the called procedure onto that stack. It can only be executed if all monitors the called procedure synchronizes on are available. Reentrant monitors are modeled

here by checking availability of monitors just against the other stacks, not against the stack of the thread that executes the call. The other stacks are left untouched.

```
refpoint-call: [(u,Call\ p,v) \in edges\ fg;\ valid\ fg\ (\{\#u\#r\#\}+c);\ mon\ fg\ p\cap mon\-c\ fg\ c=\{\}\ ]
```

- \implies (add-mset (u#r) c,LCall p, add-mset (entry fg p#v#r) c) \in refpoint fg |
- A return step pops a return node from a stack. There is no corresponding flowgraph edge for a return step. The other stacks are left untouched.

```
refpoint-ret: \llbracket valid fg (\{\#return fg p \#r \#\} + c) \rrbracket
```

- \implies $(add\text{-}mset\ (return\ fg\ p\#r)\ c,LRet,(add\text{-}mset\ r\ c)) \in refpoint\ fg\ |$
- A spawn edge transforms the top node of a stack and adds a new stack to the environment, with the entry node of the spawned procedure at the top and no stored return addresses. The other stacks are also left untouched.

```
refpoint-spawn: [(u,Spawn\ p,v)\in edges\ fg;\ valid\ fg\ (add-mset\ (u\#r)\ c)\ ] \implies (add-mset\ (u\#r)\ c,LSpawn\ p,\ add-mset\ (v\#r)\ (add-mset\ [entry\ fg\ p]\ c))\in refpoint\ fg
```

Instead of working directly with the reference point semantics, we define the operational semantics of flowgraphs by describing how a single stack is transformed in a context of environment threads, and then use the theory developed in Section 5 to derive an interleaving semantics. Note that this semantics is also defined for invalid configurations (cf. Section 7.3). In Section 7.6.1 we will show that it preserves validity of a configuration, and in Section 7.6.2 we show that it is equivalent to the reference point semantics on valid configurations.

$inductive\hbox{-} \mathbf{set}$

```
trss :: ('n,'p,'ba,'m,'more) \ flowgraph-rec-scheme \Rightarrow \\ (('n \ list * 'n \ conf) * ('p,'ba) \ label * ('n \ list * 'n \ conf)) \ set for fg where trss-base: \llbracket (u,Base \ a,v) \in edges \ fg \rrbracket \Longrightarrow \\ ((u\#r,c), LBase \ a, (v\#r,c)) \in trss \ fg \\ \mid trss-call: \llbracket (u,Call \ p,v) \in edges \ fg; \ mon \ fg \ p \cap mon-c \ fg \ c = \{\} \ \rrbracket \Longrightarrow \\ ((u\#r,c),LCall \ p, ((entry \ fg \ p)\#v\#r,c)) \in trss \ fg \\ \mid trss-ret: ((((return \ fg \ p)\#r),c),LRet,(r,c)) \in trss \ fg \\ \mid trss-spawn: \llbracket (u,Spawn \ p,v) \in edges \ fg \ \rrbracket \Longrightarrow \\ ((u\#r,c),LSpawn \ p,(v\#r,add-mset \ [entry \ fg \ p] \ c)) \in trss \ fg
```

— The interleaving semantics is generated using the general techniques from Section 5

```
abbreviation tr where tr fg == gtr (trss fg)
— We also generate the loc/env-semantics
abbreviation trp where trp fg == gtrp (trss fg)
```

7.6 Basic properties

7.6.1 Validity

 $\langle proof \rangle$

```
\mathbf{lemma} \ (\mathbf{in} \ \mathit{flowgraph}) \ \mathit{trss-valid-preserve-s} \colon
  \llbracket valid\ fg\ (add\text{-}mset\ s\ c);\ ((s,c),e,(s',c'))\in trss\ fg \rrbracket \implies valid\ fg\ (add\text{-}mset\ s'\ c')
  \langle proof \rangle
lemma (in flowgraph) trss-valid-preserve:
  \llbracket ((s,c),w,(s',c')) \in trcl \ (trss\ fg);\ valid\ fg\ (\{\#s\#\}+c) \rrbracket \implies valid\ fg\ (\{\#s'\#\}+c')
  \langle proof \rangle
lemma (in flowgraph) tr-valid-preserve-s:
  \llbracket (c,e,c') \in tr \ fg; \ valid \ fg \ c \rrbracket \implies valid \ fg \ c'
  \langle proof \rangle
lemma (in flowgraph) tr-valid-preserve:
  \llbracket (c, w, c') \in trcl \ (tr \ fg); \ valid \ fg \ c \rrbracket \implies valid \ fg \ c'
  \langle proof \rangle
lemma (in flowgraph) trp-valid-preserve-s:
  [((s,c),e,(s',c')) \in trp\ fg;\ valid\ fg\ (add-mset\ s\ c)] \implies valid\ fg\ (add-mset\ s'\ c')
  \langle proof \rangle
lemma (in flowgraph) trp-valid-preserve:
  \llbracket ((s,c),w,(s',c')) \in trcl \ (trp\ fg);\ valid\ fg\ (\{\#s\#\}+c) \rrbracket \implies valid\ fg\ (add-mset\ s'\ c')
  \langle proof \rangle
7.6.2
             Equivalence to reference point
lemma refpoint-eq-s: valid fg c \Longrightarrow ((c,e,c') \in refpoint fg) \longleftrightarrow ((c,e,c') \in tr fg)
  \langle proof \rangle
\mathbf{lemma} \ (\mathbf{in} \ \mathit{flowgraph}) \ \mathit{refpoint-eq} :
  valid fg c \Longrightarrow ((c,w,c') \in trcl \ (refpoint \ fg)) \longleftrightarrow ((c,w,c') \in trcl \ (tr \ fg))
\langle proof \rangle
             Case distinctions
7.6.3
lemma trss-c-cases-s[cases\ set,\ case-names\ no-spawn\ spawn]:
     ((s,c),e,(s',c')) \in trss\ fg;
     \llbracket c'=c \rrbracket \Longrightarrow P;
     !!p\ u\ v.\ [e=LSpawn\ p;\ (u,Spawn\ p,v)\in edges\ fg;
                  hd \ s=u; \ hd \ s'=v; \ c'=\{\#[\ entry\ fg\ p\ ]\#\}+c\ ] \Longrightarrow P
  \rrbracket \Longrightarrow P
\langle proof \rangle
lemma trss-c-fmt-s: [((s,c),e,(s',c')) \in trss fg]
  \implies \exists csp. c' = csp + c \land
```

 $(csp=\{\#\} \lor (\exists p. \ e=LSpawn \ p \land csp=\{\#[\ entry \ fg \ p \]\#\}))$

```
((s,c),e,(s',c')) \in trss\ fg;
    !!csp. \ \llbracket c' = csp + c; \ mon-c \ fg \ csp = \{\} \rrbracket \implies P
  \langle proof \rangle
lemma trss-c-cases[cases set, case-names c-case]: !!s c. [
    ((s,c),w,(s',c')) \in trcl \ (trss \ fg);
    !!csp. \ \ \ \ c'=csp+c; \ !!s. \ s \in \# \ csp \Longrightarrow \exists \ p \ u \ v. \ s=[entry \ fg \ p] \ \land
                                                 (u,Spawn\ p,v) \in edges\ fg\ \land
                                                 initial proc fg p
 \rrbracket \Longrightarrow P
\langle proof \rangle
lemma (in flowgraph) c-of-initial-no-mon:
 assumes A: !!s. \ s \in \# \ csp \Longrightarrow \exists \ p. \ s = [entry \ fg \ p] \land initial proc \ fg \ p
 shows mon-c fg csp = \{\}
  \langle proof \rangle
lemma (in flowgraph) trss-c-no-mon-s:
  assumes A: ((s,c),e,(s',c')) \in trss\ fg
  shows mon-c fg c' = mon-c fg c
  \langle proof \rangle
corollary (in flowgraph) trss-c-no-mon:
  ((s,c),w,(s',c')) \in trcl \ (trss \ fg) \Longrightarrow mon-c \ fg \ c' = mon-c \ fg \ c
  \langle proof \rangle
lemma (in flowgraph) trss-spawn-no-mon-step[simp]:
  ((s,c),LSpawn\ p,\ (s',c')) \in trss\ fg \Longrightarrow mon\ fg\ p = \{\}
  \langle proof \rangle
lemma trss-no-empty-s[simp]: (([],c),e,(s',c')) \in trss\ fg = False
  \langle proof \rangle
lemma trss-no-empty[simp]:
  assumes A: (([],c),w,(s',c')) \in trcl \ (trss\ fg)
  shows w=[] \land s'=[] \land c=c'
\langle proof \rangle
lemma trs-step-cases[cases set, case-names NO-SPAWN SPAWN]:
 assumes A: (c,e,c') \in tr fg
 assumes A-NO-SPAWN: !!s ce s' csp.
```

```
 \begin{array}{c} ((s,ce),e,(s',ce)) {\in} trss \ fg; \\ c {=} \{\#s\#\} + ce; \ c' {=} \{\#s'\#\} + ce \\ \rrbracket \implies P \\ \textbf{assumes} \ A {-} SPAWN {:} \ !! s \ ce \ s' \ p. \ \llbracket \\ ((s,ce),LSpawn \ p,(s',\{\#[entry \ fg \ p]\#\} + ce)) {\in} trss \ fg; \\ c {=} \{\#s\#\} + ce; \\ c' {=} \{\#s'\#\} + \{\#[entry \ fg \ p]\#\} + ce; \\ e {=} LSpawn \ p \\ \rrbracket \implies P \\ \textbf{shows} \ P \\ \langle proof \rangle \end{array}
```

7.7 Advanced properties

7.7.1 Stack composition / decomposition

```
lemma trss-stack-comp-s:
  ((s,c),e,(s',c')) \in trss\ fg \Longrightarrow ((s@r,c),e,(s'@r,c')) \in trss\ fg
  \langle proof \rangle
lemma trss-stack-comp:
  ((s,c),w,(s',c')) \in trcl \ (trss \ fg) \Longrightarrow ((s@r,c),w,(s'@r,c')) \in trcl \ (trss \ fg)
\langle proof \rangle
lemma trss-stack-decomp-s: [ ((s@r,c),e,(s',c')) \in trss fg; s \neq [] ]
  \implies \exists sp'. \ s'=sp'@r \land ((s,c),e,(sp',c')) \in trss \ fg
  \langle proof \rangle
lemma trss-find-return: [
    ((s@r,c),w,(r,c')) \in trcl \ (trss \ fg);
    !!wa\ wb\ ch.\ \llbracket\ w=wa@wb;\ ((s,c),wa,(\llbracket,ch))\in trcl\ (trss\ fg);
                      ((r,ch),wb,(r,c')) \in trcl \ (trss \ fg) \ ] \Longrightarrow P
  \rrbracket \Longrightarrow P
   — If s = [], the proposition follows trivially
  \langle proof \rangle
lemma trss-return-cases[cases\ set]: !!u\ r\ c.\ [
     ((u\#r,c),w,(r',c'))\in trcl\ (trss\ fg);
    !! \ s' \ u'. \ \llbracket \ r' = s'@u' \# r; \ (([u],c),w,(s'@[u'],c')) \in trcl \ (trss \ fg) \ \rrbracket \Longrightarrow P;
    !! wa wb ch. \llbracket w=wa@wb; ((\llbracket u \rrbracket,c),wa,(\llbracket ,ch)) \in trcl (trss fg);
                       ((r,ch),wb,(r',c'))\in trcl\ (trss\ fg)\ \rrbracket \Longrightarrow P
  \rrbracket \Longrightarrow P
\langle proof \rangle
lemma (in flowgraph) trss-find-call:
  !!v \ r' \ c'. \ [ (([sp],c),w,(v\#r',c')) \in trcl \ (trss \ fg); \ r'\neq [] \ ]]
  \Longrightarrow \exists \ rh \ \ ch \ \ p \ \ wa \ \ wb.
         w=wa@(LCall\ p)\#wb \land
         proc\text{-}of fg \ v = p \land
```

```
(([sp],c),wa,(rh,ch)) \in trcl\ (trss\ fg)\ \land
        ((rh,ch),LCall\ p,((entry\ fg\ p)\#r',ch))\in trss\ fg\ \land
        (([entry\ fg\ p], ch), wb, ([v], c')) \in trcl\ (trss\ fg)
\langle proof \rangle
lemma (in flowgraph) trss-find-call':
  assumes A: (([sp],c),w,(return\ fg\ p\#[u'],c'))\in trcl\ (trss\ fg)
  and EX: !!uh ch wa wb.
      w=wa@(LCall\ p)\#wb;
      (([sp],c),wa,([uh],ch)) \in trcl\ (trss\ fg);
      (([uh], ch), LCall\ p, ((entry\ fg\ p)\#[u'], ch)) \in trss\ fg;
      (uh, Call\ p, u') \in edges\ fg;
      (([entry\ fg\ p], ch), wb, ([return\ fg\ p], c')) \in trcl\ (trss\ fg)
  shows P
\langle proof \rangle
lemma (in flowgraph) trss-bot-proc-const:
  !!s' u' c'. ((s@[u],c),w,(s'@[u'],c')) \in trcl (trss fg)
    \implies proc\text{-}of fg \ u = proc\text{-}of fg \ u'
\langle proof \rangle
lemma (in flowgraph) trss-er-path-proc-const:
  (([entry\ fg\ p],c),w,([return\ fg\ q],c'))\in trcl\ (trss\ fg) \Longrightarrow p=q
  \langle proof \rangle
lemma trss-2empty-to-2return: [(s,c),w,([],c'))\in trcl\ (trss\ fg);\ s\neq []\ ] \Longrightarrow
  \exists \ w' \ p. \ w=w'@[LRet] \ \land \ ((s,c),w',([return\ fg\ p],c')) \in trcl\ (trss\ fg)
\langle proof \rangle
lemma trss-2return-to-2empty: [(s,c),w,([return\ fg\ p],c'))\in trcl\ (trss\ fg)\ ]
  \implies ((s,c),w@[LRet],([],c')) \in trcl \ (trss\ fg)
  \langle proof \rangle
7.7.2
           Adding threads
lemma trss-env-increasing-s: ((s,c),e,(s',c')) \in trss\ fg \implies c \subseteq \#c'
  \langle proof \rangle
lemma trss-env-increasing: ((s,c),w,(s',c'))\in trcl\ (trss\ fg)\implies c\subseteq\#c'
  \langle proof \rangle
7.7.3
          Conversion between environment and monitor restrictions
lemma trss-mon-e-no-ctx:
  ((s,c),e,(s',c')) \in trss\ fg \Longrightarrow mon-e\ fg\ e\cap mon-c\ fg\ c=\{\}
lemma (in flowgraph) trss-mon-w-no-ctx:
  ((s,c),w,(s',c')) \in trcl \ (trss \ fg) \Longrightarrow mon-w \ fg \ w \cap mon-c \ fg \ c = \{\}
  \langle proof \rangle
lemma (in flowgraph) trss-modify-context-s:
  !!cn. [((s,c),e,(s',c')) \in trss \ fg; \ mon-e \ fg \ e \cap mon-c \ fg \ cn = \{\}]
```

```
\implies \exists csp. \ c'=csp+c \land mon-c \ fg \ csp = \{\} \land ((s,cn),e,(s',csp+cn)) \in trss \ fg
  \langle proof \rangle
lemma (in flowgraph) trss-modify-context[rule-format]:
  \llbracket ((s,c),w,(s',c')) \in trcl \ (trss \ fg) \rrbracket
  \implies \forall cn. mon-w fg w \cap mon-c fg cn = \{\}
      \longrightarrow (\exists csp. \ c'=csp+c \land mon-c fg \ csp = \{\} \land
                   ((s,cn),w,(s',csp+cn)) \in trcl\ (trss\ fg))
\langle proof \rangle
{f lemma}\ trss-add\text{-}context\text{-}s:
  \llbracket ((s,c),e,(s',c')) \in trss\ fg;\ mon-e\ fg\ e\ \cap\ mon-c\ fg\ ce\ =\ \{\} \rrbracket
    \implies ((s,c+ce),e,(s',c'+ce)) \in trss\ fg
  \langle proof \rangle
lemma trss-add-context:
  \llbracket ((s,c),w,(s',c')) \in trcl \ (trss \ fg); \ mon\text{-}w \ fg \ w \ \cap \ mon\text{-}c \ fg \ ce \ = \ \{\} \rrbracket
    \implies ((s,c+ce),w,(s',c'+ce)) \in trcl \ (trss \ fg)
\langle proof \rangle
lemma trss-drop-context-s: [((s,c+ce),e,(s',c'+ce)) \in trss fg]
  \implies ((s,c),e,(s',c')) \in trss\ fg \land mon-e\ fg\ e \cap mon-c\ fg\ ce = \{\}
  \langle proof \rangle
lemma trss-drop-context: !!s c. [((s,c+ce),w,(s',c'+ce)) \in trcl (trss fg)]
  \implies ((s,c),w,(s',c')) \in trcl \ (trss \ fg) \land mon-w \ fg \ w \cap mon-c \ fg \ ce = \{\}
\langle proof \rangle
{\bf lemma}\ trss\text{-}xchange\text{-}context\text{-}s\text{:}
  assumes A: ((s,c),e,(s',csp+c)) \in trss\ fg
  and M:mon-c \ fg \ cn \subseteq mon-c \ fg \ c
  shows ((s,cn),e,(s',csp+cn)) \in trss\ fg
\langle proof \rangle
lemma trss-xchange-context:
  assumes A: ((s,c),w,(s',csp+c)) \in trcl\ (trss\ fg)
  and M:mon-c\ fg\ cn\subseteq mon-c\ fg\ c
  shows ((s,cn),w,(s',csp+cn))\in trcl\ (trss\ fg)
\langle proof \rangle
lemma trss-drop-all-context-s[cases set, case-names dropped]:
  assumes A: ((s,c),e,(s',c')) \in trss\ fg
  and C: !!csp. \ [ c'=csp+c; ((s,{\#}),e,(s',csp)) \in trss \ fg \ ] \Longrightarrow P
  shows P
\langle proof \rangle
lemma trss-drop-all-context[cases set, case-names dropped]:
  assumes A: ((s,c),w,(s',c')) \in trcl \ (trss \ fg)
  and C: !!csp. [ c'=csp+c; ((s,\{\#\}), w, (s',csp)) \in trcl (trss fg) ] \Longrightarrow P
```

 \mathbf{end}

8 Normalized Paths

theory Normalization imports Main ThreadTracking Semantics ConsInterleave begin

The idea of normalized paths is to consider particular schedules only. While the original semantics allows a context switch to occur after every single step, we now define a semantics that allows context switches only before non-returning calls or after a thread has reached its final stack. We then show that this semantics is able to reach the same set of configurations as the original semantics.

8.1 Semantic properties of restricted flowgraphs

It makes the formalization smoother, if we assume that every thread's execution begins with a non-returning call. For this purpose, we defined syntactic restrictions on flowgraphs already (cf. Section 6.3). We now show that these restrictions have the desired semantic effect.

```
lemma (in eflowgraph) iso-ret-no-ret: !!u c. [
    isolated-ret fg p;
    proc-of fg u = p;
    u \neq return fg p;
    (([u],c),w,([return fg p'],c'))\int trcl (trss fg)
    ] \impsize False
    \langle proof \rangle
lemma (in eflowgraph) initial-starts-with-call:
    [ (([entry fg p],c),e,(s',c'))\int trss fg; initialproc fg p ]]
    \impsize \eta p' \cdot e=LCall p' \wedge isolated-ret fg p'
    \langle proof \rangle
lemma (in eflowgraph) no-sl-from-initial:
    assumes A: w\neq [] initialproc fg p
    \left( ([entry fg p],c),w,([v],c'))\int trcl (trss fg)
```

```
shows False \langle proof \rangle lemma (in eflowgraph) no-retsl-from-initial: assumes A \colon w \neq [] initialproc fg \ p (([entry fg \ p],c),w,(r',c'))\in trcl \ (trss \ fg) length r' \leq 1 shows False \langle proof \rangle
```

8.2 Definition of normalized paths

In order to describe the restricted schedules, we define an operational semantics that performs an atomically scheduled sequence of steps in one step, called a *macrostep*. Context switches may occur after macrosteps only. We call this the *normalized semantics* and a sequence of macrosteps a *normalized path*.

Since we ensured that every path starts with a non-returning call, we can define a macrostep as an initial call followed by a same-level path² of the called procedure. This has the effect that context switches are either performed before a non-returning call (if the thread makes a further macrostep in the future) or after the thread has reached its final configuration.

As for the original semantics, we first define the normalized semantics on a single thread with a context and then use the theory developed in Section 5 to derive interleaving semantics on multisets and configurations with an explicit local thread (loc/env-semantics, cf. Section 5.4).

```
inductive-set
```

```
ntrs:: ('n,'p,'ba,'m,'more) \ flowgraph-rec-scheme \Rightarrow (('n\ list \times 'n\ conf) \times ('p,'ba)\ label\ list \times ('n\ list \times 'n\ conf))\ set for fg where — A macrostep transforms one thread by first calling a procedure and then doing a same-level path
```

```
ntrs-step: [((u\#r,ce),LCall\ p,\ (entry\ fg\ p\ \#\ u'\ \#\ r,ce))\in trss\ fg;
(([entry\ fg\ p],ce),w,([v],ce'))\in trcl\ (trss\ fg)]]\Longrightarrow
((u\#r,ce),LCall\ p\#w,(v\#u'\#r,ce'))\in ntrs\ fg
```

```
abbreviation ntr where ntr fg == gtr (ntrs fg)
abbreviation ntrp where ntrp fg == gtrp (ntrs fg)
```

```
interpretation ntrs: env-no-step ntrs fg \ \langle proof \rangle
```

²Same-level paths are paths with balanced calls and returns. The stack-level at the beginning of their execution is the same as at the end, and during the execution, the stack never falls below the initial level.

8.3 Representation property for reachable configurations

In this section, we show that a configuration is reachable if and only if it is reachable via a normalized path.

The first direction is to show that a normalized path is also a path. This follows from the definitions. Note that we first show that a single macrostep corresponds to a path and then generalize the result to sequences of macrosteps

```
lemma ntrs-is-trss-s: ((s,c),w,(s',c'))\in ntrs\ fg \Longrightarrow ((s,c),w,(s',c'))\in trcl\ (trss\ fg) \langle proof \rangle
```

```
lemma ntrs-is-trss: ((s,c),w,(s',c'))\in trcl (ntrs fg)

\implies ((s,c),foldl (@) [] w,(s',c'))\in trcl (trss fg)

\langle proof \rangle

lemma ntr-is-tr-s: (c,w,c')\in ntr fg \implies (c,w,c')\in trcl (tr fg)
```

lemma ntr-is-tr: $(c, ww, c') \in trcl (ntr fg) \Longrightarrow (c, foldl (@) [] ww, c') \in trcl (tr fg)$

The other direction requires to prove that for each path reaching a configuration there is also a normalized path reaching the same configuration. We need an auxiliary lemma for this proof, that is a kind of append rule: Given a normalized path reaching some configuration c, and a same level or returning path from some stack in c, we can derive a normalized path to c modified according to the same-level path. We cannot simply append the same-level or returning path as a macrostep, because it does not start with a non-returning call. Instead, we will have to append it to some macrostep in the normalized path, i.e. move it "left" into the normalized path.

Intuitively, we can describe the concept of the proof as follows: Due to the restrictions we made on flowgraphs, a same-level or returning path cannot be the first steps on a thread. Hence there is a last macrostep that was executed on the thread. When this macrostep was executed, all threads held less monitors then they do at the end of the execution, because the set of monitors held by every single thread is increasing during the execution of a normalized path. Thus we can append the same-level or returning path to the last macrostep on that thread. As a same-level or returning path does not allocate any monitors, the following macrosteps remain executable. If we have a same-level path, appending it to a macrostep yields a valid macrostep again and we are done. Appending a returning path to a macrostep yields a same-level path. In this case we inductively repeat our argument.

The actual proof is strictly inductive; it either appends the same-level path to the *last* macrostep or inductively repeats the argument.

lemma (in eflowgraph) ntr-sl-move-left: !!ce u r w r' ce'.

Finally we can prove: Any reachable configuration can also be reached by a normalized path. With eflowgraph.ntr-sl-move-left we can easily show this lemma With eflowgraph.ntr-sl-move-left we can easily show this by induction on the reaching path. For the empty path, the proposition follows trivially. Else we consider the last step. If it is a call, we can execute it as a macrostep and get the proposition. Otherwise the last step is a same-level (Base, Spawn) or returning (Ret) path of length 1, and we can append it to the normalized path using eflowgraph.ntr-sl-move-left.

```
lemma (in eflowgraph) normalize: [ (cstart, w, c') \in trcl \ (tr \ fg); cstart = \{ \# \ [entry \ fg \ p] \ \# \}; initialproc \ fg \ p \ ] \Longrightarrow \exists \ w'. \ (\{ \# \ [entry \ fg \ p] \ \# \}, w', c') \in trcl \ (ntr \ fg) \longrightarrow The lemma is shown by induction on the reaching path \langle proof \rangle
```

As the main result of this section we get: A configuration is reachable if and only if it is also reachable via a normalized path:

```
theorem (in eflowgraph) ntr-repr:

(\exists w. (\{\#[entry fg (main fg)]\#\}, w, c) \in trcl (tr fg))

\longleftrightarrow (\exists w. (\{\#[entry fg (main fg)]\#\}, w, c) \in trcl (ntr fg))

\langle proof \rangle
```

8.4 Properties of normalized path

Like a usual path, also a macrostep modifies one thread, spawns some threads and preserves the state of all the other threads. The spawned threads do not make any steps, thus they stay in their initial configurations.

```
 \begin{array}{c} \textbf{lemma} \ ntrs\text{-}c\text{-}cases\text{-}s[cases\ set]\text{:}\ \llbracket\\ & ((s,c),w,(s',c'))\in ntrs\ fg;\\ & \text{!!}csp.\ \llbracket\ c'=csp+c;\ \text{!!}s.\ s\in\#\ csp\implies\exists\ p\ u\ v.\ s=[entry\ fg\ p]\ \land\\ & (u,Spawn\ p,v)\in edges\ fg\ \land\\ & initialproc\ fg\ p\\ \rrbracket\implies P\\ & \P\implies P\\ & \langle proof \rangle \\ \\ \textbf{lemma} \ ntrs\text{-}c\text{-}cases[cases\ set]\text{:}\ \llbracket\\ & ((s,c),ww,(s',c'))\in trcl\ (ntrs\ fg);\\ & \text{!!}csp.\ \llbracket\ c'=csp+c;\ \text{!!}s.\ s\in\#\ csp\implies\exists\ p\ u\ v.\ s=[entry\ fg\ p]\ \land \\ \end{array}
```

8.4.1 Validity

Like usual paths, also normalized paths preserve validity of the configurations.

```
lemmas (in flowgraph) ntrs-valid-preserve-s = trss-valid-preserve[OF ntrs-is-trss-s] lemmas (in flowgraph) ntrs-valid-preserve = tr-valid-preserve[OF ntrs-is-trss] lemmas (in flowgraph) ntrs-valid-preserve = trs-valid-preserve[OF ntrs-is-trss] lemmas (in flowgraph) ntrp-valid-preserve = tr-valid-preserve[OF ntr-is-tr] lemma (in flowgraph) ntrp-valid-preserve-s: assumes A: ((s,c),e,(s',c')) \in ntrp \ fg and V: valid \ fg \ (add-mset \ s \ c) shows valid fg \ (add-mset \ s' \ c') \langle proof \rangle lemma (in flowgraph) ntrp-valid-preserve: assumes A: ((s,c),e,(s',c')) \in trcl \ (ntrp \ fg) and V: valid \ fg \ (add-mset \ s \ c) shows valid fg \ (add-mset \ s \ c) shows valid fg \ (add-mset \ s' \ c') \langle proof \rangle
```

8.4.2 Monitors

The following defines the set of monitors used by a normalized path and shows its basic properties:

```
definition
mon\text{-}ww\ fg\ ww == foldl\ (\cup)\ \{\}\ (map\ (mon\text{-}w\ fg)\ ww)

definition
mon\text{-}loc\ fg\ ww == mon\text{-}ww\ fg\ (map\ le\text{-}rem\text{-}s\ (loc\ ww))

definition
mon\text{-}env\ fg\ ww == mon\text{-}ww\ fg\ (map\ le\text{-}rem\text{-}s\ (env\ ww))

lemma mon\text{-}ww\text{-}empty[simp]\text{:}\ mon\text{-}ww\ fg\ [] = \{\}\ \langle proof\rangle

lemma mon\text{-}ww\text{-}uncons[simp]\text{:}\ mon\text{-}ww\ fg\ (ee\#ww) = mon\text{-}w\ fg\ ee\ \cup\ mon\text{-}ww\ fg\ ww
\langle proof\rangle

lemma mon\text{-}ww\text{-}unconc\text{:}\ mon\text{-}ww\ fg\ (ww1\@ww2) = mon\text{-}ww\ fg\ ww1\ \cup\ mon\text{-}ww\ fg\ ww2}
\langle proof\rangle

lemma mon\text{-}env\text{-}empty[simp]\text{:}\ mon\text{-}env\ fg\ [] = \{\}
```

```
\langle proof \rangle
\mathbf{lemma} \ mon\text{-}env\text{-}single[simp]:
  mon\text{-}env\ fg\ [e] = (case\ e\ of\ LOC\ a \Rightarrow \{\} \mid ENV\ a \Rightarrow mon\text{-}w\ fg\ a)
lemma mon\text{-}env\text{-}uncons[simp]:
  mon\text{-}env\ fg\ (e\#w)
   = (case\ e\ of\ LOC\ a \Rightarrow \{\} \mid ENV\ a \Rightarrow mon-w\ fg\ a) \cup mon-env\ fg\ w
lemma mon-env-unconc:
  mon\text{-}env\ fg\ (w1@w2) = mon\text{-}env\ fg\ w1\ \cup\ mon\text{-}env\ fg\ w2
  \langle proof \rangle
\mathbf{lemma} \ \mathit{mon-loc-empty}[\mathit{simp}] \colon \mathit{mon-loc} \ \mathit{fg} \ [] = \{\}
  \langle proof \rangle
lemma mon-loc-single[simp]:
  mon\text{-}loc\ fg\ [e] = (case\ e\ of\ ENV\ a \Rightarrow \{\} \mid LOC\ a \Rightarrow mon\text{-}w\ fg\ a)
lemma mon-loc-uncons[simp]:
  mon-loc fg (e\#w)
  = (case\ e\ of\ ENV\ a\Rightarrow \{\}\mid LOC\ a\Rightarrow mon-w\ fg\ a)\cup mon-loc\ fg\ w
lemma mon-loc-unconc:
  mon\text{-}loc\ fg\ (w1@w2) = mon\text{-}loc\ fg\ w1\ \cup\ mon\text{-}loc\ fg\ w2
  \langle proof \rangle
lemma mon-ww-of-foldl[simp]: mon-w fg (foldl (@) [] ww) = mon-ww fg ww
  \langle proof \rangle
lemma mon-ww-ileq: w \leq w' \Longrightarrow mon\text{-}ww fg \ w \subseteq mon\text{-}ww fg \ w'
  \langle proof \rangle
lemma mon-ww-cil:
  w \in w1 \otimes_{\alpha} w2 \implies mon\text{-}ww fg w = mon\text{-}ww fg w1 \cup mon\text{-}ww fg w2
  \langle proof \rangle
lemma mon-loc-cil:
  w \in w1 \otimes_{\alpha} w2 \implies mon\text{-loc fg } w = mon\text{-loc fg } w1 \cup mon\text{-loc fg } w2
  \langle proof \rangle
lemma mon-env-cil:
  w \in w1 \otimes_{\alpha} w2 \implies mon\text{-}env fg w = mon\text{-}env fg w1 \cup mon\text{-}env fg w2
  \langle proof \rangle
lemma mon-ww-of-le-rem:
  mon\text{-}ww fg (map le\text{-}rem\text{-}s w) = mon\text{-}loc fg w \cup mon\text{-}env fg w
  \langle proof \rangle
lemma mon-env-ileq: w \leq w' \Longrightarrow mon-env fg w \subseteq mon-env fg w'
  \langle proof \rangle
```

```
lemma mon-loc-ileq: w \leq w' \Longrightarrow mon-loc fg w \subseteq mon-loc fg w'
  \langle proof \rangle
lemma mon-loc-map-loc[simp]: mon-loc fg (map LOC w) = mon-ww fg w
lemma mon-env-map-env[simp]: mon-env fg (map ENV w) = mon-ww fg w
lemma mon-loc-map-env[simp]: mon-loc fg (map ENV w) = {}
lemma mon\text{-}env\text{-}map\text{-}loc[simp]: mon\text{-}env fg (map LOC w) = {}
  \langle proof \rangle
lemma (in flowgraph) ntrs-mon-increasing-s: ((s,c),e,(s',c')) \in ntrs\ fg
 \implies mon-s fg s \subseteq mon-s fg s' \land mon-c fg c = mon-c fg c'
 \langle proof \rangle
lemma (in flowgraph) ntr-mon-increasing-s:
  (c,ee,c') \in ntr\ fg \Longrightarrow mon-c\ fg\ c \subseteq mon-c\ fg\ c'
  \langle proof \rangle
lemma (in flowgraph) ntrp-mon-increasing-s: ((s,c),e,(s',c')) \in ntrp\ fg
 \implies mon-s fg s \subseteq mon-s fg s' \land mon-c fg c \subseteq mon-c fg c'
  \langle proof \rangle
lemma (in flowgraph) ntrp-mon-increasing: ((s,c),e,(s',c')) \in trcl (ntrp fg)
  \implies mon-s fg s \subseteq mon-s fg s' \land mon-c fg c \subseteq mon-c fg c'
 \langle proof \rangle
         Modifying the context
8.4.3
lemmas (in flowgraph) ntrs-c-no-mon-s = trss-c-no-mon[OF ntrs-is-trss-s]
lemmas (in flowgraph) ntrs-c-no-mon = trss-c-no-mon[OF ntrs-is-trss]
Also like a usual path, a normalized step must not use any monitors that
are allocated by other threads
lemmas (in flowgraph) ntrs-mon-e-no-ctx = trss-mon-w-no-ctx[OF <math>ntrs-is-trss-s]
lemma (in flowgraph) ntrs-mon-w-no-ctx:
 assumes A: ((s,c),w,(s',c')) \in trcl \ (ntrs \ fg)
 shows mon\text{-}ww fg w \cap mon\text{-}c fg c = \{\}
  \langle proof \rangle
lemma (in flowgraph) ntrp-mon-env-e-no-ctx:
  ((s,c),ENV\ e,(s',c'))\in ntrp\ fg \Longrightarrow mon\ w\ fg\ e\cap mon\ s\ fg\ s=\{\}
lemma (in flowgraph) ntrp-mon-loc-e-no-ctx:
  ((s,c),LOC\ e,(s',c')) \in ntrp\ fg \implies mon\ w\ fg\ e\cap mon\ c\ fg\ c=\{\}
  \langle proof \rangle
```

```
lemma (in flowgraph) ntrp-mon-env-w-no-ctx:
  ((s,c),w,(s',c')) \in trcl \ (ntrp \ fg) \Longrightarrow mon-env \ fg \ w \cap mon-s \ fg \ s = \{\}
  \langle proof \rangle
lemma (in flowgraph) ntrp-mon-loc-w-no-ctx:
  ((s,c),w,(s',c')) \in trcl \ (ntrp \ fg) \Longrightarrow mon-loc \ fg \ w \cap mon-c \ fg \ c = \{\}
  \langle proof \rangle
The next lemmas are rules how to add or remove threads while preserving
the executability of a path
lemma (in flowgraph) ntrs-modify-context-s:
 assumes A: ((s,c),ee,(s',c')) \in ntrs\ fg
 and B: mon-w fg \ ee \cap mon-c fg \ cn = \{\}
  shows \exists csp. c' = csp + c \land mon-c fg csp = \{\} \land ((s,cn),ee,(s',csp+cn)) \in ntrs fg
\langle proof \rangle
lemma (in flowgraph) ntrs-modify-context[rule-format]:
  [((s,c),w,(s',c')) \in trcl \ (ntrs \ fg)]
    \implies \forall cn. \ mon\text{-}ww \ fg \ w \cap mon\text{-}c \ fg \ cn = \{\}
        \longrightarrow (\exists csp. \ c'=csp+c \land mon-c \ fg \ csp = \{\} \land
              ((s,cn),w,(s',csp+cn)) \in trcl\ (ntrs\ fg))
\langle proof \rangle
lemma ntrs-xchange-context-s:
  assumes A: ((s,c),ee,(s',csp+c)) \in ntrs fg
 and B: mon-c fg cn \subseteq mon-c fg c
  shows ((s,cn),ee,(s',csp+cn)) \in ntrs fg
\langle proof \rangle
\mathbf{lemma}\ ntrs\text{-}replace\text{-}context\text{-}s\text{:}
  assumes A: ((s,c+cr),ee,(s',c'+cr)) \in ntrs\ fg
 and B: mon-c fg crn \subseteq mon-c fg cr
  shows ((s,c+crn),ee,(s',c'+crn)) \in ntrs fg
\langle proof \rangle
lemma (in flowgraph) ntrs-xchange-context: !!s c c' cn.
    ((s,c),ww,(s',c')) \in trcl \ (ntrs \ fg);
    mon-c fg cn \subseteq mon-c fg c
  ] \Longrightarrow \exists csp.
    c'=csp+c \land ((s,cn),ww,(s',csp+cn)) \in trcl (ntrs fg)
\langle proof \rangle
lemma (in flowgraph) ntrs-replace-context:
  assumes A: ((s,c+cr),ww,(s',c'+cr)) \in trcl (ntrs fg)
 and B: mon-c fg crn \subseteq mon-c fg cr
  shows ((s,c+crn),ww,(s',c'+crn)) \in trcl (ntrs fg)
\langle proof \rangle
```

```
lemma (in flowgraph) ntr-add-context-s:
  assumes A: (c,e,c') \in ntr fg
  and B: mon-w fg e \cap mon-c fg cn = \{\}
  shows (c+cn,e,c'+cn) \in ntr fg
\langle proof \rangle
lemma (in flowgraph) ntr-add-context:
  \llbracket (c,w,c') \in trcl \ (ntr \ fg); \ mon-ww \ fg \ w \cap mon-c \ fg \ cn = \{\} \rrbracket
    \implies (c+cn, w, c'+cn) \in trcl (ntr fg)
  \langle proof \rangle
lemma (in flowgraph) ntrs-add-context-s:
  assumes A: ((s,c),e,(s',c')) \in ntrs\ fg
 and B: mon-w fg \ e \cap mon-c fg \ cn = \{\}
 shows ((s,c+cn),e,(s',c'+cn))\in ntrs\ fg
  \langle proof \rangle
lemma (in flowgraph) ntrp-add-context-s:
  \llbracket ((s,c),e,(s',c')) \in ntrp \ fg; \ mon-w \ fg \ (le-rem-s \ e) \cap mon-c \ fg \ cn = \{\} \ \rrbracket
  \implies ((s,c+cn),e,(s',c'+cn)) \in ntrp\ fg
  \langle proof \rangle
lemma (in flowgraph) ntrp-add-context:
    ((s,c),w,(s',c')) \in trcl (ntrp fg);
    mon\text{-}ww fg (map le\text{-}rem\text{-}s w) \cap mon\text{-}c fg cn = \{\}
 \parallel \Longrightarrow ((s,c+cn),w,(s',c'+cn)) \in trcl (ntrp fg)
  \langle proof \rangle
8.4.4
          Altering the local stack
lemma ntrs-stack-comp-s:
 assumes A: ((s,c),ee,(s',c')) \in ntrs fg
 shows ((s@r,c),ee,(s'@r,c')) \in ntrs fg
  \langle proof \rangle
lemma ntrs-stack-comp: ((s,c),ww,(s',c')) \in trcl (ntrs fg)
  \implies ((s@r,c),ww,(s'@r,c')) \in trcl \ (ntrs \ fg)
  \langle proof \rangle
lemma (in flowgraph) ntrp-stack-comp-s:
  assumes A: ((s,c),ee,(s',c')) \in ntrp\ fg
  and B: mon-s fg \ r \cap mon-env fg \ [ee] = \{\}
  shows ((s@r,c),ee,(s'@r,c')) \in ntrp\ fg
  \langle proof \rangle
lemma (in flowgraph) ntrp-stack-comp:
  \llbracket ((s,c),ww,(s',c')) \in trcl \ (ntrp \ fg); \ mon-s \ fg \ r \cap mon-env \ fg \ ww = \{\} \ \rrbracket
    \implies ((s@r,c),ww,(s'@r,c')) \in trcl (ntrp fg)
```

```
\langle proof \rangle
\mathbf{lemma}\ ntrs\text{-}stack\text{-}top\text{-}decomp\text{-}s\text{:}
  assumes A: ((u\#r,c),ee,(s',c'))\in ntrs\ fg
  and EX: !!v u' p.
       s' = v \# u' \# r;
       (([u],c),ee,([v,u'],c')) \in ntrs\ fg;
       (u, Call\ p, u') \in edges\ fg
    \mathbb{I} \Longrightarrow P
  shows P
\langle proof \rangle
{f lemma} ntrs	ext{-}stack	ext{-}decomp	ext{-}s:
  assumes A: ((u\#s@r,c),ee,(s',c')) \in ntrs\ fg
  and EX: !!v u' p.
       s' = v \# u' \# s@r;
       ((u\#s,c),ee,(v\#u'\#s,c')) \in ntrs\ fg;
       (u,Call\ p,u') \in edges\ fg
    \mathbb{I} \Longrightarrow P
  shows P
  \langle proof \rangle
lemma ntrs-stack-decomp: !!u s r c P.
    ((u\#s@r,c),ww,(s',c'))\in trcl\ (ntrs\ fg);
    !!v rr. [s'=v\#rr@r; ((u\#s,c),ww,(v\#rr,c')) \in trcl (ntrs fg)] \Longrightarrow P
  \rrbracket \Longrightarrow P
\langle proof \rangle
\mathbf{lemma}\ ntrp\text{-}stack\text{-}decomp\text{-}s\text{:}
  assumes A: ((u\#s@r,c),ee,(s',c')) \in ntrp\ fg
  and EX: !!v \ rr. \ \llbracket \ s'=v\#rr@r; \ ((u\#s,c),ee,(v\#rr,c')) \in ntrp \ fg \ \rrbracket \Longrightarrow P
  shows P
  \langle proof \rangle
lemma ntrp-stack-decomp: !!u s r c P.
    ((u\#s@r,c),ww,(s',c'))\in trcl\ (ntrp\ fg);
    !!v \ rr. \ [s'=v\#rr@r; ((u\#s,c),ww,(v\#rr,c')) \in trcl \ (ntrp \ fg)]] \Longrightarrow P
  \rrbracket \Longrightarrow P
\langle proof \rangle
```

8.5 Relation to monitor consistent interleaving

In this section, we describe the relation of the consistent interleaving operator (cf. Section 2) and the macrostep-semantics.

8.5.1 Abstraction function for normalized paths

definition

We first need to define an abstraction function that maps a macrostep on a pair of entered and passed monitors, as required by the \otimes_{α} -operator:

A step on a normalized paths enters the monitors of the first called procedure and passes the monitors that occur in the following same-level path.

```
\alpha n fg e == if e = [] then (\{\},\{\}) else (mon-e fg (hd e), mon-w fg (tl e))
lemma \alpha n-simps[simp]:
  \alpha n fg | = (\{\}, \{\})
  \alpha n fg (e \# w) = (mon - e fg e, mon - w fg w)
  \langle proof \rangle
definition
  \alpha nl fg e == \alpha n fg (le\text{-}rem\text{-}s e)
lemma \alpha nl\text{-}def': \alpha nl\ fg == \alpha n\ fg \circ le\text{-}rem\text{-}s
  \langle proof \rangle
lemma \alpha nl-simps[simp]:
  \alpha nl fg (ENV x) = \alpha n fg x
  \alpha nl fg (LOC x) = \alpha n fg x
lemma \alpha nl-simps1[simp]:
  (\alpha nl fg) \circ ENV = \alpha n fg
  (\alpha nl fg) \circ LOC = \alpha n fg
lemma \alpha n-\alpha nl: (\alpha n fg) \circ le-rem-s = \alpha nl fg
lemma \alpha n-fst-snd[simp]: fst (\alpha n fg w) \cup snd (\alpha n fg w) = mon-w fg w
  \langle proof \rangle
lemma mon-pl-of-\alphanl: mon-pl (map (\alphanl fg) w) = mon-loc fg w \cup mon-env fg w
We now derive specialized introduction lemmas for \otimes_{\alpha n} f_q
lemma cil-\alpha n-cons-helper: mon-pl (map (\alpha n fg) wb) = mon-ww fg wb
  \langle proof \rangle
lemma cil-\alpha nl-cons-helper:
  mon-pl \ (map \ (\alpha nl \ fg) \ wb) = mon-ww \ fg \ (map \ le-rem-s \ wb)
  \langle proof \rangle
lemma cil-\alpha n-cons1: \llbracket w \in wa \otimes_{\alpha n} f_g wb; fst (<math>\alpha n fg e) \cap mon-ww fg wb = \{\} \rrbracket
  \implies e \# w \in e \# wa \otimes_{\alpha n \ fq} wb
  \langle proof \rangle
\textbf{lemma} \ \textit{cil-}\alpha\textit{n-cons2} \colon \llbracket \textit{w} \in \textit{wa} \otimes_{\alpha\textit{n}} \textit{fg} \textit{wb}; \textit{fst} \ (\alpha\textit{n} \textit{fg} \ e) \ \cap \ \textit{mon-ww} \textit{fg} \ \textit{wa} = \{\} \rrbracket
  \implies e \# w \in wa \otimes_{\alpha n \ fq} e \# wb
```

 $\langle proof \rangle$

8.5.2 Monitors

```
lemma (in flowgraph) ntrs-mon-s:

assumes A: ((s,c),e,(s',c')) \in ntrs fg

shows mon-s fg s' = mon-s fg s \cup fst (\alpha n fg e)

\langle proof \rangle

corollary (in flowgraph) ntrs-called-mon:

assumes A: ((s,c),e,(s',c')) \in ntrs fg

shows fst (\alpha n fg e) \subseteq mon-s fg s'

\langle proof \rangle

lemma (in flowgraph) ntr-mon-s:

(c,e,c') \in ntr fg \Longrightarrow mon-c fg c' = mon-c fg c \cup fst (\alpha n fg e)

\langle proof \rangle

lemma (in flowgraph) ntrp-mon-s:

assumes A: ((s,c),e,(s',c')) \in ntrp fg

shows mon-c fg (add-mset s' c') = mon-c fg (add-mset s c) \cup fst (\alpha nl fg e)

\langle proof \rangle
```

8.5.3 Interleaving theorem

In this section, we show that the consistent interleaving operator describes the intuition behind interleavability of normalized paths. We show: Two paths are simultaneously executable if and only if they are consistently interleavable and the monitors of the initial configurations are compatible

The split lemma splits an execution from a context of the form ca + cb into two interleavable executions from ca and cb respectively. While further down we prove this lemma for loc/env-path, which is more general but also more complicated, we start with the proof for paths of the multiset-semantics for illustrating the idea.

```
lemma (in flowgraph) ntr-split:

!!ca cb. [(ca+cb,w,c')\in trcl\ (ntr\ fg);\ valid\ fg\ (ca+cb)]]\Longrightarrow \exists\ ca'\ cb'\ wa\ wb.

c'=ca'+cb'\ \land \\
w\in (wa\otimes_{\alpha n}fgwb)\ \land \\
mon-c\ fg\ ca\cap (mon-c\ fg\ cb\cup mon-ww\ fg\ wb)=\{\}\ \land \\
mon-c\ fg\ cb\cap (mon-c\ fg\ ca\cup mon-ww\ fg\ wa)=\{\}\ \land \\
(ca,wa,ca')\in trcl\ (ntr\ fg)\ \land (cb,wb,cb')\in trcl\ (ntr\ fg)\ \land \\
proof\ \rangle
```

The next lemma is a more general version of *flowgraph.ntr-split* for the semantics with a distinguished local thread. The proof follows exactly the same ideas, but is more complex.

```
lemma (in flowgraph) ntrp-split:
  !!s c1 c2 s' c'.
  [(s,c1+c2),w,(s',c')) \in trcl \ (ntrp \ fg); \ valid \ fg \ (\{\#s\#\}+c1+c2)]
  \implies \exists w1 \ w2 \ c1' \ c2'.
         w \in w1 \otimes_{\alpha nl \ fq} (map \ ENV \ w2) \land
         c' = c1' + c2' \land
         ((s,c1),w1,(s',c1')) \in trcl (ntrp fg) \land
         (c2,w2,c2') \in trcl (ntr fg) \land
         mon\text{-}ww fg (map le\text{-}rem\text{-}s w1) \cap mon\text{-}c fg c2 = \{\} \land
         mon\text{-}ww \ fg \ w2 \cap mon\text{-}c \ fg \ (\{\#s\#\}+c1) = \{\}
\langle proof \rangle
lemma (in flowgraph) ntr-split':
  assumes A: (ca+cb,w,c') \in trcl (ntr fg)
  and VALID: valid fg (ca+cb)
  shows \exists ca' cb' wa wb.
    c'=ca'+cb' \wedge
    w \in (wa \otimes_{\alpha n} f_q wb) \land
    mon-c \ fg \ ca \cap (mon-c \ fg \ cb \cup mon-ww \ fg \ wb) = \{\} \land 
    mon-c \ fg \ cb \cap (mon-c \ fg \ ca \cup mon-ww \ fg \ wa) = \{\} \land 
    (ca,wa,ca') \in trcl (ntr fg) \land
    (cb, wb, cb') \in trcl \ (ntr \ fg)
\langle proof \rangle
```

The unsplit lemma combines two interleavable executions. For illustration purposes, we first prove the less general version for multiset-configurations. The general version for loc/env-configurations is shown later.

```
 \begin{array}{l} \textbf{lemma (in } flow graph) \ ntr\text{-}unsplit\text{:} \\ \textbf{assumes } A \colon w \in wa \otimes_{\alpha n} fgwb \ \textbf{and} \\ B \colon (ca,wa,ca') \in trcl \ (ntr \ fg) \\ (cb,wb,cb') \in trcl \ (ntr \ fg) \\ mon\text{-}c \ fg \ ca \cap (mon\text{-}c \ fg \ cb \cup mon\text{-}ww \ fg \ wb) = \{\} \\ mon\text{-}c \ fg \ cb \cap (mon\text{-}c \ fg \ ca \cup mon\text{-}ww \ fg \ wa) = \{\} \\ \textbf{shows} \ (ca+cb,w,ca'+cb') \in trcl \ (ntr \ fg) \\ \langle proof \rangle \\ \end{array}
```

```
lemma (in flowgraph) ntrp-unsplit:

assumes A: w \in wa \otimes_{\alpha nl} fg \pmod{ENV wb} and

B: ((s,ca),wa,(s',ca')) \in trcl \pmod{fg}

(cb,wb,cb') \in trcl \pmod{fg}

mon\text{-}c fg (\{\#s\#\}+ca) \cap (mon\text{-}c fg cb \cup mon\text{-}ww fg wb)=\{\}

mon\text{-}c fg cb \cap (mon\text{-}c fg (\{\#s\#\}+ca) \cup mon\text{-}ww fg \pmod{fg map le-rem-s wa})=\{\}

shows ((s,ca+cb),w,(s',ca'+cb')) \in trcl \pmod{fg}
```

And finally we get the desired theorem: Two paths are simultaneously executable if and only if they are consistently interleavable and the monitors of the initial configurations are compatible. Note that we have to assume a valid starting configuration.

```
theorem (in flowgraph) ntr-interleave: valid fg (ca+cb) \Longrightarrow
  (ca+cb,w,c') \in trcl \ (ntr \ fg) \longleftrightarrow
  (\exists ca' cb' wa wb.
    c'=ca'+cb' \land
    w \in (wa \otimes_{\alpha n} f_q wb) \land
    mon-c \ fg \ ca \cap (mon-c \ fg \ cb \cup mon-ww \ fg \ wb) = \{\} \land 
    mon-c \ fg \ cb \cap (mon-c \ fg \ ca \cup mon-ww \ fg \ wa) = \{\} \land \}
    (ca,wa,ca') \in trcl\ (ntr\ fg) \land (cb,wb,cb') \in trcl\ (ntr\ fg))
\langle proof \rangle
theorem (in flowgraph) ntrp-interleave:
  valid\ fg\ (\{\#s\#\}+c1+c2) \Longrightarrow
  ((s,c1+c2),w,(s',c')) \in trcl \ (ntrp \ fg) \longleftrightarrow
  (\exists w1 \ w2 \ c1' \ c2'.
    w \in w1 \otimes_{\alpha nl fg} (map ENV w2) \wedge
    c' = c1' + c2' \land
    ((s,c1),w1,(s',c1')) \in trcl (ntrp fg) \land
    (c2,w2,c2') \in trcl (ntr fg) \land
    mon\text{-}ww fg (map le\text{-}rem\text{-}s w1) \cap
    mon-c fg c2 = \{\} \land
    mon\text{-}ww \ fg \ w2 \cap mon\text{-}c \ fg \ (\{\#s\#\}+c1) = \{\})
  \langle proof \rangle
```

The next is a corollary of *flowgraph.ntrp-unsplit*, allowing us to convert a path to loc/env semantics by adding a local stack that does not make any steps.

```
corollary (in flowgraph) ntr2ntrp: [
(c,w,c') \in trcl \ (ntr \ fg);
mon-c \ fg \ (add-mset \ s \ cl) \cap (mon-c \ fg \ c \cup mon-ww \ fg \ w)=\{\}
[] \Longrightarrow ((s,cl+c),map \ ENV \ w,(s,cl+c')) \in trcl \ (ntrp \ fg)
\langle proof \rangle
```

8.5.4 Reverse splitting

This section establishes a theorem that allows us to find the thread in the original configuration that created some distinguished thread in the final configuration.

```
lemma (in flowgraph) ntr-reverse-split: !!w s' ce'. [  (c,w,\{\#s'\#\}+ce')\in trcl\ (ntr\ fg);  valid fg c ] \Longrightarrow \exists s\ ce\ w1\ w2\ ce1'\ ce2'.   c=\{\#s\#\}+ce\ \land   ce'=ce1'+ce2'\ \land   w\in w1\otimes_{\alpha n}fg\ w2\ \land   mon-s\ fg\ s\cap (mon-c\ fg\ ce\cup mon-ww\ fg\ w2)=\{\}\ \land   mon-c\ fg\ ce\cap (mon-s\ fg\ s\cup mon-ww\ fg\ w1)=\{\}\ \land   (\{\#s\#\},w1,\{\#s'\#\}+ce1')\in trcl\ (ntr\ fg)\ \land   (ce,w2,ce2')\in trcl\ (ntr\ fg)
```

- The proof works by induction on the initial configuration. Note that configurations consist of finitely many threads only
- FIXME: An induction over the size (rather then over the adding of some fixed element) may lead to a smoother proof here $\langle proof \rangle$

end

9 Constraint Systems

theory ConstraintSystems imports Main AcquisitionHistory Normalization begin

In this section we develop a constraint-system-based characterization of our analysis.

Constraint systems are widely used in static program analysis. There least solution describes the desired analysis information. In its generic form, a constraint system R is a set of inequations over a complete lattice (L, \sqsubseteq) and a set of variables V. An inequation has the form $R[v] \supseteq \mathsf{rhs}$, where $R[v] \in V$ and rhs is a monotonic function over the variables. Note that for program analysis, there is usually one variable per control point. The variables are then named R[v], where v is a control point. By standard fixed-point theory, those constraint systems have a least solution. Outside the constraint system definition R[v] usually refers to a component of that least solution.

Usually a constraint system is generated from the program. For example, a constraint generation pattern could be the following:

$$\begin{array}{ll} \text{for } (u, \mathsf{Call} \ q, v) \in E \colon \\ S^k[v] & \supseteq & \{ (\mathsf{mon}(q) \cup M \cup M', \tilde{P}) \mid (M, P) \in S^k[u] \land (M', P') \in S^k[\mathsf{r}_q] \\ & \land \ \tilde{P} \leq P \uplus P' \land |\tilde{P}| \leq 2 \} \end{array}$$

For some parameter k and a flowgraph with nodes N and edges E, this generates a constraint system over the variables $\{S^k[v] \mid v \in N\}$. One constraint is generated for each call edge. While we use a powerset lattice here, we can in general use any complete lattice. However, all the constraint systems needed for our conflict analysis are defined over powerset lattices $(\mathcal{P}('a), \subseteq)$ for some type 'a. This admits a convenient formalization in Isabelle/HOL using inductively defined sets. We inductively define a relation between variables³ and the elements of their values in the least solution, i.e. the set $\{(v,x) \mid x \in R[v]\}$. For example, the constraint generator pattern from above would become the following introduction rule in the inductive definition of the set S-cs fg k:

³Variables are identified by control nodes here

The main advantage of this approach is that one gets a concise formalization by using Isabelle's standard machinery, the main disadvantage is that this approach only works for powerset lattices ordered by \subseteq .

9.1 Same-level paths

9.1.1 Definition

We define a constraint system that collects abstract information about samelevel paths. In particular, we collect the set of used monitors and all multisubsets of spawned threads that are not bigger than k elements, where k is a parameter that can be freely chosen.

An element $(u,M,P) \in S$ -cs fg k means that there is a same-level path from the entry node of the procedure of u to u, that uses the monitors M and spawns at least the threads in P.

inductive-set

```
S\text{-}cs :: ('n,'p,'ba,'m,'more) \ flowgraph\text{-}rec\text{-}scheme} \Rightarrow nat \Rightarrow \\ ('n \times 'm \ set \times 'p \ multiset) \ set for fg \ k where S\text{-}init: \ (entry \ fg \ p,\{\},\{\#\}) \in S\text{-}cs \ fg \ k} \mid S\text{-}base: \ \llbracket (u,Base \ a,v) \in edges \ fg; \ (u,M,P) \in S\text{-}cs \ fg \ k \rrbracket \implies (v,M,P) \in S\text{-}cs \ fg \ k \mid S\text{-}call: \ \llbracket (u,Call \ q,v) \in edges \ fg; \ (u,M,P) \in S\text{-}cs \ fg \ k;  (return \ fg \ q,Ms,Ps) \in S\text{-}cs \ fg \ k;  P' \subseteq \#P + Ps; \ size \ P' \le k \ \rrbracket \implies (v,mon \ fg \ q \cup M \cup Ms,P') \in S\text{-}cs \ fg \ k;  P' \subseteq \#\{\#q\#\} + P; \ size \ P' \le k \ \rrbracket \implies (v,M,P') \in S\text{-}cs \ fg \ k \implies (v,M,P') \in S\text{-}cs \ fg \ k
```

The intuition underlying this constraint system is the following: The S-init-constraint describes that the procedures entry node can be reached with the empty path, that has no monitors and spawns no procedures. The S-base-constraint describes that executing a base edge does not use monitors or spawn threads, so each path reaching the start node of the base edge also induces a path reaching the end node of the base edge with the same set of monitors and the same set of spawned threads. The S-call-constraint models the effect of a procedure call. If there is a path to the start node of a call edge and a same-level path through the procedure, this also induces a path to the end node of the call edge. This path uses the monitors of both path and spawns the threads that are spawned on both paths. Since we only record a limited subset of the spawned threads, we have to choose which of the threads are recorded. The S-spawn-constraint models the effect

of a spawn edge. A path to the start node of the spawn edge induces a path to the end node that uses the same set of monitors and spawns the threads of the initial path plus the one spawned by the spawn edge. We again have to choose which of these threads are recorded.

9.1.2 Soundness and Precision

Soundness of the constraint system S-cs means, that every same-level path has a corresponding entry in the constraint system.

As usual the soundness proof works by induction over the length of execution paths. The base case (empty path) trivially follows from the *S-init* constraint. In the inductive case, we consider the edge that induces the last step of the path; for a return step, this is the corresponding call edge (cf. Lemma *flowgraph.trss-find-call'*). With the induction hypothesis, we get the soundness for the (shorter) prefix of the path, and depending on the last step we can choose a constraint that implies soundness for the whole path.

```
lemma (in flowgraph) S-sound: !!p v c' P.  [(([entry\ fg\ p], \{\#\}), w, ([v], c')) \in trcl\ (trss\ fg);  size P \leq k; (\lambda p. [entry\ fg\ p]) '# P <math>\subseteq \# c' ] \Longrightarrow (v, mon-w\ fg\ w, P) \in S-cs fg\ k \langle proof \rangle
```

Precision means that all entries appearing in the smallest solution of the constraint system are justified by some path in the operational characterization. For proving precision, one usually shows that a family of sets derived as an abstraction from the operational characterization solves all constraints.

In our formalization of constraint systems as inductive sets this amounts to constructing for each constraint a justifying path for the entries described on the conclusion side of the implication – under the assumption that corresponding paths exists for the entries mentioned in the antecedent.

```
lemma (in flowgraph) S-precise: (v,M,P) \in S-cs fg \ k
\implies \exists \ p \ c' \ w.
(([entry \ fg \ p], \{\#\}), w, ([v], c')) \in trcl \ (trss \ fg) \ \land
size \ P \subseteq k \ \land
(\lambda p. \ [entry \ fg \ p]) \ '\# \ P \subseteq \# \ c' \ \land
M = mon\text{-}w \ fg \ w
\langle proof \rangle
theorem (in flowgraph) S-sound-precise:
(v,M,P) \in S-cs fg \ k \longleftrightarrow
(\exists \ p \ c' \ w. \ (([entry \ fg \ p], \{\#\}), w, ([v], c')) \in trcl \ (trss \ fg) \ \land
size \ P \subseteq k \ \land \ (\lambda p. \ [entry \ fg \ p]) \ '\# \ P \subseteq \# \ c' \ \land \ M = mon\text{-}w \ fg \ w)
\langle proof \rangle
```

Next, we present specialized soundness and precision lemmas, that reason over a macrostep $(ntrp\ fg)$ rather than a same-level path $(trcl\ (trss\ fg))$.

They are tailored for the use in the soundness and precision proofs of the other constraint systems.

```
lemma (in flowgraph) S-sound-ntrp:
  assumes A: (([u],\{\#\}),eel,(sh,ch)) \in ntrp\ fg and
  CASE: !!p \ u' \ v \ w.
    eel=LOC (LCall p\#w);
    (u, Call \ p, u') \in edges \ fg;
    sh=[v,u'];
    proc\text{-}of fg \ v = p;
    mon-c fg ch = \{\};
    !!s. \ s \in \# \ ch \Longrightarrow \exists \ p \ u \ v. \ s = [entry \ fg \ p] \ \land
                       (u,Spawn\ p,v) \in edges\ fg\ \land
                       initial proc fg p;
    !!P. (\lambda p. [entry fg p]) '\# P \subseteq \# ch \Longrightarrow
                     (v, mon\text{-}w fg w, P) \in S\text{-}cs fg (size P)
  ]\!] \Longrightarrow Q
  shows Q
\langle proof \rangle
lemma (in flowgraph) S-precise-ntrp:
  assumes ENTRY: (v,M,P) \in S-cs fg k and
               P: proc-of fg \ v = p and
            EDGE: (u,Call\ p,u') \in edges\ fg
  shows \exists w \ ch.
    (([u],\{\#\}),LOC\ (LCall\ p\#w),([v,u'],ch))\in ntrp\ fg\ \land
    size P \leq k \wedge
    M = mon - w fg w \land
    mon-n fg v = mon fg p \land
    (\lambda p. [entry fg p]) '\# P \subseteq \# ch \wedge
    mon-c fg ch=\{\}
\langle proof \rangle
```

9.2 Single reaching path

In this section we define a constraint system that collects abstract information of paths reaching a control node at U. The path starts with a single initial thread. The collected information are the monitors used by the steps of the initial thread, the monitors used by steps of other threads and the acquisition history of the path. To distinguish the steps of the initial thread from steps of other threads, we use the loc/env-semantics (cf. Section 5.4).

9.2.1 Constraint system

An element $(u, Ml, Me, h) \in RU$ -cs $fg\ U$ corresponds to a path from $\{\#[u]\#\}$ to some configuration at U, that uses monitors from Ml in the steps of the initial thread, monitors from Me in the steps of other threads and has acquisition history h.

Here, the correspondence between paths and entries included into the inductively defined set is not perfect but strong enough for our purposes: While each constraint system entry corresponds to a path, not each path corresponds to a constraint system entry. But for each path reaching a configuration at U, we find an entry with less or equal monitors and an acquisition history less or equal to the acquisition history of the path.

inductive-set

```
RU\text{-}cs :: ('n,'p,'ba,'m,'more) \ flowgraph\text{-}rec\text{-}scheme} \Rightarrow 'n \ set \Rightarrow \\ ('n \times 'm \ set \times 'm \ set \times ('m \Rightarrow 'm \ set)) \ set for fg \ U where RU\text{-}init: \ u \in U \Longrightarrow (u,\{\},\{\},\lambda x.\{\}) \in RU\text{-}cs \ fg \ U \\ | \ RU\text{-}call: \ [ \ (u,Call \ p,u') \in edges \ fg; \ proc\text{-}of \ fg \ v = \ p; \ (v,M,P) \in S\text{-}cs \ fg \ 0; \\ (v,Ml,Me,h) \in RU\text{-}cs \ fg \ U; \ mon\text{-}n \ fg \ u \cap Me = \{\} \ ]] \Longrightarrow (\ u, \ mon \ fg \ p \cup M \cup Ml, \ Me, \ ah\text{-}update \ h \ (mon \ fg \ p,M) \ (Ml \cup Me)) \in RU\text{-}cs \ fg \ U | \ RU\text{-}spawn: \ [ \ (u,Call \ p,u') \in edges \ fg; \ proc\text{-}of \ fg \ v = \ p; \ (v,M,P) \in S\text{-}cs \ fg \ 1; \\ q \in \# \ P; \ (entry \ fg \ q,Ml,Me,h) \in RU\text{-}cs \ fg \ U; \\ (mon\text{-}n \ fg \ u \cup mon \ fg \ p) \cap (Ml \cup Me) = \{\} \ ]] \Longrightarrow (u,mon \ fg \ p \cup M, \ Ml \cup Me, \ ah\text{-}update \ h \ (mon \ fg \ p,M) \ (Ml \cup Me)) \in RU\text{-}cs \ fg \ U
```

The constraint system works by tracking only a single thread. Initially, there is just one thread, and from this thread we reach a configuration at U. After a macrostep, we have the transformed initial thread and some spawned threads. The key idea is, that the actual node U is reached by just one of these threads. The steps of the other threads are useless for reaching U. Because of the nice properties of normalized paths, we can simply prune those steps from the path.

The RU-init-constraint reflects that we can reach a control node from itself with the empty path. The RU-call-constraint describes the case that Uis reached from the initial thread, and the RU-spawn-constraint describes the case that U is reached from one of the spawned threads. In the two latter cases, we have to check whether prepending the macrostep to the reaching path is allowed or not due to monitor restrictions. In the call case, the procedure of the initial node must not own monitors that are used in the environment steps of the appended reaching path (mon-n fg $u \cap Me$ = {}). As we only test disjointness with the set of monitors used by the environment, reentrant monitors can be handled. In the spawn case, we have to check disjointness with both, the monitors of local and environment steps of the reaching path from the spawned thread, because from the perspective of the initial thread, all these steps are environment steps ((mon-n fg $u \cup$ $mon\ fg\ p)\cap (Ml\cup Me)=\{\}$). Note that in the call case, we do not need to explicitly check that the monitors used by the environment are disjoint from the monitors acquired by the called procedure because this already

follows from the existence of a reaching path, as the starting point of this path already holds all these monitors.

However, in the spawn case, we have to check for both the monitors of the start node and of the called procedure to be compatible with the already known reaching path from the entry node of the spawned thread.

9.2.2 Soundness and precision

The following lemma intuitively states: If we can reach a configuration that is at U from some start configuration, then there is a single thread in the start configuration that can reach a configuration at U with a subword of the original path.

The proof follows from Lemma flowgraph.ntr-reverse-split rather directly.

```
lemma (in flowgraph) ntr-reverse-split-atU:
assumes V: valid fg c and
A: atU \ U \ c' and
B: (c,w,c') \in trcl \ (ntr \ fg)
shows \exists s \ w' \ c1'.
s \in \# \ c \land w' \preceq w \land c1' \subseteq \# \ c' \land atU \ U \ c1' \land (\{\#s\#\},w',c1') \in trcl \ (ntr \ fg)
\langle proof \rangle
```

The next lemma shows the soundness of the RU constraint system.

The proof works by induction over the length of the reaching path. For the empty path, the proposition follows by the RU-init-constraint. For a non-empty path, we consider the first step. It has transformed the initial thread and may have spawned some other threads. From the resulting configuration, U is reached. Due to flowgraph.ntr-split we get two interleavable paths from the rest of the original path, one from the transformed initial thread and one from the spawned threads. We then distinguish two cases: if the first path reaches U, the proposition follows by the induction hypothesis and the RU-call constraint.

Otherwise, we use flow graph.ntr-reverse-split-at U to identify the thread that actually reaches U among all the spawned threads. Then we apply the induction hypothesis to the path of that thread and prepend the first step using the RU-spawn-constraint.

The main complexity of the proof script below results from fiddling with the monitors and converting between the multiset-and loc/env-semantics. Also the arguments to show that the acquisition histories are sound approximations require some space.

```
lemma (in flowgraph) RU-sound:

!!u s' c'. [(([u],{\#}),w,(s',c'))\in trcl\ (ntrp\ fg);\ atU\ U\ (add-mset\ s'\ c')]

\Longrightarrow \exists\ Ml\ Me\ h.

(u,Ml,Me,h)\in RU-cs\ fg\ U\ \land
```

```
\begin{array}{l} \mathit{Ml} \subseteq \mathit{mon\text{-}loc} \mathit{fg} \ \mathit{w} \land \\ \mathit{Me} \subseteq \mathit{mon\text{-}env} \mathit{fg} \ \mathit{w} \land \\ \mathit{h} \le \alpha \mathit{ah} \ (\mathit{map} \ (\alpha \mathit{nl} \ \mathit{fg}) \ \mathit{w}) \\ \text{--- The proof works by induction over the length of the reaching path} \\ \langle \mathit{proof} \rangle \end{array}
```

Now we prove a statement about the precision of the least solution. As in the precision proof of the *S-cs* constraint system, we construct a path for the entry on the conclusion side of each constraint, assuming that there already exists paths for the entries mentioned in the antecedent.

We show that each entry in the least solution corresponds exactly to some executable path, and is not just an under-approximation of a path; while for the soundness direction, we could only show that every executable path is under-approximated. The reason for this is that in effect, the constraint system prunes the steps of threads that are not needed to reach the control point. However, each pruned path is executable.

```
lemma (in flowgraph) RU-precise: (u,Ml,Me,h) \in RU-cs fg U \implies \exists w \ s' \ c'.
(([u],\{\#\}),w,(s',c')) \in trcl \ (ntrp \ fg) \land 
atU \ U \ (\{\#s'\#\}+c') \land 
mon\text{-loc } fg \ w = Ml \land 
mon\text{-env } fg \ w = Me \land 
\alpha ah \ (map \ (\alpha nl \ fg) \ w) = h 
\langle proof \rangle
```

9.3 Simultaneously reaching path

In this section, we define a constraint system that collects abstract information for paths starting at a single control node and reaching two program points simultaneously, one from a set U and one from a set V.

9.3.1 Constraint system

An element $(u, Ml, Me) \in RUV$ -cs $fg \ U \ V$ means, that there is a path from $\{\#[u]\#\}$ to some configuration that is simultaneously at U and at V. That path uses monitors from Ml in the first thread and monitors from Me in the other threads.

inductive-set

```
RUV\text{-}cs :: ('n,'p,'ba,'m,'more) \ flowgraph\text{-}rec\text{-}scheme \Rightarrow \\ \quad 'n \ set \Rightarrow 'n \ set \Rightarrow ('n \times 'm \ set \times 'm \ set) \ set for fg \ U \ V where RUV\text{-}call: \\ [ (u,Call \ p,u')\in edges \ fg; \ proc\text{-}of \ fg \ v = p; \ (v,M,P)\in S\text{-}cs \ fg \ 0; \\ (v,Ml,Me)\in RUV\text{-}cs \ fg \ U \ V; \ mon\text{-}n \ fg \ u \ \cap Me = \{\} \ ]] \\ \Longrightarrow (u,mon \ fg \ p \cup M \cup Ml,Me)\in RUV\text{-}cs \ fg \ U \ V
```

```
\mid RUV-spawn:
     \llbracket (u, Call \ p, u') \in edges \ fg; \ proc\text{-}of \ fg \ v = p; \ (v, M, P) \in S\text{-}cs \ fg \ 1; \ q \in \# \ P; 
       (entry fg q, Ml, Me) \in RUV - cs fg U V;
       (mon-n fg \ u \cup mon fg \ p) \cap (Ml \cup Me) = \{\}\ 
    \implies (u, mon fg p \cup M, Ml \cup Me) \in RUV - cs fg U V
\mid RUV-split-le:
    [(u,Call\ p,u')\in edges\ fg;\ proc\text{-}of\ fg\ v=p;\ (v,M,P)\in S\text{-}cs\ fg\ 1;\ q\in\#P;
        (v,Ml,Me,h) \in RU-cs fg U; (entry fg q,Ml',Me',h') \in RU-cs fg V;
       (mon-n fg \ u \cup mon fg \ p) \cap (Me \cup Ml' \cup Me') = \{\}; \ h \ [*] \ h' \ [
    \implies (u, mon fg p \cup M \cup Ml, Me \cup Ml' \cup Me') \in RUV - cs fg U V
\mid RUV-split-el:
    [(u,Call\ p,u')\in edges\ fg;\ proc\text{-}of\ fg\ v=p;\ (v,M,P)\in S\text{-}cs\ fg\ 1;\ q\in\#P;
       (v,Ml,Me,h) \in RU-cs fg V; (entry\ fg\ q,Ml',Me',h') \in RU-cs fg U;
       (mon-n fg u \cup mon fg p) \cap (Me \cup Ml' \cup Me') = \{\}; h [*] h' \|
    \implies (u, mon fg p \cup M \cup Ml, Me \cup Ml' \cup Me') \in RUV-cs fg U V
\mid RUV-split-ee:
    [(u,Call\ p,u')\in edges\ fg;\ proc\text{-}of\ fg\ v=p;\ (v,M,P)\in S\text{-}cs\ fg\ 2;
       \{\#q\#\}+\{\#q'\#\}\subseteq \#P;
       (entry\ fg\ q,Ml,Me,h)\in RU\text{-}cs\ fg\ U;\ (entry\ fg\ q',Ml',Me',h')\in RU\text{-}cs\ fg\ V;
       (mon-n fg u \cup mon fg p) \cap (Ml \cup Me \cup Ml' \cup Me') = \{\}; h [*] h' ]
    \implies (u, mon fg p \cup M, Ml \cup Me \cup Ml' \cup Me') \in RUV - cs fg U V
```

The idea underlying this constraint system is similar to the RU-cs-constraint system for reaching a single node set. Initially, we just track one thread. After a macrostep, we have a configuration consisting of the transformed initial thread and the spawned threads. From this configuration, we reach two nodes simultaneously, one in U and one in V. Each of these nodes is reached by just a single thread. The constraint system contains one constraint for each case how these threads are related to the initial and the spawned threads:

RUV call Both, U and V are reached from the initial thread.

RUV spawn Both, U and V are reached from a single spawned thread.

 ${f RUV_split_le}\ U$ is reached from the initial thread, V is reached from a spawned thread.

 $\mathbf{RUV_split_el}\ V$ is reached from the initial thread, U is reached from a spawned thread.

RUV split ee Both, U and V are reached from different spawned threads.

In the latter three cases, we have to analyze the interleaving of two paths each reaching a single control node. This is done via the acquisition history information that we collected in the RU-cs-constraint system.

Note that we do not need an initializing constraint for the empty path, as a single configuration cannot simultaneously be at two control nodes.

9.3.2 Soundness and precision

10 Main Result

end

theory MainResult imports ConstraintSystems begin

At this point everything is available to prove the main result of this project: The constraint system RUV-cs precisely characterizes simultaneously reachable control nodes w.r.t. to our semantic reference point.

The "trusted base" of this proof, that are all definitions a reader that trusts the Isabelle prover must additionally trust, is the following:

- The flowgraph and the assumptions made on it in the *flowgraph* and *eflowgraph*-locales. Note that we show in Section 6.4 that there is at least one non-trivial model of *eflowgraph*.
- The reference point semantics (refpoint) and the transitive closure operator (trcl).
- The definition of at UV.
- All dependencies of the above definitions in the Isabelle standard libraries.

```
theorem (in eflowgraph) RUV-is-sim-reach:

(\exists w \ c'. (\{\#[entry \ fg \ (main \ fg)]\#\}, w, c') \in trcl \ (refpoint \ fg) \land atUV \ U \ V \ c')
\longleftrightarrow (\exists Ml \ Me. \ (entry \ fg \ (main \ fg), Ml, Me) \in RUV-cs \ fg \ U \ V)
```

— The proof uses the soundness and precision theorems wrt. to normalized paths (flowgraph.RUV-sound, flowgraph.RUV-precise) as well as the normalization result, i.e. that every reachable configuration is also reachable using a normalized path (eflowgraph.normalize) and, vice versa, that every normalized path is also a usual path (ntr-is-tr). Finally the conversion between our working semantics and the semantic reference point is exploited (flowgraph.refpoint-eq).

```
\begin{array}{c} (\mathbf{is} ? lhs \longleftrightarrow ? rhs) \\ \langle proof \rangle \end{array}
```

end

11 Conclusion

We have formalized a flowgraph-based model for programs with recursive procedure calls, dynamic thread creation and reentrant monitors and its operational semantics. Based on the operational semantics, we defined a conflict as being able to simultaneously reach two control points from two given sets U and V when starting at the initial program configuration, just consisting of a single thread at the entry point of the main procedure. We then formalized a constraint-system-based analysis for conflicts and proved it sound and precise w.r.t. the operational definition of a conflict. The main idea of the analysis was to restrict the possible schedules of a program. On the one hand, this restriction enabled the constraint system based analysis, on the other hand it did not change the set of reachable configurations (and thus the set of conflicts).

We characterized the constraint systems as inductive sets. While we did not derive an executable algorithm explicitly, the steps from the inductive sets characterization to an algorithm follow the path common in program analysis and pose no particular difficulty. The algorithm would have to construct a constraint system (system of inequalities over a finite height lattice) from a given program corresponding to the inductively defined sets studied here and then determine its least solution, e.g. by a worklist algorithm. In order to make the algorithm executable, we would have to introduce finiteness assumptions for our programs. The derivation of executable algorithms is currently in preparation.

A formal analysis of the algorithmic complexity of the problem will be presented elsewhere. Here we only present some results: Already the problem of deciding the reachability of a single control node is NP-hard, as can be shown by a simple reduction from SAT. On the other hand, we can decide simultaneous reachability in nondeterministic polynomial time in the program size, where the number of random bits depends on the possible nesting depth of the monitors. This can be shown by analyzing the constraint systems.

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References

- [1] A. Bouajjani, M. Müller-Olm, and T. Touili. Regular symbolic analysis of dynamic networks of pushdown systems. In *Proc. of CONCUR'05*. Springer, 2005.
- [2] J. Esparza and J. Knoop. An automata-theoretic approach to interprocedural data-flow analysis. In *Proc. of FoSSaCS'99*, pages 14–30. Springer, 1999.
- [3] J. Esparza and A. Podelski. Efficient algorithms for pre* and post* on interprocedural parallel flow graphs. In *Proc. of POPL'00*, pages 1–11. Springer, 2000.
- [4] V. Kahlon and A. Gupta. An automata-theoretic approach for model checking threads for LTL properties. In *Proc. of LICS 2006*, pages 101–110. IEEE Computer Society, 2006.
- [5] V. Kahlon, F. Ivancic, and A. Gupta. Reasoning about threads communicating via locks. In *Proc. of CAV 2005*, pages 505–518. Springer, 2005.
- [6] P. Lammich and M. Müller-Olm. Precise fixpoint-based analysis of programs with thread-creation. In *Proc. of CONCUR 2007*, pages 287–302. Springer, 2007.
- [7] H. Seidl and B. Steffen. Constraint-based inter-procedural analysis of parallel programs. *Nordic Journal of Computing (NJC)*, 7(4):375–400, 2000.