

A bytecode logic for JML and types (Isabelle/HOL sources)

Lennart Beringer and Martin Hofmann

June 14, 2026

Abstract

This document contains the Isabelle/HOL sources underlying our paper *A bytecode logic for JML and types* [2], updated to Isabelle 2008. We present a program logic for a subset of sequential Java bytecode that is suitable for representing both, features found in high-level specification language JML as well as interpretations of high-level type systems. To this end, we introduce a fine-grained collection of assertions, including strong invariants, local annotations and VDM-reminiscent partial-correctness specifications. Thanks to a goal-oriented structure and interpretation of judgements, verification may proceed without recourse to an additional control flow analysis. The suitability for interpreting intensional type systems is illustrated by the proof-carrying-code style encoding of a type system for a first-order functional language which guarantees a constant upper bound on the number of objects allocated throughout an execution, be the execution terminating or non-terminating.

Like the published paper, the formal development is restricted to a comparatively small subset of the JVM, lacking (among other features) exceptions, arrays, virtual methods, and static fields. This shortcoming has been overcome meanwhile, as our paper has formed the basis of the MOBIUS base logic [9], a program logic for the full sequential fragment of the JVM. Indeed, the present formalisation formed the basis of a subsequent formalisation of the MOBIUS base logic in the proof assistant Coq, which includes a proof of soundness with respect to the Bicolano operational semantics [10].

Contents

1	Preliminaries: association lists	2
2	Language	3
2.1	Syntax	3
2.2	Dynamic semantics	5
2.2.1	Semantic components	5

2.2.2	Operational judgements	6
2.3	Basic properties	8
3	Axiomatic semantics	8
3.1	Assertion forms	8
3.2	Proof system	9
4	Auxiliary operational judgements	14
4.1	Multistep execution	14
4.2	Reachability relation	15
5	Soundness	16
5.1	Validity	16
5.2	Soundness under valid contexts	18
5.3	Soundness of verified programs	20
6	A derived logic for a strong type system	20
6.1	Syntax and semantics of judgements	21
6.2	Derived proof rules	22
6.3	Soundness of high-level type system	23

1 Preliminaries: association lists

Finite maps are used frequently, both in the representation of syntax and in the program logics. Instead of restricting Isabelle’s partial map type $\alpha \rightarrow \beta$ to finite domains, we found it easier for the present development to use the following adhoc data type of association lists.

type-synonym $(‘a, ‘b)$ *AssList* = $(‘a \times ‘b)$ *list*

primrec *lookup*:: $(‘a, ‘b)$ *AssList* \Rightarrow $‘a \Rightarrow (‘b \text{ option})$ ($\langle \cdot \downarrow \cdot \rangle$ [90,0] 90)
where
lookup [] $l = \text{None}$ |
lookup ($h \# t$) $l = (\text{if } \text{fst } h = l \text{ then } \text{Some}(\text{snd } h) \text{ else } \text{lookup } t \ l)$

The statement following the type declaration of *lookup* indicates that we may use the infix notation $L \downarrow a$ for the lookup operation, and asserts some precedence for bracketing. In a similar way, shorthands are introduced for various operations throughout this document.

primrec *delete*:: $(‘a, ‘b)$ *AssList* \Rightarrow $‘a \Rightarrow (‘a, ‘b)$ *AssList*
where
delete [] $a = []$ |
delete ($h \# t$) $a = (\text{if } (\text{fst } h) = a \text{ then } \text{delete } t \ a \text{ else } (h \# (\text{delete } t \ a)))$

definition *upd*:: $(‘a, ‘b)$ *AssList* \Rightarrow $‘a \Rightarrow ‘b \Rightarrow (‘a, ‘b)$ *AssList*

```

    (⟨-|+→-⟩ [1000,0,0] 1000)
where upd L a b = (a,b) # (delete L a)

```

The empty map is represented by the empty list.

```

definition emp::('a, 'b)AssList
where emp = []
definition contained::('a,'b) AssList ⇒ ('a,'b) AssList ⇒ bool
where contained L M = (∀ a b . L↓a = Some b → M↓a = Some b)

```

The following operation defined the cardinality of a map.

```

fun AL-Size :: ('a, 'b) AssList ⇒ nat (⟨-|⟩ [1000] 1000) where
  AL-Size [] = 0
| AL-Size (h # t) = Suc (AL-Size (delete t (fst h)))

```

Some obvious basic properties of association lists and their operations are easily proven, but have been suppressed during the document preparation.

2 Language

2.1 Syntax

We have syntactic classes of (local) variables, class names, field names, and method names. Naming restrictions, namespaces, long Java names etc. are not modelled.

```

typedecl Var
typedecl Class
typedecl Field
typedecl Method

```

Since arithmetic operations are modelled as unimplemented functions, we introduce the type of values in this section. The domain of heap locations is arbitrary.

```

typedecl Addr

```

A reference is either null or an address.

```

datatype Ref = Nullref | Loc Addr

```

Values are either integer numbers or references.

```

datatype Val = RVal Ref | IVal int

```

The type of (instruction) labels is fixed, since the operational semantics increments the program counter after each instruction.

```

type-synonym Label = int

```

Regarding the instructions, we support basic operand-stack manipulations, object creation, field modifications, casts, static method invocations, conditional and unconditional jumps, and a return instruction.

For every (Isabelle) function $f : Val \Rightarrow Val \Rightarrow Val$ we have an instruction *binop f* whose semantics is to invoke f on the two topmost values on the operand stack and replace them with the result. Similarly for *unop f*.

```
datatype Instr =
  const Val
| dup
| pop
| swap
| load Var
| store Var
| binop Val  $\Rightarrow$  Val  $\Rightarrow$  Val
| unop Val  $\Rightarrow$  Val
| new Class
| getfield Class Field
| putfield Class Field
| checkcast Class
| invokeS Class Method
| goto Label
| iftrue Label
| vreturn
```

Method body declarations contain a list of formal parameters, a mapping from instruction labels to instructions, and a start label. The operational semantics assumes that instructions are labelled consecutively¹.

```
type-synonym Mbody = Var list  $\times$  (Label, Instr) AssList  $\times$  Label
```

A class definition associates method bodies to method names.

```
type-synonym Classdef = (Method, Mbody) AssList
```

Finally, a program consists of classes.

```
type-synonym Prog = (Class, Classdef) AssList
```

Taken together, the three types *Prog*, *Classdef*, and *Mbody* represent an abstract model of the virtual machine environment. In our opinion, it would be desirable to avoid modelling this environment at a finer level, at least for the purpose of the program logic. For example, we prefer not to consider in detail the representation of the constant pool.

¹In the paper, we slightly abstract from this by including a successor functions on labels

2.2 Dynamic semantics

2.2.1 Semantic components

An object consists of the identifier of its dynamic class and a map from field names to values. Currently, we do not model type-correctness, nor do we require that all (or indeed any) of the fields stem from the static definition of the class, or a super-class. Note, however, that type correctness can be expressed in the logic.

type-synonym $Object = Class \times (Field, Val) AssList$

The heap is represented as a map from addresses to values. The JVM specification does not prescribe any particular object layout. The proposed type reflects this indeterminacy, but allows one to calculate the byte-correct size of a heap only after a layout scheme has been supplied. Alternative heap models would be the store-less semantics in the sense of Jonkers [8] and Deutsch [5], (where the heap is modelled as a partial equivalence relation on access paths), or object-based semantics in the sense of Reddy [11], where the heap is represented as a history of update operations. Hähnle et al. use a variant of the latter in their dynamic logic for a JAVACARD [6].

type-synonym $Heap = (Addr, Object) AssList$

Later, one might extend heaps by a component for static fields.

The types of the (register) store and the operand stack are as expected.

type-synonym $Store = (Var, Val) AssList$

type-synonym $OpStack = Val list$

States contain an operand stack, a store, and a heap.

type-synonym $State = OpStack \times Store \times Heap$

definition $heap::State \Rightarrow Heap$

where $heap\ s = snd(snd\ s)$

The operational semantics and the program logic are defined relative to a fixed program P . Alternatively, the type of the operational semantics (and proof judgements) could be extended by a program component. We also define the constant value $TRUE$, the representation of which does not matter for the current formalisation.

axiomatization $P::Prog$ and $TRUE::Val$

In order to obtain more readable rules, we define operations for extracting method bodies and instructions from the program.

definition $mbody-is::Class \Rightarrow Method \Rightarrow Mbody \Rightarrow bool$

where $mbody-is\ C\ m\ M = (\exists\ CD . P \downarrow C = Some\ CD \wedge CD \downarrow m = Some\ M)$

definition $get-ins::Mbody \Rightarrow Label \Rightarrow Instr \Rightarrow option$
where $get-ins\ M\ l = (fst(snd\ M))\downarrow l$

definition $ins-is::Class \Rightarrow Method \Rightarrow Label \Rightarrow Instr \Rightarrow bool$
where $ins-is\ C\ m\ l\ ins = (\exists\ M . mbody-is\ C\ m\ M \wedge get-ins\ M\ l = Some\ ins)$

The transfer of method arguments from the caller's operand stack to the formal parameters of an invoked method is modelled by the predicate

inductive-set $Frame::(OpStack \times (Var\ list) \times Store \times OpStack)\ set$
where
 $FrameNil: \llbracket oo=ops \rrbracket \Longrightarrow (ops, [], emp, oo) : Frame$
 $|$
 $Frame-cons: \llbracket (oo, par, S, ops) : Frame; R = S[x \mapsto v] \rrbracket$
 $\Longrightarrow (v \# oo, x \# par, R, ops) : Frame$

In order to obtain a deterministic semantics, we assume the existence of a function, with the obvious freshness axiom for this construction.

axiomatization $nextLoc::Heap \Rightarrow Addr$
where $nextLoc-fresh: h\downarrow(nextLoc\ h) = None$

2.2.2 Operational judgements

Similar to Bannwart-Müller [1], we define two operational judgements: a one-step relation and a relation that represents the transitive closure of the former until the end of the current method invocation. These relations are mutually recursive, since the method invocation rule contracts the execution of the invoked method to a single step. The one-step relation associates a state to its immediate successor state, where the program counter is interpreted with respect to the current method body. The transitive closure ignores the bottom part of the operand stack and the store of the final configuration. It simply returns the heap and the result of the method invocation, where the latter is given by the topmost value on the operand stack. In contrast to [1], we do not use an explicit *return* variable. Both relations take an additional index of type *nat* that monitors the derivation height. This is useful in the proof of soundness of the program logic.

Intuitively, $(M, l, s, n, l', s') : Step$ means that method (body) M evolves in one step from state s to state s' , while statement $(M, s, n, h, v) : Exec$ indicates that executing from s in method M leads eventually to a state whose final value is h , where precisely the last step in this sequence is a *vreturn* instruction and the return value is v .

Like Bannwart and Müller, we define a "frame-less" semantics. i.e. the execution of a method body is modelled by a transitive closure of the basic step-relation, which results in a one-step reduction at the invocation site.

Arguably, an operational semantics with an explicit frame stack is closer to the real JVM. It should not be difficult to verify the operational soundness of the present system w.r.t. such a finer model, or to modify the semantics.

inductive-set

Step::(*Mbody* × *Label* × *State* × *nat* × *Label* × *State*) *set*
and
Exec::(*Mbody* × *Label* × *State* × *nat* × *Heap* × *Val*) *set*
where
Const: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{const } v); \text{NEXT} = (v \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Dup: $\llbracket \text{get-ins } M \ l = \text{Some } \text{dup}; \text{NEXT} = (v \ \# \ v \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Pop: $\llbracket \text{get-ins } M \ l = \text{Some } \text{pop}; \text{NEXT} = (os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Swap: $\llbracket \text{get-ins } M \ l = \text{Some } \text{swap}; \text{NEXT} = (w \ \# \ (v \ \# \ os), s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ (w \ \# \ os), s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Load: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{load } x); s \downarrow x = \text{Some } v;$
 $\text{NEXT} = (v \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Store: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{store } x); \text{NEXT} = (os, s[x \mapsto v], h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Binop: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{binop } f); \text{NEXT} = ((f \ v \ w) \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ (w \ \# \ os), s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Unop: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{unop } f); \text{NEXT} = ((f \ v) \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
New: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{new } d); \text{newobj} = (d, \text{emp}); a = \text{nextLoc } h;$
 $\text{NEXT} = ((RVal \ (\text{Loc } a)) \ \# \ os, s, h[a \mapsto \text{newobj}]); ll=l+1 \rrbracket$
 $\implies (M, l, (os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Get: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{getfield } d \ F); h \downarrow a = \text{Some } (d, \text{Flds});$
 $\text{Flds} \downarrow F = \text{Some } v; \text{NEXT} = (v \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, ((RVal \ (\text{Loc } a)) \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Put: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{putfield } d \ F); h \downarrow a = \text{Some } (d, \text{Flds});$
 $\text{newobj} = (d, \text{Flds}[F \mapsto v]); \text{NEXT} = (os, s, h[a \mapsto \text{newobj}]); ll=l+1 \rrbracket$
 $\implies (M, l, (v \ \# \ ((RVal \ (\text{Loc } a)) \ \# \ os), s, h), 1, ll, \text{NEXT}) : \text{Step}$
|
Cast: $\llbracket \text{get-ins } M \ l = \text{Some } (\text{checkcast } d); h \downarrow a = \text{Some } (d, \text{Flds});$
 $\text{NEXT} = ((RVal \ (\text{Loc } a)) \ \# \ os, s, h); ll=l+1 \rrbracket$
 $\implies (M, l, ((RVal \ (\text{Loc } a)) \ \# \ os, s, h), 1, ll, \text{NEXT}) : \text{Step}$
|

$$\begin{array}{l}
\text{Goto: } \llbracket \text{get-ins } M \ l = \text{Some } (\text{goto } pc) \rrbracket \Longrightarrow (M, l, S, 1, pc, S) : \text{Step} \\
| \\
\text{IfT: } \llbracket \text{get-ins } M \ l = \text{Some } (\text{iftrue } pc); \text{NEXT} = (os, s, h) \rrbracket \\
\quad \Longrightarrow (M, l, (\text{TRUE} \# os, s, h), 1, pc, \text{NEXT}) : \text{Step} \\
| \\
\text{IfF: } \llbracket \text{get-ins } M \ l = \text{Some } (\text{iftrue } pc); v \neq \text{TRUE}; \text{NEXT} = (os, s, h); ll=l+1 \rrbracket \\
\quad \Longrightarrow (M, l, (v \# os, s, h), 1, ll, \text{NEXT}) : \text{Step} \\
| \\
\text{InvS: } \llbracket \text{get-ins } M \ l = \text{Some } (\text{invokeS } C \ m); \text{mbody-is } C \ m \ (\text{par}, \text{code}, l0); \\
\quad ((\text{par}, \text{code}, l0), l0, (\square, S, h), n, hh, v) : \text{Exec}; \\
\quad (\text{ops}, \text{par}, S, os) : \text{Frame}; \text{NEXT} = (v \# os, s, hh); ll = l+1 \rrbracket \\
\quad \Longrightarrow (M, l, (\text{ops}, s, h), \text{Suc } n, ll, \text{NEXT}) : \text{Step} \\
| \\
\text{Vret: } \llbracket \text{get-ins } M \ l = \text{Some } \text{vreturn} \rrbracket \Longrightarrow (M, l, (v \# os, s, h), 1, h, v) : \text{Exec} \\
| \\
\text{Run: } \llbracket (M, l, s, n, ll, t) : \text{Step}; (M, ll, t, m, h, v) : \text{Exec}; k = (\max n \ m) + 1 \rrbracket \\
\quad \Longrightarrow (M, l, s, k, h, v) : \text{Exec}
\end{array}$$

A big-step operational judgement that abstracts from the derivation height is easily defined.

definition $\text{Opsem} :: \text{Mbody} \Rightarrow \text{Label} \Rightarrow \text{State} \Rightarrow \text{Heap} \Rightarrow \text{Val} \Rightarrow \text{bool}$
where $\text{Opsem } M \ l \ s \ h \ v = (\exists n . (M, l, s, n, h, v) : \text{Exec})$

2.3 Basic properties

We provide elimination lemmas for the inductively defined relations

inductive-cases *eval-cases*:

$(M, l, s, n, ll, t) : \text{Step}$
 $(M, l, s, n, h, v) : \text{Exec}$

and observe that no derivations of height 0 exist.

lemma *no-zero-height-Step-deriv*: $(M, l, s, 0, ll, t) : \text{Step} \Longrightarrow \text{False}$

lemma *no-zero-height-Exec-deriv*: $(M, l, s, 0, h, v) : \text{Exec} \Longrightarrow \text{False}$

By induction on the derivation system one can show determinism.

lemma *Step-determ*:

$\llbracket (M, l, s, n, ll, t) \in \text{Step}; (M, l, s, m, ll, r) : \text{Step} \rrbracket \Longrightarrow n=m \wedge t=r \wedge ll=l2$

lemma *Exec-determ*:

$\llbracket (M, l, s, n, h, v) \in \text{Exec}; (M, l, s, m, k, w) : \text{Exec} \rrbracket \Longrightarrow n=m \wedge h=k \wedge v=w$

3 Axiomatic semantics

3.1 Assertion forms

We introduce two further kinds of states. Initial states do not contain operand stacks, terminal states lack operand stacks and local variables, but include return values.

type-synonym $InitState = Store \times Heap$
type-synonym $TermState = Heap \times Val$

A judgements relating to a specific program point $C.m.l$ consists of pre- and post-conditions, an invariant, and – optionally – a local annotation. Local pre-conditions and annotations relate initial states to states, i.e. are of type

type-synonym $Assn = InitState \Rightarrow State \Rightarrow bool$

Post-conditions additionally depend on a terminal state

type-synonym $Post = InitState \Rightarrow State \Rightarrow TermState \Rightarrow bool$

Invariants hold for the heap components of all future (reachable) states in the current frame as well as its subframes. They relate these heaps to the current state and the initial state of the current frame.

type-synonym $Inv = InitState \Rightarrow State \Rightarrow Heap \Rightarrow bool$

Local annotations of a method implementation are collected in a table of type

type-synonym $ANNO = (Label, Assn) AssList$

Implicitly, the labels are always interpreted with respect to the current method. In addition to such a table, the behaviour of methods is specified by a partial-correctness assertion of type

type-synonym $MethSpec = InitState \Rightarrow TermState \Rightarrow bool$

and a method invariant of type

type-synonym $MethInv = InitState \Rightarrow Heap \Rightarrow bool$

A method invariant is expected to be satisfied by the heap components of all states during the execution of the method, including states in subframes, irrespectively of the termination behaviour.

All method specifications are collected in a table of type

type-synonym $MSPEC = (Class \times Method, MethSpec \times MethInv \times ANNO) AssList$

A table of this type assigns to each method a partial-correctness specification, a method invariant, and a table of local annotations for the instructions in this method.

3.2 Proof system

The proof system derives judgements of the form $G \triangleright \{A\}C, m, l \{B\} I$ where A is a pre-condition, B is a post-condition, I is a strong invariant, $C.m.l$ represents a program point, and G is a proof context (see below). The proof

system consists of syntax-directed rules and structural rules. These rules are formulated in such a way that assertions in the conclusions are unconstrained, i.e. a rule can be directly applied to derive a judgement. In the case of the syntax-directed rules, the hypotheses require the derivation of related statements for the control flow successor instructions. Judgements occurring as hypotheses involve assertions that are notationally constrained, and relate to the conclusions' assertions via uniform constructions that resemble strongest postconditions in Hoare-style logics.

On pre-conditions, the operator

definition $SP\text{-pre}::Mbody \Rightarrow Label \Rightarrow Assn \Rightarrow Assn$
where $SP\text{-pre } M \ l \ A = (\lambda \ s0 \ r \ . \ (\exists \ s \ l1 \ n. \ A \ s0 \ s \wedge (M, l, s, n, l1, r):Step))$

constructs an assertion that holds of a state r precisely if the argument assertion A held at the predecessor state of r . Similar readings explain the constructions on post-conditions

definition $SP\text{-post}::Mbody \Rightarrow Label \Rightarrow Post \Rightarrow Post$
where $SP\text{-post } M \ l \ B = (\lambda \ s0 \ r \ t \ . \ (\forall \ s \ l1 \ n. \ (M, l, s, n, l1, r):Step \longrightarrow B \ s0 \ s \ t))$

and invariants

definition $SP\text{-inv}::Mbody \Rightarrow Label \Rightarrow Inv \Rightarrow Inv$
where $SP\text{-inv } M \ l \ I = (\lambda \ s0 \ r \ h \ . \ \forall \ s \ l1 \ n. \ (M, l, s, n, l1, r):Step \longrightarrow I \ s0 \ s \ h)$

For the basic instructions, the appearance of the single-step execution relation in these constructions makes the strongest-postcondition interpretation apparent, but could easily be eliminated by unfolding the definition of $Step$. In the proof rule for static method invocations, such a direct reference to the operational semantics is clearly undesirable. Instead, the proof rule extracts the invoked method's specification from the specification table. In order to simplify the formulation of the proof rule, we introduce three operators which manipulate the extracted assertions in a similar way as the above SP -operators.

definition $SINV\text{-pre}::Var \ list \Rightarrow MethSpec \Rightarrow Assn \Rightarrow Assn$ **where**
 $SINV\text{-pre } par \ T \ A =$
 $(\lambda \ s0 \ s \ . \ (\exists \ ops1 \ ops2 \ S \ R \ h \ k \ w.$
 $(ops1, par, R, ops2) : Frame \wedge T (R, k) (h, w) \wedge$
 $A \ s0 \ (ops1, S, k) \wedge s = (w\#ops2, S, h)))$

definition $SINV\text{-post}::Var \ list \Rightarrow MethSpec \Rightarrow Post \Rightarrow Post$ **where**
 $SINV\text{-post } par \ T \ B =$
 $(\lambda \ s0 \ s \ t \ . \ \forall \ ops1 \ ops2 \ S \ R \ h \ k \ w \ r.$
 $(ops1, par, R, ops2) : Frame \longrightarrow T (R, k) (h, w) \longrightarrow$
 $s=(w\#ops2, S, h) \longrightarrow r=(ops1, S, k) \longrightarrow B \ s0 \ r \ t)$

definition $SINV\text{-inv}::Var \ list \Rightarrow MethSpec \Rightarrow Inv \Rightarrow Inv$ **where**
 $SINV\text{-inv } par \ T \ I =$

$$\begin{aligned}
& (\lambda s0 s h' . \forall ops1 ops2 S R h k w . \\
& \quad (ops1, par, R, ops2) : Frame \longrightarrow T (R, k) (h, w) \longrightarrow \\
& \quad s = (w \# ops2, S, h) \longrightarrow I s0 (ops1, S, k) h')
\end{aligned}$$

The derivation system is formulated using contexts G of proof-theoretic assumptions representing local judgements. The type of contexts is

type-synonym $CTXT = (Class \times Method \times Label, Assn \times Post \times Inv) AssList$

The existence of the proof context also motivates that the hypotheses in the syntax-directed rules are formulated using an auxiliary judgement form, $G \triangleright \langle A \rangle C, m, l \langle B \rangle I$. Statements for this auxiliary form can be derived by essentially two rules. The first rule, AX , allows us to extract assumptions from the context, while the second rule, $INJECT$, converts an ordinary judgement $G \triangleright \{A\} C, m, l \{B\} I$ into $G \triangleright \langle A \rangle C, m, l \langle B \rangle I$. No rule is provided for a direct embedding in the opposite direction. As a consequence of this formulation, contextual assumptions cannot be used directly to justify a statement $G \triangleright \{A\} C, m, l \{B\} I$. Instead, the extraction of such an assumption has to be followed by at least one "proper" (syntax-driven) rule. In particular, attempts to verify jumps by assuming a judgement and immediately using it to prove the rule's hypothesis are ruled out. More specifically, the purpose of this technical device will become obvious when we discharge the context in the proof of soundness (Section 5.3). Here, each entry $((C', m', l'), (A', B', I'))$ of the context will need to be justified by a derivation $G \triangleright \{A'\} C', m', l' \{B'\} I'$. This justification should in principle be allowed to rely on other entries of the context. However, an axiom rule for judgements $G \triangleright \{A\} C, m, l \{B\} I$ would allow a trivial discharge of any context entry. The introduction of the auxiliary judgement form $G \triangleright \langle A \rangle C, m, l \langle B \rangle I$ thus ensures that the discharge of a contextual assumption involves at least one application of a "proper" (i.e. syntax-directed) rule. Rule $INJECT$ is used to chain together syntax-directed rules. Indeed, it allows us to discharge a hypothesis $G \triangleright \langle A \rangle C, m, l \langle B \rangle I$ of a syntax-directed rule using a derivation of $G \triangleright \{A\} C, m, l \{B\} I$.

The proof rules are defined relative to a fixed method specification table MST .

axiomatization $MST :: MSPEC$

In Isabelle, the distinction between the two judgement forms may for example be achieved by introducing a single judgement form that includes a boolean flag, and defining two pretty-printing notions.

inductive-set $SP\text{-Judgement} ::$

$(bool \times CTXT \times Class \times Method \times Label \times Assn \times Post \times Inv) set$

and

$SP\text{-Deriv} :: CTXT \Rightarrow Assn \Rightarrow Class \Rightarrow Method \Rightarrow Label \Rightarrow$
 $Post \Rightarrow Inv \Rightarrow bool$

$(\langle \cdot \triangleright \{ \cdot \} \cdot, \cdot, \cdot, \{ \cdot \} \cdot \rightarrow [100, 100, 100, 100, 100, 100, 100, 100] 50)$
and
 $SP\text{-Assum} :: CTXT \Rightarrow Assn \Rightarrow Class \Rightarrow Method \Rightarrow Label \Rightarrow$
 $Post \Rightarrow Inv \Rightarrow bool$
 $(\langle \cdot \triangleright \langle \cdot \rangle \cdot, \cdot, \cdot, \langle \cdot \rangle \cdot \rightarrow [100, 100, 100, 100, 100, 100, 100, 100] 50)$
where
 $G \triangleright \{ A \} C, m, l \{ B \} I == (False, G, C, m, l, A, B, I):SP\text{-Judgement}$
 $|$
 $G \triangleright \langle A \rangle C, m, l \langle B \rangle I == (True, G, C, m, l, A, B, I):SP\text{-Judgement}$
 $|$
INSTR:
 $\llbracket mbody\text{-is } C \ m \ M; \text{get-ins } M \ l = \text{Some } ins;$
 $lookup \ MST \ (C, m) = \text{Some } (Mspec, Minv, Anno);$
 $\forall Q . lookup \ Anno \ l = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s);$
 $\forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s);$
 $ins \in \{ const \ c, dup, pop, swap, load \ x, store \ x, binop \ f, unop \ g,$
 $new \ d, getfield \ d \ F, putfield \ d \ F, checkcast \ d \};$
 $G \triangleright \langle (SP\text{-pre } M \ l \ A) \rangle C, m, (l+1) \langle (SP\text{-post } M \ l \ B) \rangle (SP\text{-inv } M \ l \ I) \rrbracket$
 $\implies G \triangleright \{ A \} C, m, l \{ B \} I$
 $|$
GOTO:
 $\llbracket mbody\text{-is } C \ m \ M; \text{get-ins } M \ l = \text{Some } (goto \ pc);$
 $MST \downarrow (C, m) = \text{Some } (Mspec, Minv, Anno);$
 $\forall Q . Anno \downarrow (l) = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s);$
 $\forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s);$
 $G \triangleright \langle SP\text{-pre } M \ l \ A \rangle C, m, pc \langle SP\text{-post } M \ l \ B \rangle (SP\text{-inv } M \ l \ I) \rrbracket$
 $\implies G \triangleright \{ A \} C, m, l \{ B \} I$
 $|$
IF:
 $\llbracket mbody\text{-is } C \ m \ M; \text{get-ins } M \ l = \text{Some } (iftrue \ pc);$
 $MST \downarrow (C, m) = \text{Some } (Mspec, Minv, Anno);$
 $\forall Q . Anno \downarrow (l) = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s);$
 $\forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s);$
 $G \triangleright \langle SP\text{-pre } M \ l \ (\lambda s0 \ s . (\forall ops \ S \ k . s = (TRUE \# ops, S, k) \longrightarrow A \ s0 \ s)) \rangle$
 C, m, pc
 $\langle SP\text{-post } M \ l \ (\lambda s0 \ s \ t .$
 $(\forall ops \ S \ k . s = (TRUE \# ops, S, k) \longrightarrow B \ s0 \ s \ t)) \rangle$
 $(SP\text{-inv } M \ l \ (\lambda s0 \ s \ t .$
 $(\forall ops \ S \ k . s = (TRUE \# ops, S, k) \longrightarrow I \ s0 \ s \ t)) \rangle);$
 $G \triangleright \langle SP\text{-pre } M \ l \ (\lambda s0 \ s .$
 $(\forall ops \ S \ k \ v . s = (v \# ops, S, k) \longrightarrow v \neq TRUE \longrightarrow A \ s0 \ s)) \rangle$
 $C, m, (l+1)$
 $\langle SP\text{-post } M \ l \ (\lambda s0 \ s \ t .$
 $(\forall ops \ S \ k \ v . s = (v \# ops, S, k) \longrightarrow v \neq TRUE \longrightarrow B \ s0 \ s \ t)) \rangle$
 $(SP\text{-inv } M \ l \ (\lambda s0 \ s \ t .$
 $(\forall ops \ S \ k \ v . s = (v \# ops, S, k) \longrightarrow v \neq TRUE \longrightarrow I \ s0 \ s \ t)) \rangle \rrbracket$
 $\implies G \triangleright \{ A \} C, m, l \{ B \} I$

$$\begin{aligned}
& \text{VRET:} \\
& \llbracket \text{mbody-is } C \ m \ M; \text{ get-ins } M \ l = \text{Some vreturn}; \\
& \quad \text{MST}\downarrow(C,m) = \text{Some } (M\text{spec}, \text{Minv}, \text{Anno}); \\
& \quad \forall Q . \text{Anno}\downarrow(l) = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s); \\
& \quad \forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s); \\
& \quad \forall s0 \ s . A \ s0 \ s \longrightarrow (\forall v \ \text{ops } S \ h . s = (v\#\text{ops}, S, h) \longrightarrow B \ s0 \ s \ (h, v)) \rrbracket \\
& \Longrightarrow G \triangleright \llbracket A \rrbracket C, m, l \llbracket B \rrbracket I \\
& \text{INVS:} \\
& \llbracket \text{mbody-is } C \ m \ M; \text{ get-ins } M \ l = \text{Some } (\text{invokeS } D \ m'); \\
& \quad \text{MST}\downarrow(C,m) = \text{Some } (M\text{spec}, \text{Minv}, \text{Anno}); \\
& \quad \text{MST}\downarrow(D, m') = \text{Some } (T, MI, \text{Anno}2); \text{mbody-is } D \ m' \ (\text{par}, \text{code}, l0); \\
& \quad \forall Q . \text{Anno}\downarrow(l) = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s); \\
& \quad \forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s); \\
& \quad \forall s0 \ \text{ops1} \ \text{ops2} \ S \ R \ k \ t . (\text{ops1}, \text{par}, R, \text{ops2}) : \text{Frame} \longrightarrow \\
& \quad \quad A \ s0 \ (\text{ops1}, S, k) \longrightarrow MI \ (R, k) \ t \longrightarrow I \ s0 \ (\text{ops1}, S, k) \ t; \\
& \quad G \triangleright \langle \text{SINV-pre par } T \ A \rangle C, m, (l+1) \langle \text{SINV-post par } T \ B \rangle \\
& \quad \quad (\text{SINV-inv par } T \ I) \rrbracket \\
& \Longrightarrow G \triangleright \llbracket A \rrbracket C, m, l \llbracket B \rrbracket I \\
& \text{CONSEQ:} \\
& \llbracket (b, G, C, m, l, AA, BB, II) \in \text{SP-Judgement}; \\
& \quad \forall s0 \ s . A \ s0 \ s \longrightarrow AA \ s0 \ s; \forall s0 \ s \ t . BB \ s0 \ s \ t \longrightarrow B \ s0 \ s \ t; \\
& \quad \forall s0 \ s \ k . II \ s0 \ s \ k \longrightarrow I \ s0 \ s \ k \rrbracket \\
& \Longrightarrow (b, G, C, m, l, A, B, I) \in \text{SP-Judgement} \\
& \text{INJECT: } \llbracket G \triangleright \llbracket A \rrbracket C, m, l \llbracket B \rrbracket I \rrbracket \Longrightarrow G \triangleright \langle A \rangle C, m, l \langle B \rangle I \\
& \text{AX:} \\
& \llbracket G\downarrow(C, m, l) = \text{Some } (A, B, I); \text{MST}\downarrow(C, m) = \text{Some } (M\text{spec}, \text{Minv}, \text{Anno}); \\
& \quad \forall Q . \text{Anno}\downarrow(l) = \text{Some } Q \longrightarrow (\forall s0 \ s . A \ s0 \ s \longrightarrow Q \ s0 \ s); \\
& \quad \forall s0 \ s . A \ s0 \ s \longrightarrow I \ s0 \ s \ (\text{heap } s) \rrbracket \\
& \Longrightarrow G \triangleright \langle A \rangle C, m, l \langle B \rangle I
\end{aligned}$$

As a first consequence, we can prove by induction on the proof system that a derivable judgement entails its strong invariant and an annotation that may be attached to the instruction.

lemma *AssertionsImPLYMethInvariants:*

$\llbracket G \triangleright \llbracket A \rrbracket C, m, l \llbracket B \rrbracket I; A \ s0 \ s \rrbracket \Longrightarrow I \ s0 \ s \ (\text{heap } s)$

lemma *AssertionsImPLYAnnoInvariants:*

$\llbracket G \triangleright \llbracket A \rrbracket C, m, l \llbracket B \rrbracket I; \text{MST}\downarrow(C, m) = \text{Some}(M\text{spec}, \text{Minv}, \text{Anno}); \\ \text{Anno}\downarrow(l) = \text{Some } Q; A \ s0 \ s \rrbracket \Longrightarrow Q \ s0 \ s$

For *verified programs*, all preconditions can be justified by proof derivations, and initial labels of all methods (again provably) satisfy the method preconditions.

definition $\text{mkState}::\text{InitState} \Rightarrow \text{State}$

where $\text{mkState } s0 = ([], \text{fst } s0, \text{snd } s0)$

definition $mkPost::MethSpec \Rightarrow Post$
where $mkPost T = (\lambda s0 s t . s=mkState s0 \longrightarrow T s0 t)$

definition $mkInv::MethInv \Rightarrow Inv$
where $mkInv MI = (\lambda s0 s t . s=mkState s0 \longrightarrow MI s0 t)$

definition $VP-G::CTXT \Rightarrow bool$ **where**
 $VP-G G =$
 $((\forall C m l A B I . G\downarrow(C,m,l) = Some (A,B,I) \longrightarrow G \triangleright \{\!\{A\}\!\} C,m,l \{\!\{B\}\!\} I) \wedge$
 $(\forall C m \text{ par code } l0 T MI Anno .$
 $\text{mbody-is } C m (\text{par},\text{code},l0) \longrightarrow MST\downarrow(C,m) = Some(T,MI,Anno) \longrightarrow$
 $G \triangleright \{\!\{(\lambda s0 s . s = mkState s0)\}\!\} C,m,l0 \{\!\{mkPost T\}\!\} (mkInv MI)))$

definition $VP::bool$ **where** $VP = (\exists G . VP-G G)$

4 Auxiliary operational judgements

Beside the basic operational judgements *Step* and *Exec*, the interpretation of judgements refers to two multi-step relations which we now define.

4.1 Multistep execution

The first additional operational judgement is the reflexive and transitive closure of *Step*. It relates states s and t if the latter can be reached from the former by a chain of single steps, all in the same frame. Note that t does not need to be a terminal state. As was the case in the definition of *Step*, we first define a relation with an explicit derivation height index (*MStep*).

inductive-set

$MStep::(Mbody \times Label \times State \times nat \times Label \times State) set$

where

$MS\text{-zero}: \llbracket k=0; t=s; ll=l \rrbracket \Longrightarrow (M,l,s,k,ll,t):MStep$

|

$MS\text{-step}: \llbracket (M,l,s,n,l1,r):Step; (M,l1,r,k,l2,t):MStep; m=Suc k+n \rrbracket$
 $\Longrightarrow (M,l,s,m,l2,t) : MStep$

The following properties of *MStep* are useful to notice.

lemma *ZeroHeightMultiElim*: $(M,l,s,0,ll,r) \in MStep \Longrightarrow r=s \wedge ll=l$

lemma *MultiSplit*:

$\llbracket (M, l, s, k, ll, t) \in MStep; 1 \leq k \rrbracket \Longrightarrow$

$\exists n m r l1 . (M,l,s,n,l1,r):Step \wedge (M,l1,r,m,ll,t):MStep \wedge Suc m + n = k$

lemma *MStep-returnElim*:

$\llbracket (M,l,s,k,ll,t) \in MStep; \text{get-ins } M l = Some \text{ vreturn} \rrbracket \Longrightarrow t=s \wedge ll = l$

lemma *MultiApp*:

$\llbracket (M,l,s,k,l1,r):MStep; (M,l1,r,n,l2,t):Step \rrbracket \Longrightarrow (M,l,s,Suc k+n,l2,t):MStep$

lemma *MStep-Compose*:

$$\begin{aligned} & \llbracket (M, l, s, n, ll, r):MStep; (M, ll, r, k, l2, t):MStep; nk=n+k \rrbracket \\ & \implies (M, l, s, nk, l2, t):MStep \end{aligned}$$

Here are two simple lemmas relating the operational judgements.

lemma *MStep-Exec1*:

$$\begin{aligned} & \llbracket (M, l, s, kb, ll, t) \in MStep; (M, l, s, k, hh, v) \in Exec \rrbracket \\ & \implies \exists n. (M, ll, t, n, hh, v) \in Exec \end{aligned}$$

lemma *MStep-Exec2*:

$$\begin{aligned} & \llbracket (M, l, s, kb, ll, t) \in MStep; (M, ll, t, k, hh, v) \in Exec \rrbracket \\ & \implies \exists n. (M, l, s, n, hh, v) \in Exec \end{aligned}$$

Finally, the definition of the non-height-indexed relation.

definition $MS::Mbody \Rightarrow Label \Rightarrow State \Rightarrow Label \Rightarrow State \Rightarrow bool$
where $MS\ M\ l\ s\ ll\ t = (\exists\ k.\ (M, l, s, k, ll, t):MStep)$

4.2 Reachability relation

The second auxiliary operational judgement is required for the interpretation of invariants and method invariants. Invariants are expected to be satisfied in all heap components of (future) states that occur either in the same frame as the current state or a subframe thereof. Likewise, method invariants are expected to be satisfied by all heap components of states observed during the execution of a method, including subframes. None of the previous three operational judgements allows us to express these interpretations, as *Step* injects the execution of an invoked method as a single step. Thus, states occurring in subframes cannot be related to states occurring in the parent frame using these judgements. This motivates the introduction of predicates relating states s and t whenever the latter can be reach from the former, i.e. whenever t occurs as a successor of s in the same frame as s or one of its subframes. Again, we first define a relation that includes an explicit derivation height index.

inductive-set

Reachable:: $(Mbody \times Label \times State \times nat \times State)\ set$

where

Reachable-zero: $\llbracket k=0; t=s \rrbracket \implies (M, l, s, k, t):Reachable$

|

Reachable-step:

$$\begin{aligned} & \llbracket (M, l, s, n, ll, r):Step; (M, ll, r, m, t):Reachable; k=Suc\ m+n \rrbracket \\ & \implies (M, l, s, k, t) : Reachable \end{aligned}$$

|

Reachable-invS:

$$\begin{aligned} & \llbracket mbody-is\ C\ m\ (par, code, l0); get-ins\ M\ l = Some\ (invokeS\ C\ m); \\ & \quad s = (ops, S, h); (ops, par, R, ops1):Frame; \\ & \quad ((par, code, l0), l0, ([], R, h), n, t):Reachable; k=Suc\ n \rrbracket \\ & \implies (M, l, s, k, t) : Reachable \end{aligned}$$

The following properties of are useful to notice.

lemma *ZeroHeightReachableElim*: $(M, l, s, 0, r) \in \text{Reachable} \implies r = s$

lemma *ReachableSplit*[rule-format]:

$$\begin{aligned} & (M, l, s, k, t) \in \text{Reachable} \implies \\ & \quad 1 \leq k \longrightarrow \\ & \quad ((\exists n \ m \ r \ ll. (M, l, s, n, ll, r): \text{Step} \wedge \\ & \quad \quad (M, ll, r, m, t): \text{Reachable} \wedge \text{Suc } m + n = k) \vee \\ & \quad (\exists n \ ops \ S \ h \ c \ m \ par \ R \ ops1 \ code \ l0. \\ & \quad \quad s = (ops, S, h) \wedge \text{get-ins } M \ l = \text{Some } (\text{invokeS } c \ m) \wedge \\ & \quad \quad \text{mbody-is } c \ m \ (par, code, l0) \wedge (ops, par, R, ops1): \text{Frame} \wedge \\ & \quad \quad ((par, code, l0), l0, ([], R, h), n, t): \text{Reachable} \wedge \text{Suc } n = k)) \end{aligned}$$

lemma *Reachable-returnElim*[rule-format]:

$$(M, l, s, k, t) \in \text{Reachable} \implies \text{get-ins } M \ l = \text{Some } v \text{return} \longrightarrow t = s$$

Similar to the operational semantics, we define a variation of the reachability relation that hides the index.

definition *Reach*:: $Mbody \Rightarrow Label \Rightarrow State \Rightarrow State \Rightarrow bool$

where *Reach* $M \ l \ s \ t = (\exists k . (M, l, s, k, t): \text{Reachable})$

5 Soundness

This section contains the soundness proof of the program logic. In the first subsection, we define our notion of validity, thus formalising our intuitive explanation of the terms preconditions, specifications, and invariants. The following two subsections contain the details of the proof and can easily be skipped during a first pass through the document.

5.1 Validity

A judgement is valid at the program point $C.m.l$ (i.e. at label l in method m of class C), written *valid* $C \ m \ l \ A \ B \ I$ or, in symbols,

$$\models \{A\} C, m, l \{B\} I,$$

if A is a precondition for B and for all local annotations following l in an execution of m , and all reachable states in the current frame or yet-to-be created subframes satisfy I . More precisely, whenever an execution of the method starting in an initial state s_0 reaches the label l with state s , the following properties are implied by $A(s_0, s)$.

1. If the continued execution from s reaches a final state t (i.e. the method terminates), then that final state t satisfies $B(s_0, s, t)$.
2. Any state s' visited in the current frame during the remaining program execution whose label carries an annotation Q will satisfy $Q(s_0, s')$, even if the execution of the frame does not terminate.

3. Any state s' visited in the current frame or a subframe of the current frame will satisfy $I(s_0, s, \text{heap}(s'))$, again even if the execution does not terminate.

Formally, this interpretation is expressed as follows.

definition $\text{valid} :: \text{Class} \Rightarrow \text{Method} \Rightarrow \text{Label} \Rightarrow \text{Assn} \Rightarrow \text{Post} \Rightarrow \text{Inv} \Rightarrow \text{bool}$ **where**
 $\text{valid } C \ m \ l \ A \ B \ I =$

$$\begin{aligned}
& (\forall M. \text{mbody-is } C \ m \ M \longrightarrow \\
& (\forall \text{Mspec } \text{Minv } \text{Anno} . \text{MST}\downarrow(C, m) = \text{Some}(\text{Mspec}, \text{Minv}, \text{Anno}) \longrightarrow \\
& (\forall \text{par } \text{code } l0 . M = (\text{par}, \text{code}, l0) \longrightarrow \\
& (\forall s0 \ s . \text{MS } M \ l0 \ (\text{mkState } s0) \ l \ s \longrightarrow A \ s0 \ s \longrightarrow \\
& ((\forall h \ v . \text{Opsem } M \ l \ s \ h \ v \longrightarrow B \ s0 \ s \ (h, v)) \wedge \\
& (\forall ll \ r . (\text{MS } M \ l \ s \ ll \ r \longrightarrow (\forall Q . \text{Anno}\downarrow(ll) = \text{Some } Q \longrightarrow Q \ s0 \ r)) \wedge \\
& (\text{Reach } M \ l \ s \ r \longrightarrow I \ s0 \ s \ (\text{heap } r))))))
\end{aligned}$$

abbreviation $\text{valid-syntax} :: \text{Assn} \Rightarrow \text{Class} \Rightarrow \text{Method} \Rightarrow$
 $\text{Label} \Rightarrow \text{Post} \Rightarrow \text{Inv} \Rightarrow \text{bool}$

$$(\langle \models \{ \} - \{ \} - , - , - \{ \} - \{ \} \rightarrow [200, 200, 200, 200, 200, 200] \ 200)$$

where $\text{valid-syntax } A \ C \ m \ l \ B \ I == \text{valid } C \ m \ l \ A \ B \ I$

This notion of validity extends that of Bannwart-Müller by allowing the postcondition to differ from method specification and to refer to the initial state, and by including invariants. In the logic of Bannwart and Müller, the validity of a method specification is given by a partial correctness (Hoare-style) interpretation, while the validity of preconditions of individual instructions is such that a precondition at l implies the preconditions of its immediate control flow successors.

Validity is lifted to contexts and the method specification table. In the case of the former, we simply require that all entries be valid.

definition $G\text{-valid} :: \text{CTX} \Rightarrow \text{bool}$ **where**

$$\begin{aligned}
G\text{-valid } G = & (\forall C \ m \ l \ A \ B \ I . G\downarrow(C, m, l) = \text{Some } (A, B, I) \longrightarrow \\
& \models \{A\} \ C, \ m, \ l \ \{B\} \ I)
\end{aligned}$$

Regarding the specification table, we require that the initial label of each method satisfies an assertion that ties the method precondition to the current state.

definition $\text{MST-valid} :: \text{bool}$ **where**

$$\begin{aligned}
\text{MST-valid} = & (\forall C \ m \ \text{par } \text{code } l0 \ T \ MI \ \text{Anno} . \\
& \text{mbody-is } C \ m \ (\text{par}, \text{code}, l0) \longrightarrow \text{MST}\downarrow(C, m) = \text{Some } (T, MI, \text{Anno}) \longrightarrow \\
& \models \{(\lambda s0 \ s . s = \text{mkState } s0)\} \ C, \ m, \ l0 \ \{(\text{mkPost } T)\} \ (\text{mkInv } MI)
\end{aligned}$$

definition $\text{Prog-valid} :: \text{bool}$ **where**

$$\text{Prog-valid} = (\exists G . G\text{-valid } G \wedge \text{MST-valid})$$

The remainder of this section contains a proof of soundness, i.e. of the property

$$VP \Longrightarrow \text{Prog-valid},$$

and is structured into two parts. The first step (Section 5.2) establishes a soundness result where the *VP* property is replaced by validity assumptions regarding the method specification table and the context. In the second step (Section 5.3), we show that these validity assumptions are satisfied by verified programs, which implies the overall soundness theorem.

5.2 Soundness under valid contexts

The soundness proof proceeds by induction on the axiomatic semantics, based on an auxiliary lemma for method invocations that is proven by induction on the derivation height of the operational semantics. For the latter induction, relativised notions of validity are employed that restrict the derivation height of the program continuations affected by an assertion. The appropriate definitions of relativised validity for judgements, for the precondition table, and for the method specification table are as follows.

definition *validn*::

$nat \Rightarrow Class \Rightarrow Method \Rightarrow Label \Rightarrow Assn \Rightarrow Post \Rightarrow Inv \Rightarrow bool$ **where**
 $validn\ K\ C\ m\ l\ A\ B\ I =$
 $(\forall\ M.\ mbody\text{-}is\ C\ m\ M \longrightarrow$
 $(\forall\ Mspec\ Minv\ Anno.\ MST\downarrow(C,m) = Some(Mspec,Minv,Anno) \longrightarrow$
 $(\forall\ par\ code\ l0.\ M = (par,code,l0) \longrightarrow$
 $(\forall\ s0\ s.\ MS\ M\ l0\ (mkState\ s0)\ l\ s \longrightarrow A\ s0\ s \longrightarrow$
 $(\forall\ k.\ k \leq K \longrightarrow$
 $((\forall\ h\ v.\ (M,l,s,k,h,v):Exec \longrightarrow B\ s0\ s\ (h,v)) \wedge$
 $(\forall\ ll\ r.\ ((M,l,s,k,ll,r):MStep \longrightarrow$
 $(\forall\ Q.\ Anno\downarrow(ll) = Some\ Q \longrightarrow Q\ s0\ r)) \wedge$
 $((M,l,s,k,r):Reachable \longrightarrow I\ s0\ s\ (heap\ r))))))))))$

abbreviation *validn-syntax* :: $nat \Rightarrow Assn \Rightarrow Class \Rightarrow Method \Rightarrow$
 $Label \Rightarrow Post \Rightarrow Inv \Rightarrow bool$
 $(\langle \models - \rangle - \} - , - , - \} - \} - \} - \} [200,200,200,200,200,200,200,200] 200)$
where *validn-syntax* $K\ A\ C\ m\ l\ B\ I == validn\ K\ C\ m\ l\ A\ B\ I$

definition *G-validn*:: $nat \Rightarrow CTXT \Rightarrow bool$ **where**

$G\text{-}validn\ K\ G = (\forall\ C\ m\ l\ A\ B\ I.\ G\downarrow(C,m,l) = Some\ (A,B,I) \longrightarrow$
 $\models_K \{A\}\ C,\ m,\ l\ \{B\}\ I)$

definition *MST-validn*:: $nat \Rightarrow bool$ **where**

$MST\text{-}validn\ K = (\forall\ C\ m\ par\ code\ l0\ T\ MI\ Anno.$
 $mbody\text{-}is\ C\ m\ (par,code,l0) \longrightarrow MST\downarrow(C,m) = Some\ (T,MI,Anno) \longrightarrow$
 $\models_K \{(\lambda\ s0\ s.\ s = mkState\ s0)\}\ C,\ m,\ l0\ \{(mkPost\ T)\}\ (mkInv\ MI))$

definition *Prog-validn*:: $nat \Rightarrow bool$ **where**

$Prog\text{-}validn\ K = (\exists\ G.\ G\text{-}validn\ K\ G \wedge MST\text{-}validn\ K)$

The relativised notions are related to each other, and to the native notions of validity as follows.

lemma valid-validn: $\models \{A\} C, m, l \{B\} I \implies \models_K \{A\} C, m, l \{B\} I$
lemma validn-valid: $\llbracket \forall K . \models_K \{A\} C, m, l \{B\} I \rrbracket \implies \models \{A\} C, m, l \{B\} I$
lemma validn-lower:
 $\llbracket \models_K \{A\} C, m, l \{B\} I; L \leq K \rrbracket \implies \models_L \{A\} C, m, l \{B\} I$
lemma G-valid-validn: $G\text{-valid } G \implies G\text{-validn } K G$
lemma G-validn-valid: $\llbracket \forall K . G\text{-validn } K G \rrbracket \implies G\text{-valid } G$
lemma G-validn-lower: $\llbracket G\text{-validn } K G; L \leq K \rrbracket \implies G\text{-validn } L G$
lemma MST-validn-valid: $\llbracket \forall K . \text{MST-validn } K \rrbracket \implies \text{MST-valid}$
lemma MST-valid-validn: $\text{MST-valid} \implies \text{MST-validn } K$
lemma MST-validn-lower: $\llbracket \text{MST-validn } K; L \leq K \rrbracket \implies \text{MST-validn } L$

We define an abbreviation for the side conditions of the rule for static method invocations. . .

definition INVS-SC::

$\text{Class} \Rightarrow \text{Method} \Rightarrow \text{Label} \Rightarrow \text{Class} \Rightarrow \text{Method} \Rightarrow \text{MethSpec} \Rightarrow \text{MethInv} \Rightarrow$
 $\text{ANNO} \Rightarrow \text{ANNO} \Rightarrow \text{Mbody} \Rightarrow \text{Assn} \Rightarrow \text{Inv} \Rightarrow \text{bool} \text{ where}$
 $\text{INVS-SC } C m l D m' T MI Anno Anno2 M' A I = (\exists M \text{ par } \text{code } l0 T1 MI1.$
 $\text{mbody-is } C m M \wedge \text{get-ins } M l = \text{Some } (\text{invokeS } D m') \wedge$
 $\text{MST}\downarrow(C, m) = \text{Some } (T1, MI1, Anno) \wedge$
 $\text{MST}\downarrow(D, m') = \text{Some } (T, MI, Anno2) \wedge$
 $\text{mbody-is } D m' M' \wedge M' = (\text{par}, \text{code}, l0) \wedge$
 $(\forall Q . \text{Anno}\downarrow(l) = \text{Some } Q \longrightarrow (\forall s0 s . A s0 s \longrightarrow Q s0 s)) \wedge$
 $(\forall s0 s . A s0 s \longrightarrow I s0 s (\text{heap } s)) \wedge$
 $(\forall s0 \text{ ops1 ops2 } S R h t . (\text{ops1}, \text{par}, R, \text{ops2}) : \text{Frame} \longrightarrow$
 $A s0 (\text{ops1}, S, h) \longrightarrow MI (R, h) t \longrightarrow I s0 (\text{ops1}, S, h) t))$

. . . and another abbreviation for the soundness property of the same rule.

definition INVS-soundK::

$\text{nat} \Rightarrow \text{CTEXT} \Rightarrow \text{Class} \Rightarrow \text{Method} \Rightarrow \text{Label} \Rightarrow \text{Class} \Rightarrow \text{Method} \Rightarrow$
 $\text{MethSpec} \Rightarrow \text{MethInv} \Rightarrow \text{ANNO} \Rightarrow \text{ANNO} \Rightarrow \text{Mbody} \Rightarrow \text{Assn} \Rightarrow$
 $\text{Post} \Rightarrow \text{Inv} \Rightarrow \text{bool} \text{ where}$
 $\text{INVS-soundK } K G C m l D m' T MI Anno Anno2 M' A B I =$
 $(\text{INVS-SC } C m l D m' T MI Anno Anno2 M' A I \longrightarrow$
 $G\text{-validn } K G \longrightarrow \text{MST-validn } K \longrightarrow$
 $\models_K \{(\text{SINV-pre } (\text{fst } M') T A)\} C, m, (l+1)$
 $\{(\text{SINV-post } (\text{fst } M') T B)\} (\text{SINV-inv } (\text{fst } M') T I)$
 $\longrightarrow \models_{(K+1)} \{A\} C, m, l \{B\} I)$

The proof that this property holds for all K proceeds by induction on K .

lemma INVS-soundK-all:

$\text{INVS-soundK } K G C m l D m' T MI Anno Anno2 M' A B I$

The heart of the soundness proof - the induction on the axiomatic semantics.

lemma SOUND-Aux[rule-format]:

$(b, G, C, m, l, A, B, I) : \text{SP-Judgement} \implies G\text{-validn } K G \longrightarrow \text{MST-validn } K \longrightarrow$
 $((b \longrightarrow \models_K \{A\} C, m, l \{B\} I) \wedge$
 $((\neg b) \longrightarrow \models_{(\text{Suc } K)} \{A\} C, m, l \{B\} I))$

The statement of this lemma gives a semantic interpretation of the two judgement forms, as *SP-Assum*-judgements enjoy validity up to execution height K , while *SP-Deriv*-judgements are valid up to level $K + 1$.

From this, we obtain a soundness result that still involves context validity.

theorem *SOUND-in-CTXT*:

$$\llbracket G \triangleright \{A\} C, m, l \{B\} I; G\text{-valid } G; MST\text{-valid} \rrbracket \implies \models \{A\} C, m, l \{B\} I$$

We will now show that the two semantic assumptions can be replaced by the verified-program property.

5.3 Soundness of verified programs

In order to obtain a soundness result that does not require validity assumptions of the context or the specification table, we show that the *VP* property implies context validity. First, the elimination of contexts. By induction on k we prove

lemma *VPG-MSTn-Gn[rule-format]*:

$$VP\text{-}G \ G \longrightarrow MST\text{-}validn \ k \longrightarrow G\text{-}validn \ k \ G$$

which implies

$$\text{lemma } VPG\text{-}MST\text{-}G: \llbracket VP\text{-}G \ G; MST\text{-}valid \rrbracket \implies G\text{-}valid \ G$$

Next, the elimination of *MST-valid*. Again by induction on k , we prove

$$\text{lemma } VPG\text{-}MSTn[\text{rule-format}]: VP\text{-}G \ G \longrightarrow MST\text{-}validn \ k$$

which yields

$$\text{lemma } VPG\text{-}MST: VP\text{-}G \ G \implies MST\text{-}valid$$

Combining these two results, and unfolding the definition of program validity yields the final soundness result.

theorem *VP-VALID*: $VP \implies Prog\text{-}valid$

6 A derived logic for a strong type system

In this section we consider a system of derived assertions, for a type system for bounded heap consumption. The type system arises by reformulating the analysis of Cachera, Jensen, Pichardie, and Schneider [4] for a high-level functional language. The original approach of Cachera et al. consists of formalising the correctness proof of a certain analysis technique in Coq. Consequently, the verification of a program requires the execution of the analysis algorithm inside the theorem prover, which involves the computation of the (method) call graph and fixed point iterations. In contrast, our

approach follows the proof-carrying code paradigm more closely: the analysis amounts to a type inference which is left unformalised and can thus be carried out outside the trusted code base. Only the result of the analysis is communicated to the code recipient. The recipient verifies the validity of the certificate by a largely syntax-directed single-pass traversal of the (low-level) code using a domain-specific program logic. This approach to proof-carrying code was already explored in the MRG project, with respect to program logics of partial correctness [3] and a type system for memory consumption by Hofmann and Jost [7]. In order to obtain syntax-directedness of the proof rules, these had to be formulated at the granularity of typing judgements. In contrast, the present proof system admits proof rules for individual JVM instructions.

Having derived proof rules for individual JVM instructions, we introduce a type system for a small functional language, and a compilation into bytecode. The type system associates a natural number n to an expression e , in a typing context Σ . Informally, the interpretation of a typing judgement $\Sigma \triangleright e : n$ is that the evaluation of e (which may include the invocation of functions whose resource behaviour is specified in Σ) does not perform more than n allocations. The type system is then formally proven sound, using the derived logic for bytecode. By virtue of the invariants, the guarantee given by the present system is stronger than the one given by our encoding of the Hofmann-Jost system, as even non-terminating programs can be verified in a meaningful way.

6.1 Syntax and semantics of judgements

The formal interpretation at JVM level of a type n is given by a triple

$$\text{Cachera}(n) = (A, B, I)$$

consisting of a (trivial) precondition, a post-condition, and a strong invariant.

definition $\text{Cachera}::\text{nat} \Rightarrow (\text{Assn} \times \text{Post} \times \text{Inv})$ **where**
 $\text{Cachera } n = (\lambda s0 \ s \ . \ \text{True},$
 $\lambda s0 \ (\text{ops}, s, h) \ (k, v) \ . \ |k| \leq |h| + n,$
 $\lambda s0 \ (\text{ops}, s, h) \ k. \ |k| \leq |h| + n)$

This definition is motivated by the expectation that $\triangleright\{A\} \lceil e \rceil \{B\} I$ should be derivable whenever the type judgement $\Sigma \triangleright e : n$ holds, where $\lceil e \rceil$ is the translation of compiling the expression e into JVML, and the specification table MST contains the interpretations of the entries in Σ .

We abbreviate the above construction of judgements by a predicate *deriv*.

definition $\text{deriv}::\text{CTXT} \Rightarrow \text{Class} \Rightarrow \text{Method} \Rightarrow \text{Label} \Rightarrow$
 $(\text{Assn} \times \text{Post} \times \text{Inv}) \Rightarrow \text{bool}$ **where**

$deriv\ G\ C\ m\ l\ (ABI) = (let\ (A,B,I) = ABI\ in\ (G \triangleright \{ A \} C, m, l \{ B \} I))$

Thus, the intended interpretation of a typing judgement $\Sigma \triangleright e : n$ is

$$deriv\ C\ m\ l\ (Cachera\ n)$$

if e translates to a code block whose first instruction is at $C.m.l$.

We also define a judgement of the auxiliary form of sequents.

definition $derivAssum::CTXT \Rightarrow Class \Rightarrow Method \Rightarrow Label \Rightarrow$
 $(Assn \times Post \times Inv) \Rightarrow bool$ **where**
 $derivAssum\ G\ C\ m\ l\ (ABI) = (let\ (A,B,I) = ABI\ in\ G \triangleright \langle A \rangle C, m, l \langle B \rangle I)$

The following operation converts a derived judgement into the syntactical form of method specifications.

definition $mkSPEC::(Assn \times Post \times Inv) \Rightarrow ANNO \Rightarrow$
 $(MethSpec \times MethInv \times ANNO)$ **where**
 $mkSPEC\ (ABI)\ Anno = (let\ (A,B,I) = ABI\ in$
 $(\lambda\ s0\ t . B\ s0\ (mkState\ s0)\ t, \lambda\ s0\ h . I\ s0\ (mkState\ s0)\ h, Anno))$

This enables the interpretation of typing contexts Σ as a set of constraints on the specification table MST .

6.2 Derived proof rules

We are now ready to prove derived rules, i.e. proof rules where assumptions as well as conclusions are of the restricted assertion form. While their justification unfolds the definition of the predicate $deriv$, their application will not. We first give syntax-directed proof rules for all JVM instructions:

lemma *CACH-NEW*:

$\llbracket ins-is\ C\ m\ l\ (new\ c); MST \downarrow (C, m) = Some(Mspec, Minv, Anno);$
 $Anno \downarrow (l) = None; n = k + 1; derivAssum\ G\ C\ m\ (l+1)\ (Cachera\ k) \rrbracket$
 $\implies deriv\ G\ C\ m\ l\ (Cachera\ n)$

lemma *CACH-INSTR*:

$\llbracket ins-is\ C\ m\ l\ I;$
 $I \in \{ const\ c, dup, pop, swap, load\ x, store\ x, binop\ f,$
 $unop\ g, getfield\ d\ F, putfield\ d\ F, checkcast\ d \};$
 $MST \downarrow (C, m) = Some(Mspec, Minv, Anno); Anno \downarrow (l) = None;$
 $derivAssum\ G\ C\ m\ (l+1)\ (Cachera\ n) \rrbracket$
 $\implies deriv\ G\ C\ m\ l\ (Cachera\ n)$

lemma *CACH-RET*:

$\llbracket ins-is\ C\ m\ l\ vreturn; MST \downarrow (C, m) = Some(Mspec, Minv, Anno);$
 $Anno \downarrow (l) = None \rrbracket$
 $\implies deriv\ G\ C\ m\ l\ (Cachera\ 0)$

lemma *CACH-GOTO*:

$\llbracket ins-is\ C\ m\ l\ (goto\ pc); MST \downarrow (C, m) = Some(Mspec, Minv, Anno);$
 $Anno \downarrow (l) = None; derivAssum\ G\ C\ m\ pc\ (Cachera\ n) \rrbracket$
 $\implies deriv\ G\ C\ m\ l\ (Cachera\ n)$

lemma CACH-IF:

$$\begin{aligned} & \llbracket \text{ins-is } C \ m \ l \ (\text{iftrue } pc); \text{MST}\downarrow(C,m)=\text{Some}(M\text{spec},M\text{inv},\text{Anno}); \\ & \quad \text{Anno}\downarrow(l) = \text{None}; \text{derivAssum } G \ C \ m \ pc \ (\text{Cachera } n); \\ & \quad \text{derivAssum } G \ C \ m \ (l+1) \ (\text{Cachera } n) \rrbracket \\ & \implies \text{deriv } G \ C \ m \ l \ (\text{Cachera } n) \end{aligned}$$

lemma CACH-INVS:

$$\begin{aligned} & \llbracket \text{ins-is } C \ m \ l \ (\text{invokeS } D \ m'); \text{mbody-is } D \ m' \ (\text{par}, \text{code}, l0); \\ & \quad \text{MST}\downarrow(C,m)=\text{Some}(M\text{spec},M\text{inv},\text{Anno}); \text{Anno}\downarrow(l) = \text{None}; \\ & \quad \text{MST}\downarrow(D, m') = \text{Some}(mk\text{SPEC } (\text{Cachera } k) \ \text{Anno2}); \\ & \quad nk = n+k; \text{derivAssum } G \ C \ m \ (l+1) \ (\text{Cachera } n) \rrbracket \\ & \implies \text{deriv } G \ C \ m \ l \ (\text{Cachera } nk) \end{aligned}$$

In addition, we have two rules for subtyping

lemma CACH-SUB:

$$\llbracket \text{deriv } G \ C \ m \ l \ (\text{Cachera } n); n \leq k \rrbracket \implies \text{deriv } G \ C \ m \ l \ (\text{Cachera } k)$$

lemma CACHAssum-SUB:

$$\begin{aligned} & \llbracket \text{derivAssum } G \ C \ m \ l \ (\text{Cachera } n); n \leq k \rrbracket \\ & \implies \text{derivAssum } G \ C \ m \ l \ (\text{Cachera } k) \end{aligned}$$

and specialised forms of the axiom rule and the injection rule.

lemma CACH-AX:

$$\begin{aligned} & \llbracket G\downarrow(C,m,l) = \text{Some } (\text{Cachera } n); \text{MST}\downarrow(C,m)=\text{Some}(M\text{spec},M\text{inv},\text{Anno}); \\ & \quad \text{Anno}\downarrow(l) = \text{None} \rrbracket \\ & \implies \text{derivAssum } G \ C \ m \ l \ (\text{Cachera } n) \end{aligned}$$

lemma CACH-INJECT:

$$\text{deriv } G \ C \ m \ l \ (\text{Cachera } n) \implies \text{derivAssum } G \ C \ m \ l \ (\text{Cachera } n)$$

Finally, a verified-program rule relates specifications to judgements for the method bodies. Thus, even the method specifications may be given as derived assertions (modulo the *mkSPEC*-conversion).

lemma CACH-VP:

$$\begin{aligned} & \llbracket \forall c \ m \ \text{par} \ \text{code} \ l0. \text{mbody-is } c \ m \ (\text{par}, \text{code}, l0) \longrightarrow \\ & \quad (\exists n \ \text{Anno} . \text{MST}\downarrow(c,m) = \text{Some}(mk\text{SPEC}(\text{Cachera } n) \ \text{Anno}) \wedge \\ & \quad \quad \text{deriv } G \ c \ m \ l0 \ (\text{Cachera } n)); \\ & \quad \forall c \ m \ l \ A \ B \ I. G\downarrow(c,m,l) = \text{Some}(A,B,I) \longrightarrow \\ & \quad (\exists n . (A,B,I) = \text{Cachera } n \wedge \text{deriv } G \ c \ m \ l \ (\text{Cachera } n)) \rrbracket \\ & \implies VP \end{aligned}$$

6.3 Soundness of high-level type system

We define a first-order functional language where expressions are stratified into primitive expressions and general expressions. The language supports the construction of lists using constructors *NilPrim* and *ConsPrim h t*, and includes a corresponding pattern match operation. In order to simplify the compilation, function identifiers are taken to be pairs of class names and method names.

type-synonym $Fun = Class \times Method$

```

datatype Prim =
  IntPrim int
| UnPrim Val  $\Rightarrow$  Val Var
| BinPrim Val  $\Rightarrow$  Val  $\Rightarrow$  Val Var Var
| NilPrim
| ConsPrim Var Var
| CallPrim Fun Var list

```

```

datatype Expr =
  PrimE Prim
| LetE Var Prim Expr
| CondE Var Expr Expr
| MatchE Var Expr Var Var Expr

```

```

type-synonym FunProg = (Fun, Var list  $\times$  Expr) AssList

```

The type system uses contexts that associate a type (natural number) to function identifiers.

```

type-synonym TP-Sig = (Fun, nat) AssList

```

We first give the rules for primitive expressions.

```

inductive-set TP-prim::(TP-Sig  $\times$  Prim  $\times$  nat) set
where
  TP-int:  $(\Sigma, \text{IntPrim } i, 0) : \text{TP-prim}$ 
|
  TP-un:  $(\Sigma, \text{UnPrim } f \ x, 0) : \text{TP-prim}$ 
|
  TP-bin:  $(\Sigma, \text{BinPrim } f \ x \ y, 0) : \text{TP-prim}$ 
|
  TP-nil:  $(\Sigma, \text{NilPrim}, 0) : \text{TP-prim}$ 
|
  TP-cons:  $(\Sigma, \text{ConsPrim } x \ y, 1) : \text{TP-prim}$ 
|
  TP-Call:  $\llbracket \Sigma \downarrow f = \text{Some } n \rrbracket \Longrightarrow (\Sigma, \text{CallPrim } f \ \text{args}, n) : \text{TP-prim}$ 

```

Next, the rules for general expressions.

```

inductive-set TP-expr::(TP-Sig  $\times$  Expr  $\times$  nat) set
where
  TP-sub:  $\llbracket (\Sigma, e, m) : \text{TP-expr}; m \leq n \rrbracket \Longrightarrow (\Sigma, e, n) : \text{TP-expr}$ 
|
  TP-prim:  $\llbracket (\Sigma, p, n) : \text{TP-prim} \rrbracket \Longrightarrow (\Sigma, \text{PrimE } p, n) : \text{TP-expr}$ 
|
  TP-let:  $\llbracket (\Sigma, p, k) : \text{TP-prim}; (\Sigma, e, m) : \text{TP-expr}; n = k+m \rrbracket$ 
     $\Longrightarrow (\Sigma, \text{LetE } x \ p \ e, n) : \text{TP-expr}$ 
|
  TP-Cond:  $\llbracket (\Sigma, e1, n) : \text{TP-expr}; (\Sigma, e2, n) : \text{TP-expr} \rrbracket$ 
     $\Longrightarrow (\Sigma, \text{CondE } x \ e1 \ e2, n) : \text{TP-expr}$ 
|

```

$$\begin{aligned}
& TP\text{-Match}::[(\Sigma, e1, n):TP\text{-expr}; (\Sigma, e2, n):TP\text{-expr}] \\
& \implies (\Sigma, MatchE\ x\ e1\ h\ t\ e2, n):TP\text{-expr}
\end{aligned}$$

A functional program is well-typed if its domain agrees with that of some context such that each function body validates the context entry.

definition $TP::TP\text{-Sig} \Rightarrow FunProg \Rightarrow bool$ **where**
 $TP\ \Sigma\ F = ((\forall f . (\Sigma \downarrow f = None) = (F \downarrow f = None)) \wedge$
 $(\forall f\ n\ par\ e . \Sigma \downarrow f = Some\ n \longrightarrow F \downarrow f = Some\ (par, e) \longrightarrow (\Sigma, e, n):TP\text{-expr}))$

For the translation into bytecode, we introduce identifiers for a class of lists, the expected field names, and a temporary (reserved) variable name.

axiomatization

$LIST::Class$ **and**
 $HD::Field$ **and**
 $TL::Field$ **and**
 $tmp::Var$

The compilation of primitive expressions extends a code block by a sequence of JVM instructions that leave a value on the top of the operand stack.

inductive-set $compilePrim::$

$(Label \times (Label, Instr)\ AssList \times Prim \times ((Label, Instr)\ AssList \times Label))\ set$

where

$compileInt: (l, code, IntPrim\ i, (code[l \mapsto (const\ (IVal\ i))], l+1)) : compilePrim$

|

$compileUn:$

$(l, code, UnPrim\ f\ x, (code[l \mapsto (load\ x)][(l+1) \mapsto (unop\ f)], l+2)) : compilePrim$

|

$compileBin:$

$(l, code, BinPrim\ f\ x\ y,$
 $(code[l \mapsto (load\ x)][(l+1) \mapsto (load\ y)][(l+2) \mapsto (binop\ f)], l+3)) : compilePrim$

|

$compileNil:$

$(l, code, NilPrim, (code[l \mapsto (const\ (RVal\ Nullref))], l+1)) : compilePrim$

|

$compileCons:$

$(l, code, ConsPrim\ x\ y,$
 $(code[l \mapsto (load\ y)][(l+1) \mapsto (load\ x)]$
 $[(l+2) \mapsto (new\ LIST)][(l+3) \mapsto store\ tmp]$
 $[(l+4) \mapsto load\ tmp][(l+5) \mapsto (putfield\ LIST\ HD)]$
 $[(l+6) \mapsto load\ tmp][(l+7) \mapsto (putfield\ LIST\ TL)]$
 $[(l+8) \mapsto (load\ tmp)], l+9)) : compilePrim$

|

$compileCall\text{-Nil}:$

$(l, code, CallPrim\ f\ [], (code[l \mapsto invokeS\ (fst\ f)\ (snd\ f)], l+1)) : compilePrim$

|

$compileCall\text{-Cons}:$

$[(l+1, code[l \mapsto load\ x], CallPrim\ f\ args, OUT) : compilePrim]$
 $\implies (l, code, CallPrim\ f\ (x\#\ args), OUT) : compilePrim$

The following lemma shows that the resulting code is an extension of the code submitted as an argument, and that the new instructions define a contiguous block.

lemma *compilePrim-Prop1*[*rule-format*]:
 $(l, code, p, OUT) : compilePrim \implies$
 $(\forall code1 l1 . OUT = (code1, l1) \implies$
 $(l < l1 \wedge (\forall ll . ll < l \implies code1 \downarrow ll = code \downarrow ll) \wedge$
 $(\forall ll . l \leq ll \implies ll < l1 \implies (\exists ins . code1 \downarrow ll = Some\ ins))))$

A signature corresponds to a method specification table if all context entries are represented as *MST* entries and method names that are defined in the global program *P*.

definition *Sig-good*::*TP-Sig* \Rightarrow *bool* **where**
Sig-good $\Sigma =$
 $(\forall C\ m\ n . \Sigma \downarrow (C, m) = Some\ n \implies$
 $(MST \downarrow (C, m) = Some\ (mkSPEC\ (Cachera\ n)\ emp) \wedge$
 $(\exists\ par\ code\ l0 . mbody-is\ C\ m\ (par, code, l0))))$

This definition requires *MST* to associate the specification

$$mkSPEC\ (Cachera\ n)\ emp$$

to each method to which the type signature associates the type *n*. In particular, this requires the annotation table of such a method to be empty. Additionally, the global program *P* is required to contain a method definition for each method (i.e. function) name occurring in the domain of the signature.

An auxiliary abbreviation that captures when a block of code has trivial annotations and only comprises defined program labels.

definition *Segment*::
 $Class \Rightarrow Method \Rightarrow Label \Rightarrow Label \Rightarrow (Label, Instr) AssList \Rightarrow bool$
where
Segment $C\ m\ l\ l1\ code =$
 $(\exists\ Mspec\ Minv\ Anno . MST \downarrow (C, m) = Some\ (Mspecc, Minv, Anno) \wedge$
 $(\forall ll . l \leq ll \implies ll < l1 \implies$
 $Anno \downarrow (ll) = None \wedge (\exists\ ins . ins-is\ C\ m\ ll\ ins \wedge code \downarrow ll = Some\ ins)))$

The soundness of (the translation of) a function call is proven by induction on the list of arguments.

lemma *Call-SoundAux*[*rule-format*]:
 $\Sigma \downarrow f = Some\ n \implies$
 $MST \downarrow (fst\ f, snd\ f) = Some\ (mkSPEC\ (Cachera\ n)\ Anno2) \implies$
 $(\exists\ par\ body\ l0 . mbody-is\ (fst\ f)\ (snd\ f)\ (par, body, l0) \implies$
 $(\forall l\ code\ code1\ l1\ G\ C\ m\ T\ MI\ k.$
 $(l, code, CallPrim\ f\ args, code1, l1) \in compilePrim \implies$

$$\begin{aligned} MST\downarrow(C, m) = \text{Some } (T, MI, Anno) &\longrightarrow \text{Segment } C \ m \ l \ l1 \ code1 \longrightarrow \\ derivAssum \ G \ C \ m \ l1 \ (\text{Cachera } k) &\longrightarrow \\ deriv \ G \ C \ m \ l \ (\text{Cachera } (n+k)) & \end{aligned}$$

lemma *Call-Sound*:

$$\begin{aligned} \llbracket \text{Sig-good } \Sigma; \Sigma\downarrow f = \text{Some } n; \\ (l, code, \text{CallPrim } f \ \text{args}, code1, l1) \in \text{compilePrim}; \\ MST\downarrow(C, m) = \text{Some } (T, MI, Anno); \text{Segment } C \ m \ l \ l1 \ code1; \\ derivAssum \ G \ C \ m \ l1 \ (\text{Cachera } nn); k = n+nn \rrbracket \\ \implies deriv \ G \ C \ m \ l \ (\text{Cachera } k) \end{aligned}$$

The definition of basic instructions.

definition *basic::Instr \Rightarrow bool* **where**

$$\begin{aligned} basic \ ins = & ((\exists c . ins = \text{const } c) \vee ins = \text{dup} \vee \\ & ins = \text{pop} \vee ins = \text{swap} \vee (\exists x. ins = \text{load } x) \vee \\ & (\exists y. ins = \text{store } y) \vee (\exists f. ins = \text{binop } f) \vee \\ & (\exists g. ins = \text{unop } g) \vee (\exists c1 \ F1. ins = \text{getfield } c1 \ F1) \vee \\ & (\exists c2 \ F2. ins = \text{putfield } c2 \ F2) \vee \\ & (\exists c3. ins = \text{checkcast } c3)) \end{aligned}$$

Next, we prove the soundness of basic instructions. The hypothesis refers to instructions located at the program continuation.

lemma *Basic-Sound*:

$$\begin{aligned} \llbracket \text{Segment } C \ m \ l \ l1 \ code; MST\downarrow(C, m) = \text{Some } (T, MI, Anno); l \leq l1; l1 < l2; \\ l2 = l1 + 1; code\downarrow l1 = \text{Some } ins; basic \ ins; derivAssum \ G \ C \ m \ l2 \ (\text{Cachera } n) \rrbracket \\ \implies deriv \ G \ C \ m \ l1 \ (\text{Cachera } n) \end{aligned}$$

Following this, the soundness of the type system for primitive expressions. The proof proceeds by induction on the typing judgement.

lemma *TP-prim-Sound*[*rule-format*]:

$$\begin{aligned} (\Sigma, p, n): TP\text{-prim} \implies \\ \text{Sig-good } \Sigma \longrightarrow \\ (\forall l \ code \ code1 \ l1 \ G \ C \ m \ T \ MI \ Anno \ nn \ k. \\ (l, code, p, (code1, l1)) : \text{compilePrim} \longrightarrow \\ MST\downarrow(C, m) = \text{Some } (T, MI, Anno) \longrightarrow \\ \text{Segment } C \ m \ l \ l1 \ code1 \longrightarrow derivAssum \ G \ C \ m \ l1 \ (\text{Cachera } nn) \longrightarrow \\ k = n+nn \longrightarrow deriv \ G \ C \ m \ l \ (\text{Cachera } k)) \end{aligned}$$

The translation of general expressions is defined similarly, but no code continuation is required.

inductive-set *compileExpr*:

$$(\text{Label} \times (\text{Label}, \text{Instr}) \ \text{AssList} \times \text{Expr} \times ((\text{Label}, \text{Instr}) \ \text{AssList} \times \text{Label})) \ \text{set}$$

where

compilePrimE:

$$\begin{aligned} \llbracket (l, code, p, (code1, l1)) : \text{compilePrim}; OUT = (code1[l1 \mapsto vreturn], l1 + 1) \rrbracket \\ \implies (l, code, \text{PrimE } p, OUT) : \text{compileExpr} \end{aligned}$$

|

compileLetE:

$$\llbracket (l, code, p, (code1, l1)) : \text{compilePrim}; (code2, l2) = (code1[l1 \mapsto (\text{store } x)], l1 + 1); \rrbracket$$

$$\begin{array}{l}
(l2, code2, e, OUT) : compileExpr \\
\Longrightarrow (l, code, LetE\ x\ p\ e, OUT) : compileExpr \\
| \\
compileCondE: \\
[[(l+2, code, e2, (codeElse, XXX)) : compileExpr; \\
(XXX, codeElse, e1, (codeThen, YYY)) : compileExpr ; \\
OUT = (codeThen[l \mapsto load\ x][(l+1) \mapsto (iftrue\ XXX)], YYY)] \\
\Longrightarrow (l, code, CondE\ x\ e1\ e2, OUT) : compileExpr \\
| \\
compileMatchE: \\
[[(l+9, code, e2, (codeCons, lNil)) : compileExpr; \\
(lNil, codeCons, e1, (codeNil, lRes)) : compileExpr ; \\
OUT = (codeNil[l \mapsto (load\ x)] \\
[(l+1) \mapsto (unop\ (\lambda\ v.\ if\ v = RVal\ Nullref \\
then\ TRUE\ else\ FALSE))] \\
[(l+2) \mapsto (iftrue\ lNil)] \\
[(l+3) \mapsto (load\ x)] \\
[(l+4) \mapsto (getfield\ LIST\ HD)] \\
[(l+5) \mapsto (store\ h)] \\
[(l+6) \mapsto (load\ x)] \\
[(l+7) \mapsto (getfield\ LIST\ TL)] \\
[(l+8) \mapsto (store\ t)], lRes] \\
\Longrightarrow (l, code, MatchE\ x\ e1\ h\ t\ e2, OUT) : compileExpr
\end{array}$$

Again, we prove an auxiliary result on the emitted code, by induction on the compilation judgement.

lemma *compileExpr-Prop1*[rule-format]:
 $(l, code, e, OUT) : compileExpr \Longrightarrow$
 $(\forall\ code1\ l1.\ OUT = (code1, l1) \longrightarrow$
 $(l < l1 \wedge$
 $(\forall\ ll.\ ll < l \longrightarrow code1 \downarrow ll = code \downarrow ll) \wedge$
 $(\forall\ ll.\ l \leq ll \longrightarrow ll < l1 \longrightarrow (\exists\ ins.\ code1 \downarrow ll = Some\ ins))))$

Then, soundness of the expression type system is proven by induction on the typing judgement.

lemma *TP-expr-Sound*[rule-format]:
 $(\Sigma, e, n) : TP\text{-expr} \Longrightarrow Sig\text{-good}\ \Sigma \longrightarrow$
 $(\forall\ l\ code\ code1\ l1\ G\ C\ m\ T\ MI\ Anno.$
 $(l, code, e, (code1, l1)) : compileExpr \longrightarrow$
 $MST \downarrow (C, m) = Some\ (T, MI, Anno) \longrightarrow$
 $Segment\ C\ m\ l\ l1\ code1 \longrightarrow deriv\ G\ C\ m\ l\ (Cachera\ n))$

The full translation of a functional program into a bytecode program is defined as follows.

definition *compileProg*::FunProg \Rightarrow bool **where**
 $compileProg\ F =$
 $((\forall\ C\ m\ par\ e.\ F \downarrow (C, m) = Some(par, e) \longrightarrow$
 $(\exists\ code\ l0\ l.\ mbody\text{-is}\ C\ m\ (rev\ par, code, l0) \wedge$

$$\begin{aligned}
& (l0, [], e, (code, l)): compileExpr)) \wedge \\
& (\forall C m. (\exists M. mbody-is C m M) = (\exists fdecl . F\downarrow(C, m) = Some fdecl))
\end{aligned}$$

The final condition relating a typing context to a method specification table.

definition *TP-MST*::*TP-Sig* \Rightarrow *bool* **where**

TP-MST $\Sigma =$

$$\begin{aligned}
& (\forall C m . \text{case } (MST\downarrow(C, m)) \text{ of} \\
& \quad \text{None} \Rightarrow \Sigma\downarrow(C, m) = \text{None} \\
& \quad | \text{Some}(T, MI, Anno) \Rightarrow Anno = emp \wedge \\
& \quad \quad (\exists n . \Sigma\downarrow(C, m) = \text{Some } n \wedge \\
& \quad \quad (T, MI, Anno) = mkSPEC (Cachera n) emp))
\end{aligned}$$

For well-typed programs, this property implies the earlier condition on signatures.

lemma *translation-good*: $\llbracket compileProg F; TP-MST \Sigma; TP \Sigma F \rrbracket \Longrightarrow Sig-good \Sigma$

We can thus prove that well-typed function bodies satisfy the specifications asserted by the typing context.

lemma *CACH-BodiesDerivable*[*rule-format*]:

$$\begin{aligned}
& \llbracket mbody-is C m (par, code, l); compileProg F; TP-MST \Sigma; TP \Sigma F \rrbracket \\
& \Longrightarrow \exists n . MST\downarrow(C, m) = \text{Some}(mkSPEC(Cachera n) emp) \wedge \\
& \quad deriv \llbracket C m l (Cachera n)
\end{aligned}$$

From this, the overall soundness result follows easily.

theorem *CACH-VERIFIED*: $\llbracket TP \Sigma F; TP-MST \Sigma; compileProg F \rrbracket \Longrightarrow VP$

References

- [1] F. Y. Bannwart and P. Müller. A logic for bytecode. *Electronic Notes in Theoretical Computer Science*, 141(1):255–273, 2005.
- [2] L. Beringer and M. Hofmann. A bytecode logic for JML and types. In N. Kobayashi, editor, *Proceedings of the 4th Asian Symposium on Programming Languages and Systems (APLAS 2006)*, volume 4279 of *LNCS*, pages 389–405. Springer, 2006.
- [3] L. Beringer, M. Hofmann, A. Momigliano, and O. Shkaravska. Automatic certification of heap consumption. In A. V. Franz Baader, editor, *LPAR 2004*, volume 3425 of *LNCS*, pages 347–362. Springer-Verlag, 2005.
- [4] D. Cachera, T. Jensen, D. Pichardie, and G. Schneider. Certified memory usage analysis. In *Proceedings of the 13th International Symposium on Formal Methods (FM'05)*, volume 3582 of *LNCS*, pages 91–106. Springer-Verlag, 2005.

- [5] A. Deutsch. A storeless model of aliasing and its abstractions using finite representations of right-regular equivalence relations. In *Proceedings of the International Conference on Computer Languages*, pages 2–13. IEEE, Apr. 1992.
- [6] R. Hähnle and W. Mostowski. Verification of safety properties in the presence of transactions. In *Post Conference Proceedings of CASSIS: Construction and Analysis of Safe, Secure and Interoperable Smart devices*, volume 3362 of *LNCS*, pages 151–171. Springer, 2005.
- [7] M. Hofmann and S. Jost. Static prediction of heap space usage for first-order functional programs. In *Proceedings of the 30th ACM Symposium on Principles of Programming Languages (POPL'03)*, pages 185–197. ACM Press, Jan. 2003.
- [8] H. M. B. Jonkers. Abstract storage structures. *Algorithmic languages*, 1981.
- [9] MOBIUS Consortium. Deliverable 3.1: Bytecode specification language and program logic, 2006. Available online from <http://mobius.inria.fr>.
- [10] D. Pichardie. Bicolano – Byte Code Language in Coq. <http://mobius.inria.fr/twiki/bin/view/Bicolano>. Summary appears in [9], 2006.
- [11] U. S. Reddy. Global state considered unnecessary: An introduction to object-based semantics. *Journal of Lisp and Symbolic Computation*, 9:7–76, 1996.